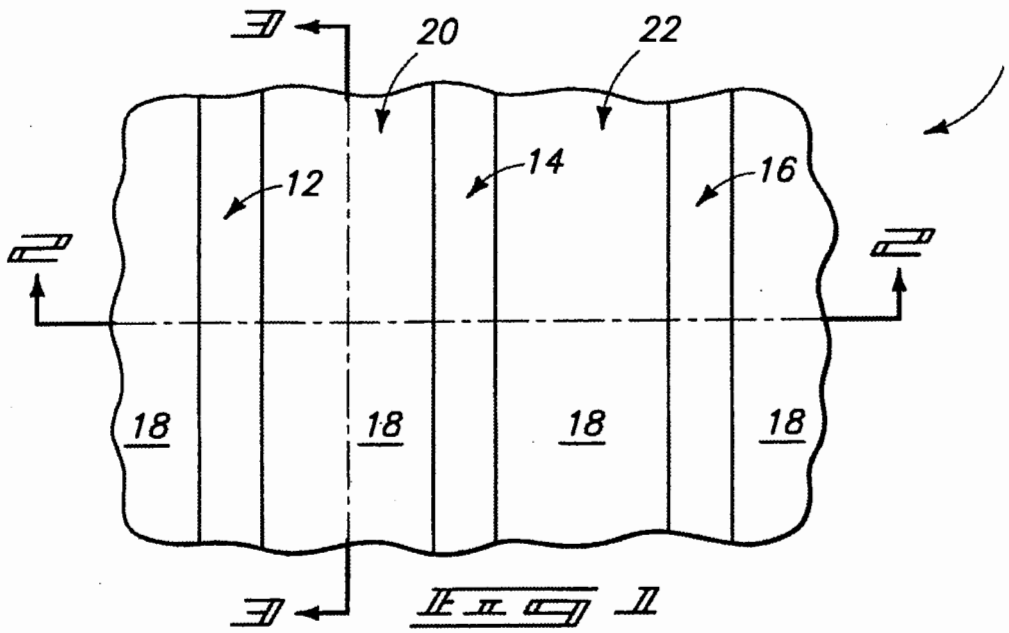
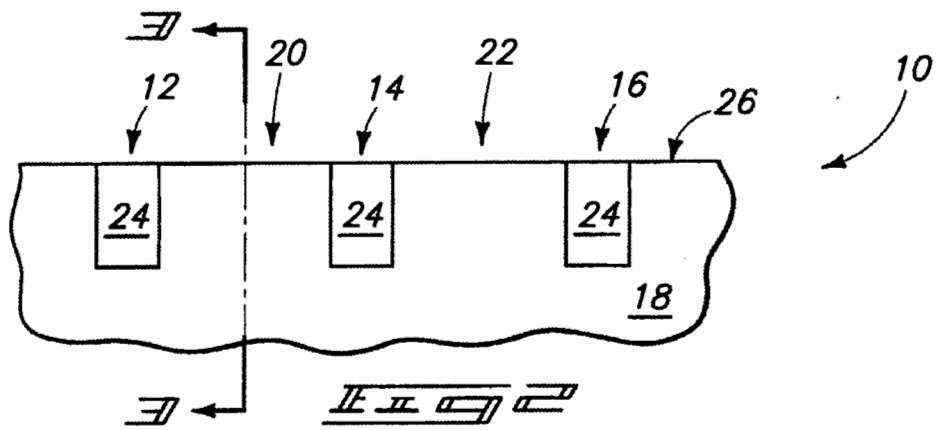
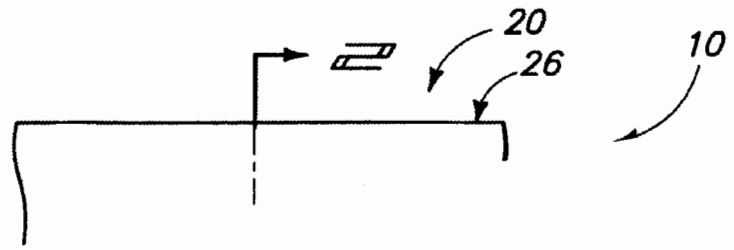

3
20
22
12
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16
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18
18
3
Fig 1


3
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Fig 2


20
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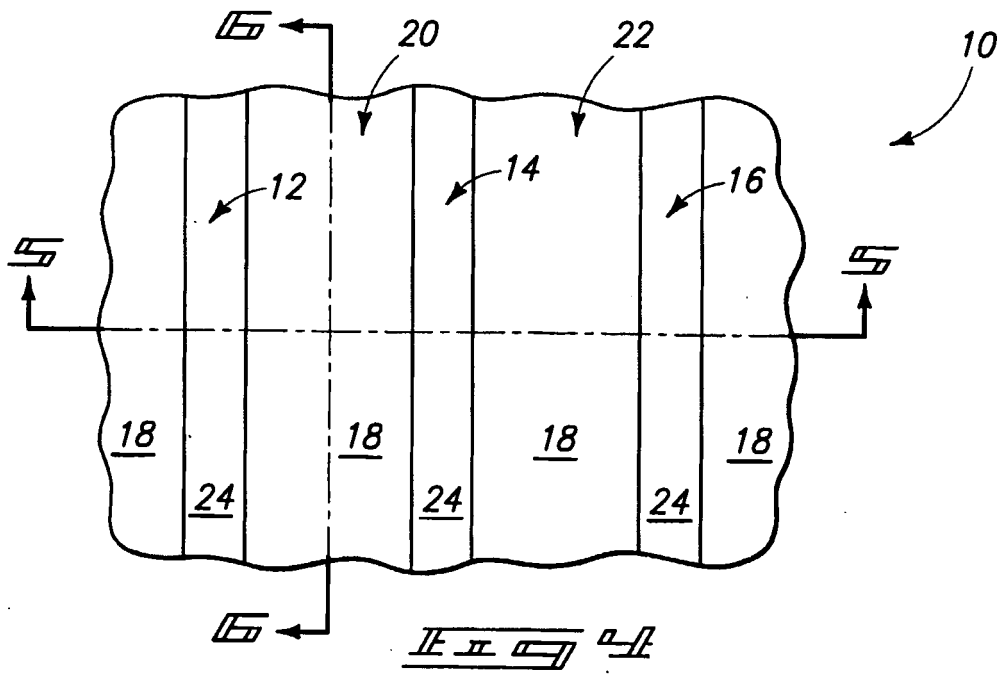



Fig 4

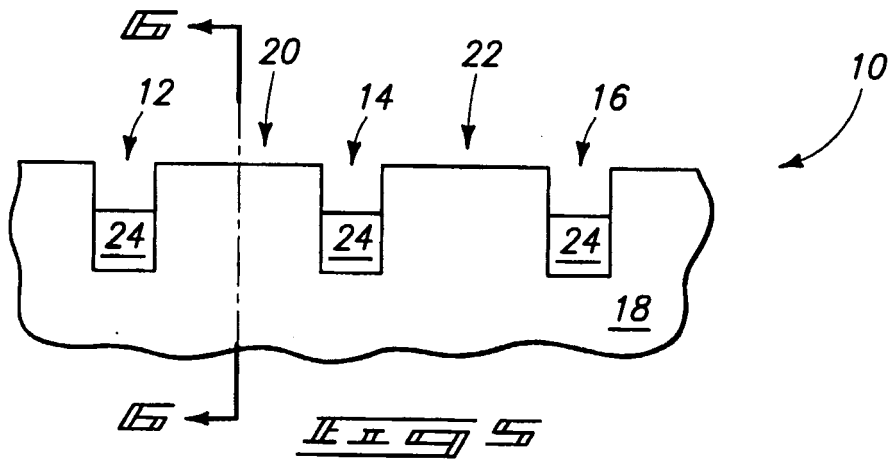



Fig 5

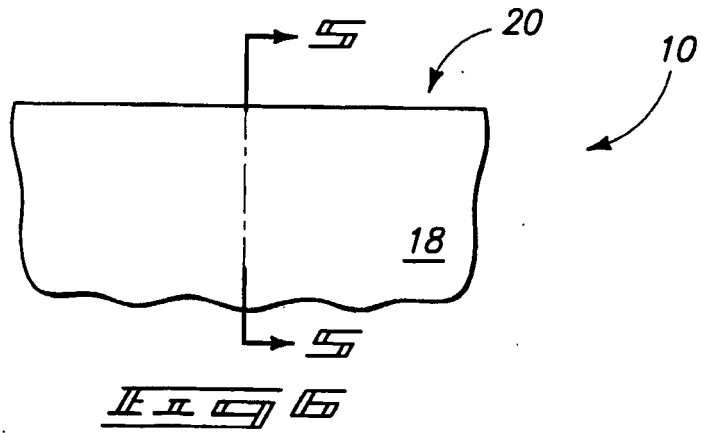
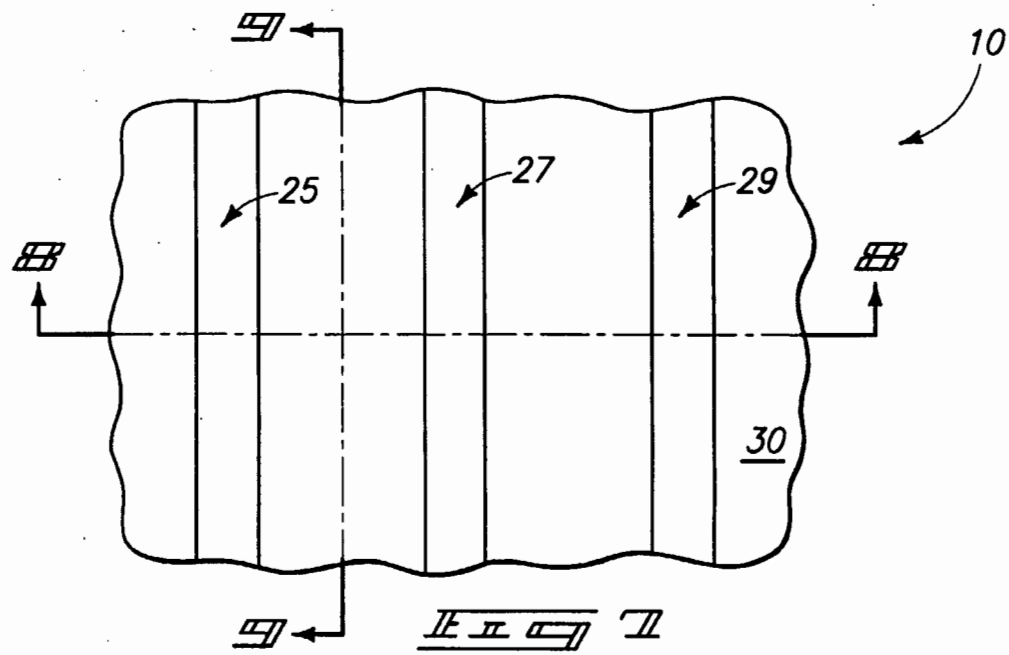
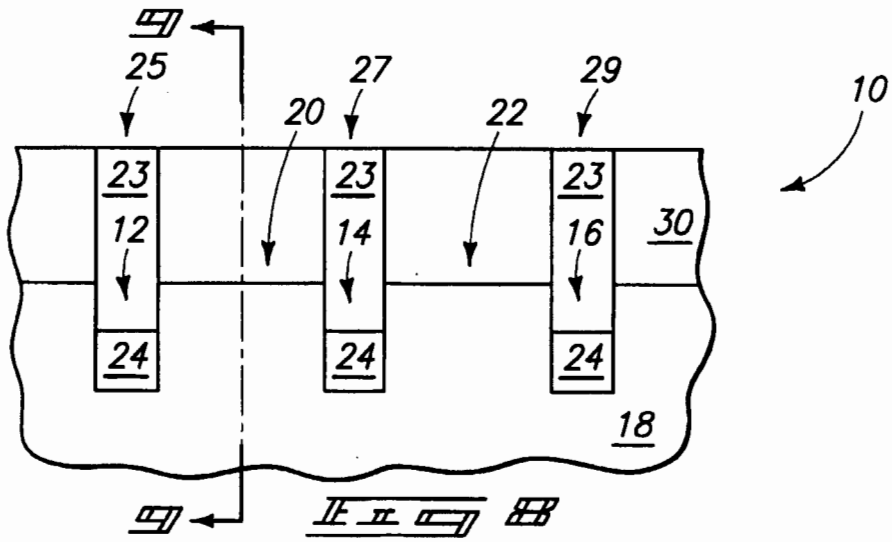


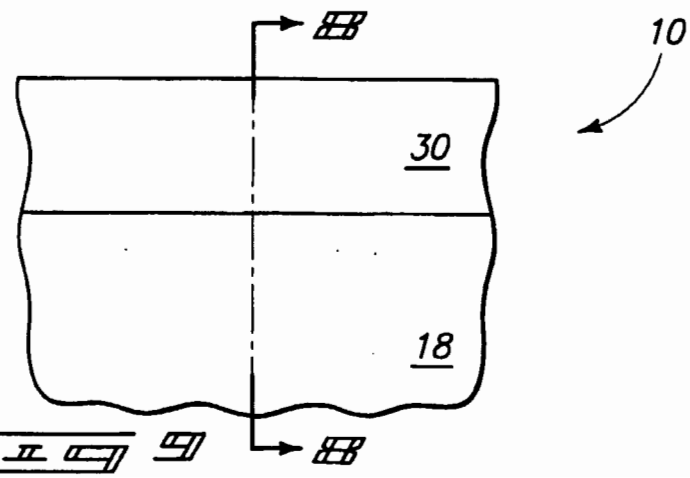

Fig 6




Фиг 11




Фиг 8




Фиг 9

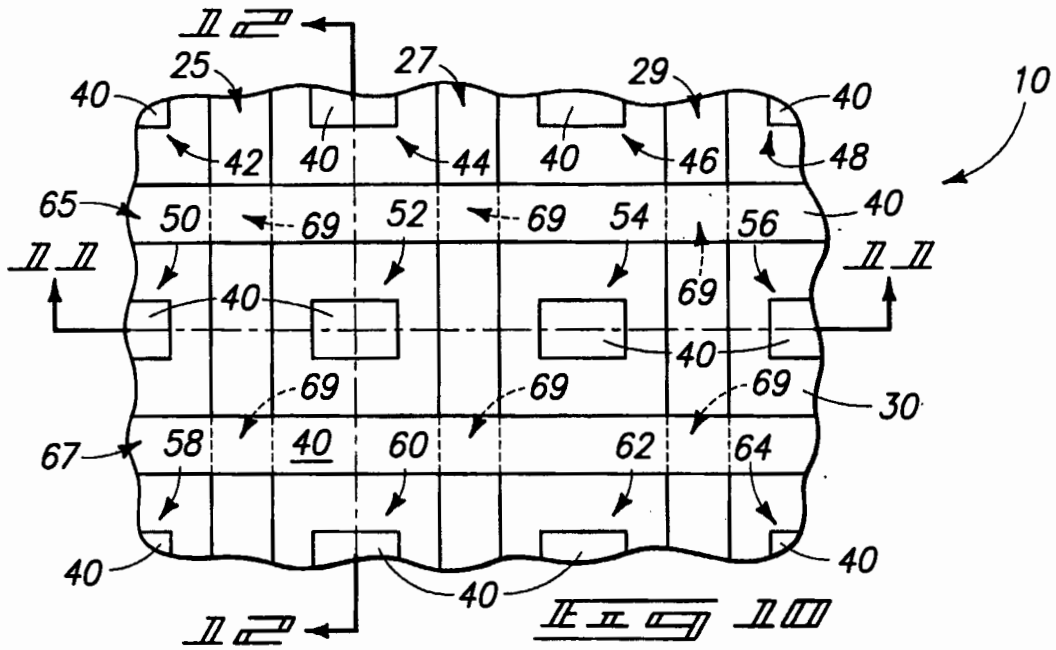



Fig II

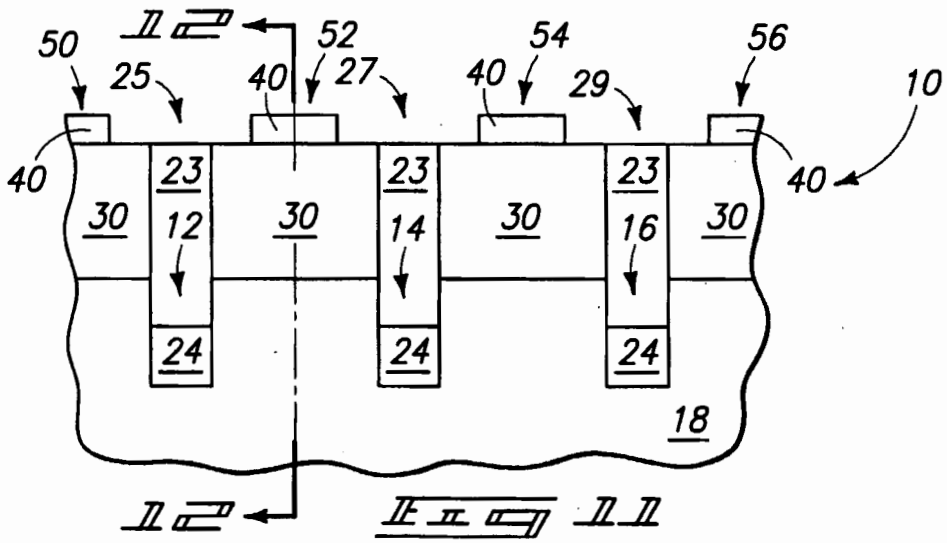



Fig II

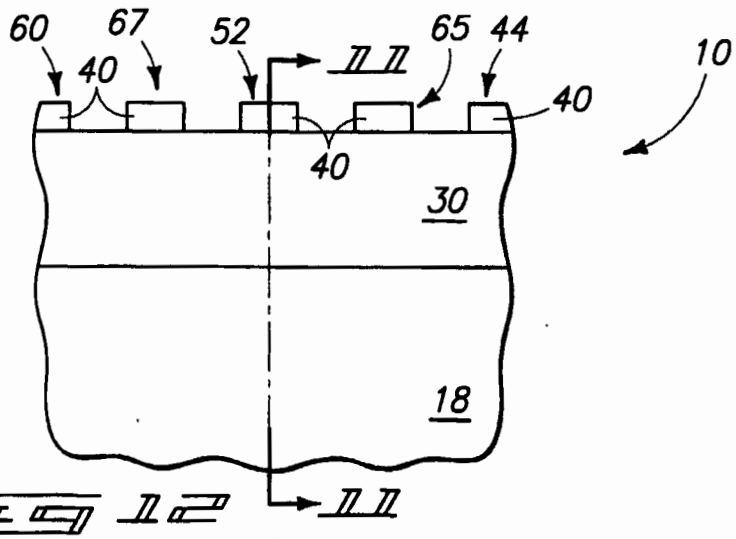



Fig II

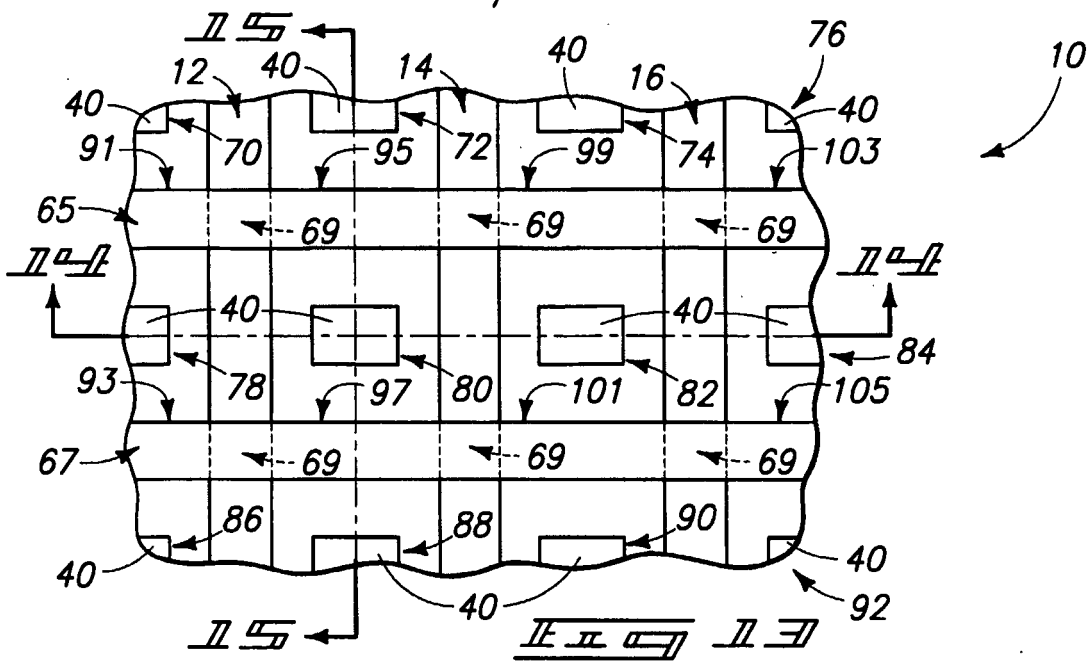



Fig 13

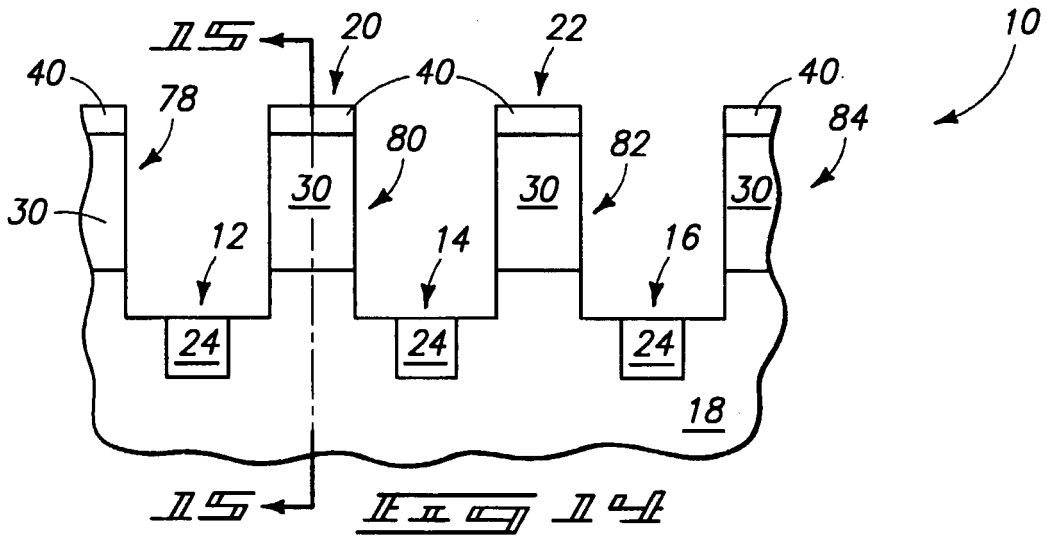



Fig 14

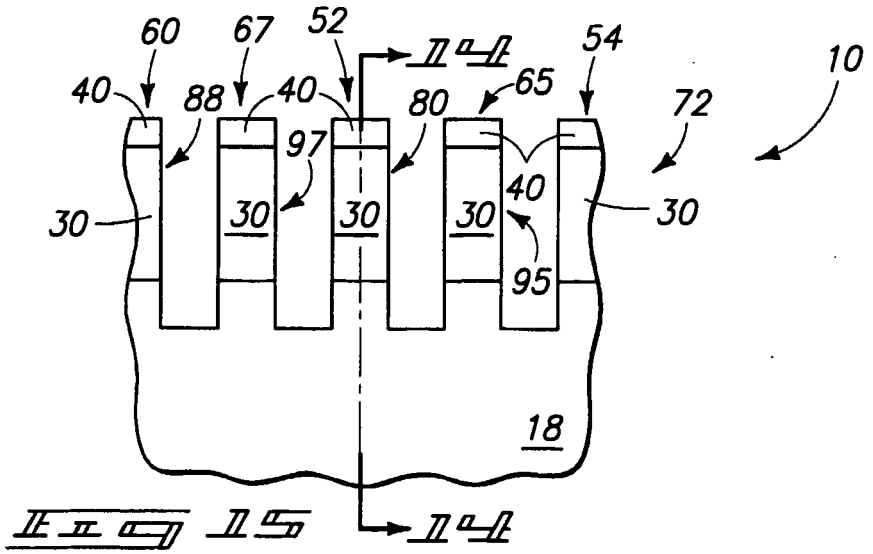



Fig 15

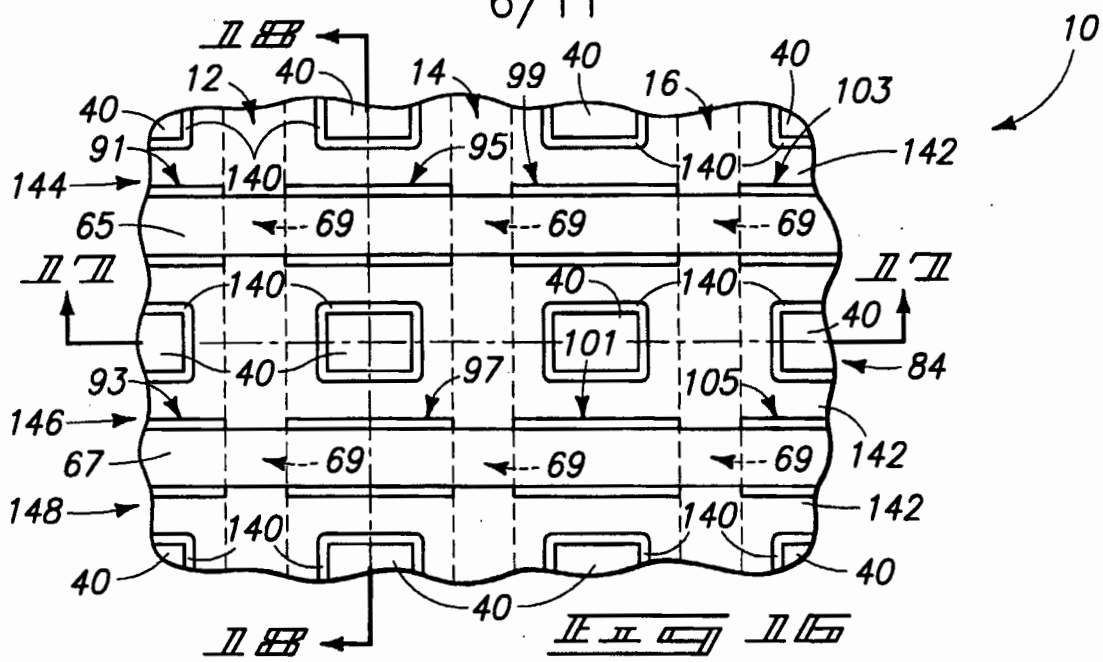



Fig 16

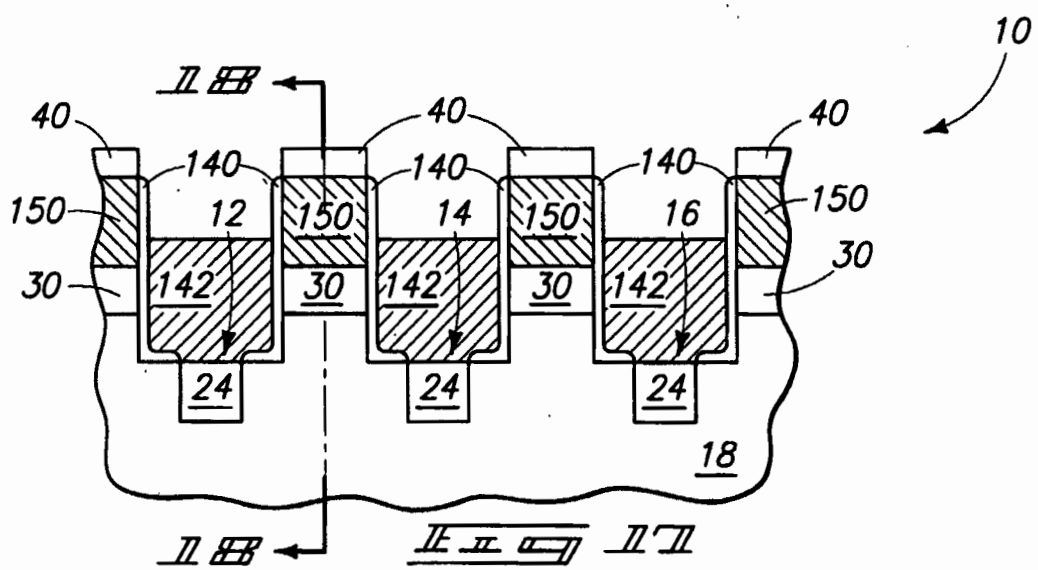



Fig 11

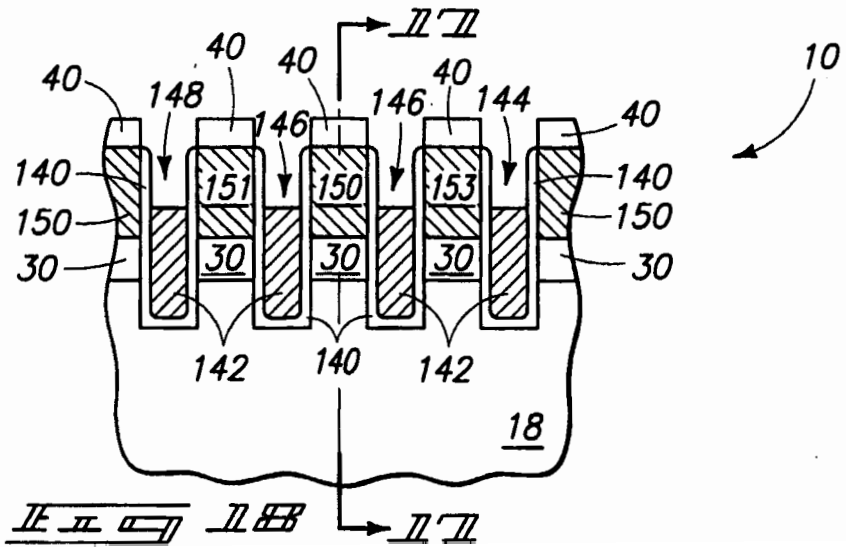



Fig 12

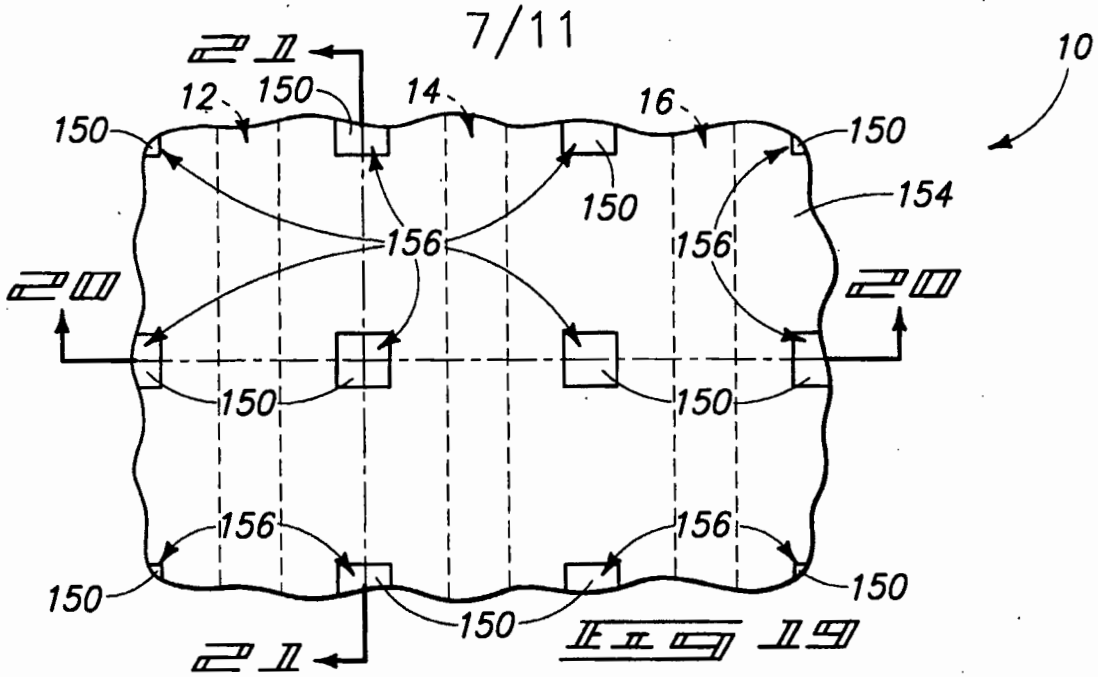



Fig 19

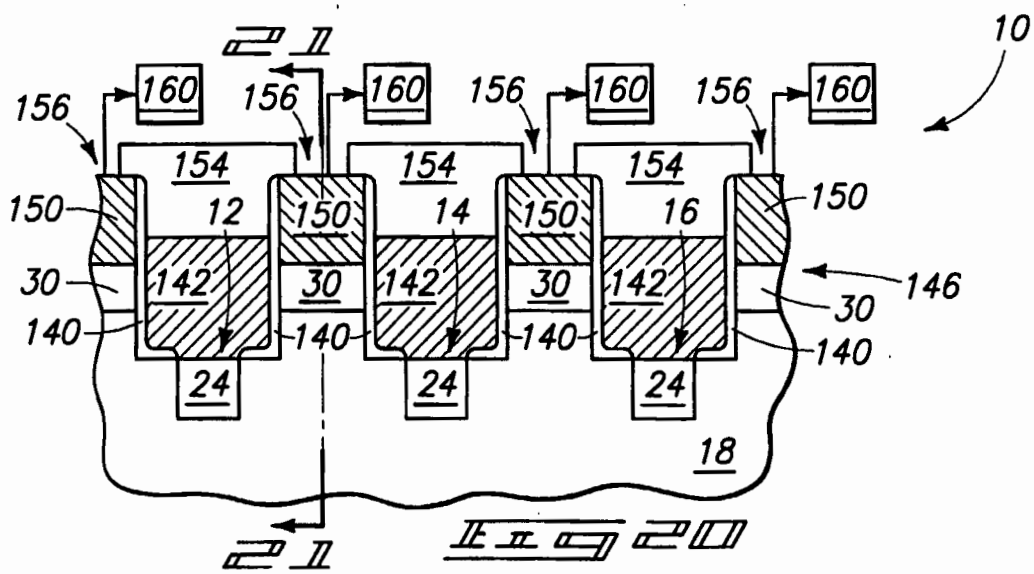



Fig 20

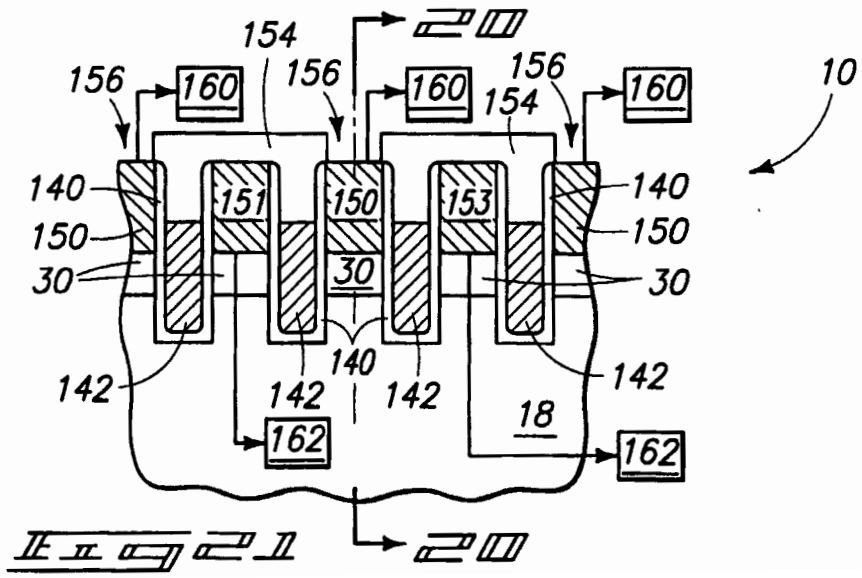



Fig 21

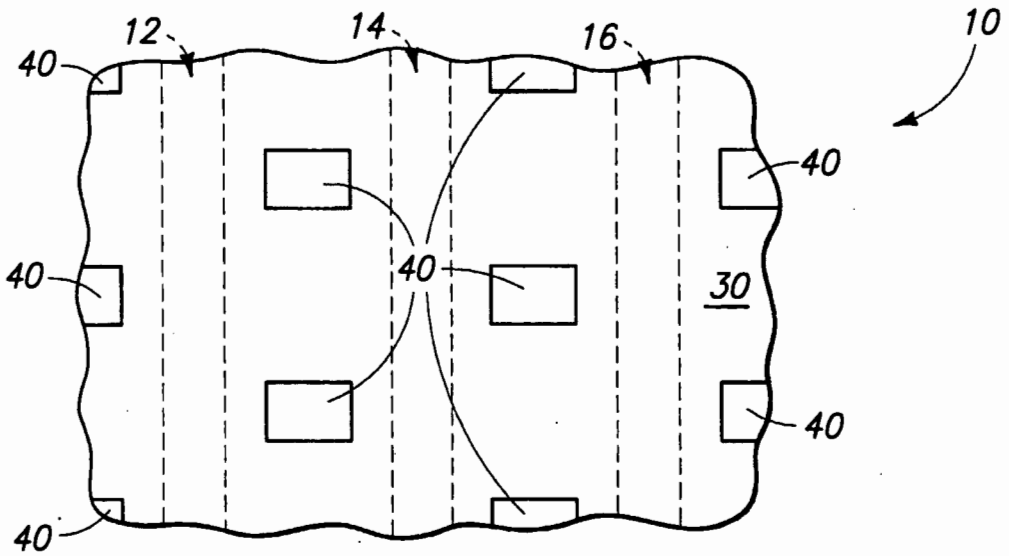



Fig 22

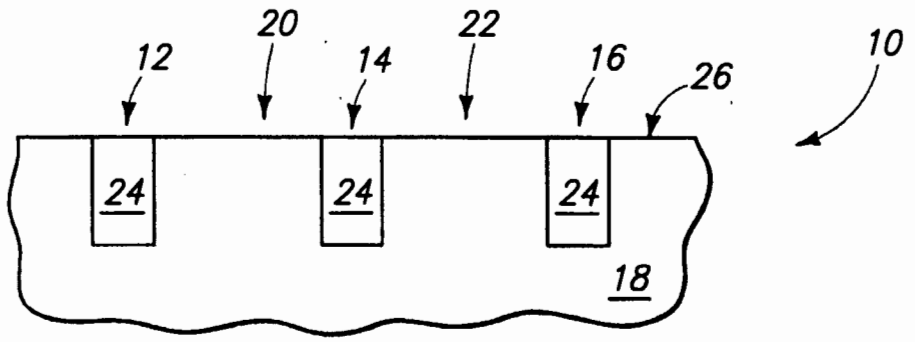



Fig 23

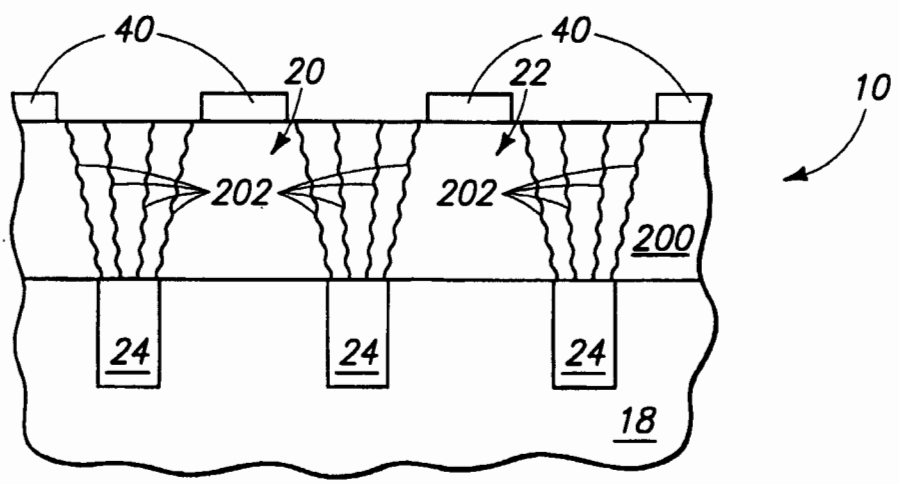



Fig 24

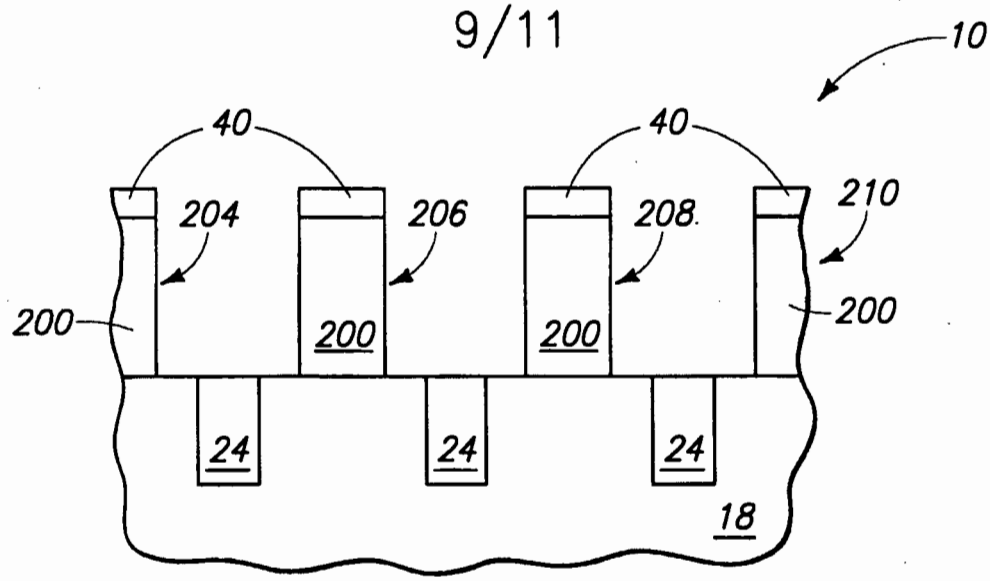



Fig 25

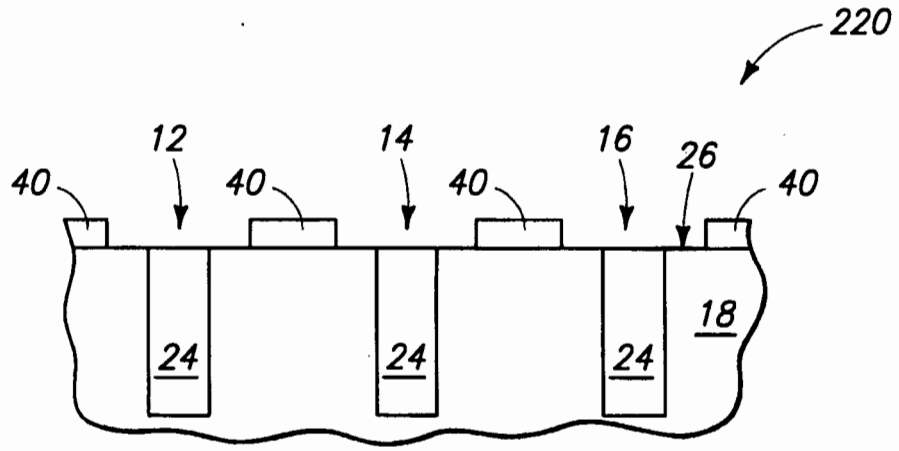



Fig 26

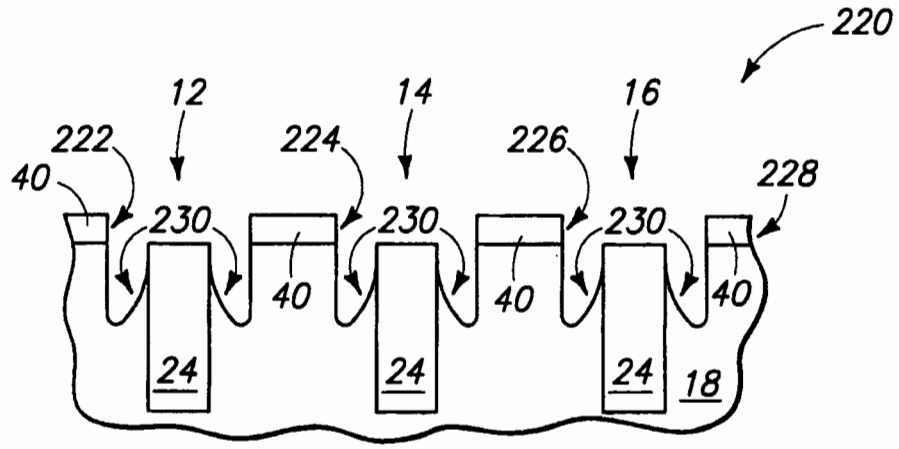



Fig 27

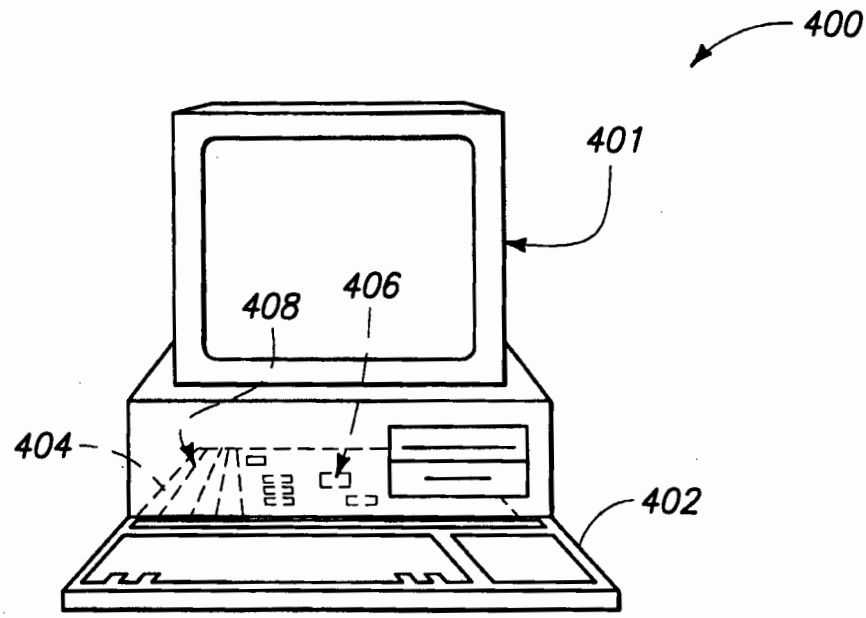



Fig 28

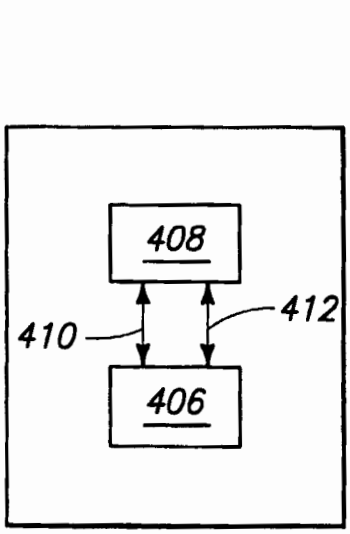



Fig 29

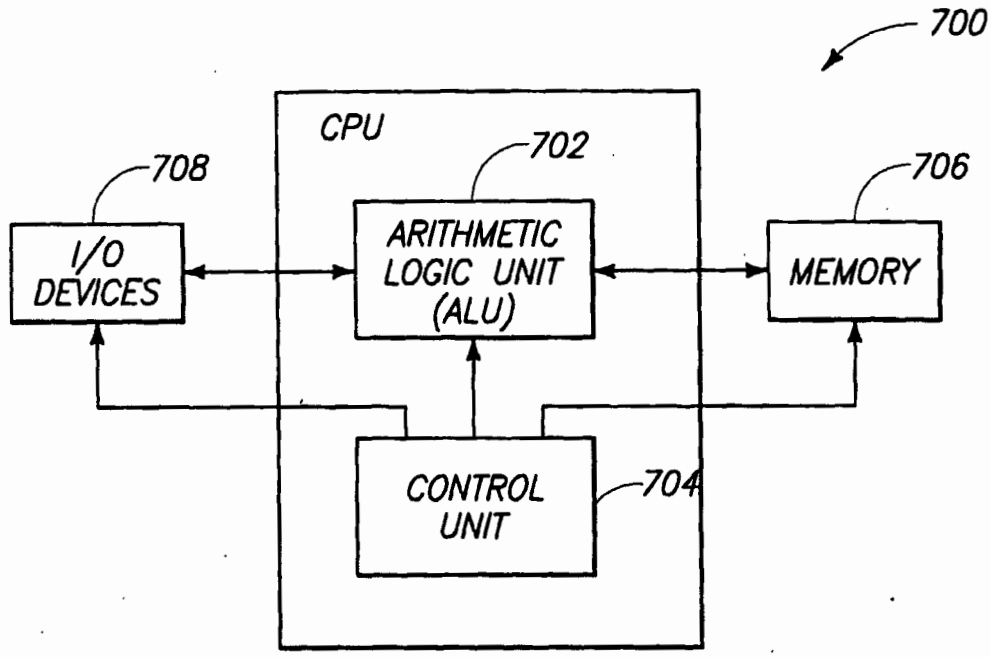

700
CPU
702
708
706
I/O DEVICES
ARITHMETIC LOGIC UNIT (ALU)
MEMORY
CONTROL UNIT
704

Fig 30

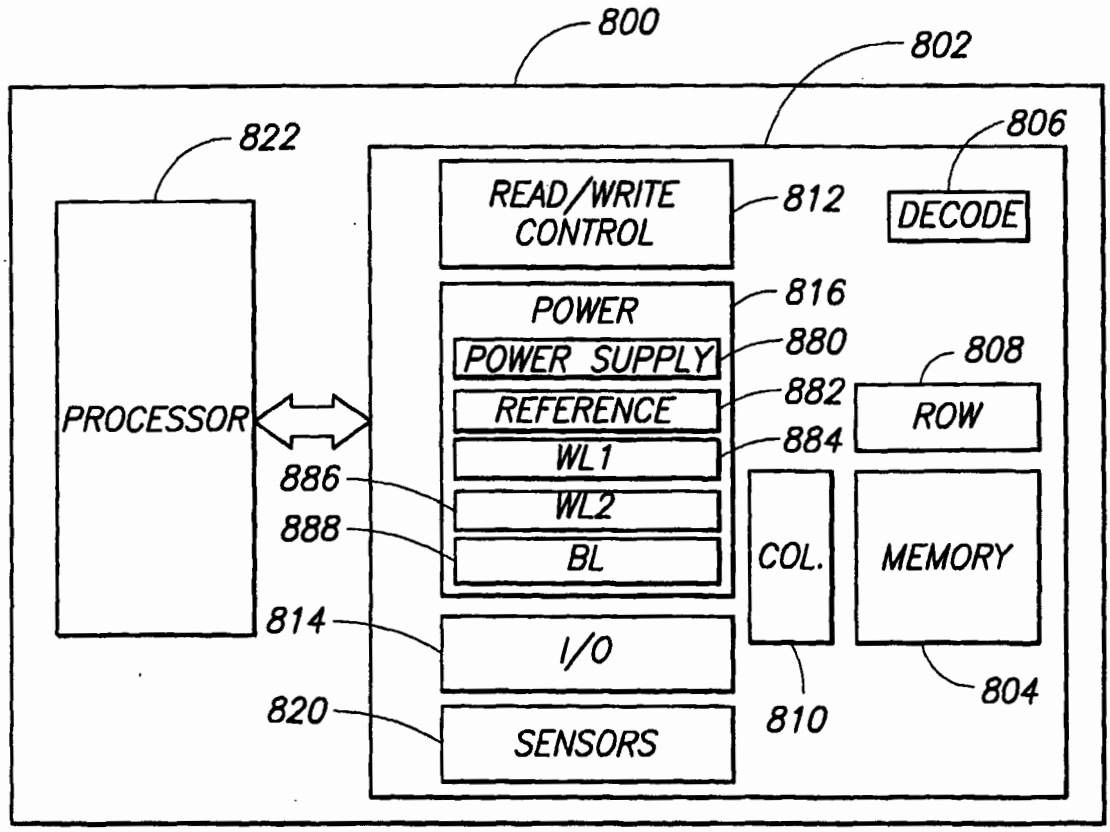

800
802
822
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READ/WRITE CONTROL
812
DECODE
816
POWER
POWER SUPPLY
880
808
PROCESSOR
882
REFERENCE
ROW
884
WL1
886
WL2
888
BL
COL.
MEMORY
814
I/O
804
820
SENSORS
810

Fig 31