

# **BULK ACOUSTIC RESONATOR, ACOUSTIC FILTER, AND ELECTRONIC DEVICE**

## **TECHNICAL FIELD**

**[0001]** This application relates to the field of semiconductor technologies, and in particular, to  
5 a bulk acoustic resonator, an acoustic filter, and an electronic device.

## **BACKGROUND**

**[0002]** As mobile communication develops and people's requirements for a communication speed are increasing, more frequency bands are used in a mobile communication system. An acoustic filter is an important component in the mobile communication system.

10 **[0003]** A conventional acoustic filter is a bottleneck for improving communication quality. With implementation of more communication modes and communication scenarios, acoustic filtering performance faces a greater challenge. Improving performance of an acoustic filter is currently a research focus and difficulty of the acoustic filter.

## **SUMMARY**

15 **[0004]** Embodiments of this application provide a bulk acoustic resonator, an acoustic filter, and an electronic device, to improve performance of the acoustic filter.

**[0005]** To achieve the foregoing objective, this application uses the following technical solutions.

**[0006]** According to a first aspect, a bulk acoustic resonator is provided. The bulk acoustic  
20 resonator includes a piezoelectric material layer, an interdigital transducer, and a dielectric layer. The interdigital transducer is disposed on the piezoelectric material layer. The interdigital transducer includes a first busbar and a second busbar that are disposed opposite to each other, a plurality of first electrodes, and a plurality of second electrodes. The plurality of first electrodes sequentially protrude from the first busbar to the second busbar along an extension direction of the  
25 first busbar. The plurality of second electrodes sequentially protrude from the second busbar to the first busbar along an extension direction of the second busbar. The plurality of first electrodes and the plurality of second electrodes are sequentially arranged in a staggered manner between the first busbar and the second busbar. The dielectric layer covers the piezoelectric material layer and the

interdigital transducer, and thicknesses of a part, of the dielectric layer, located between at least one pair of adjacent first electrode and second electrode fluctuate. The part, of the dielectric layer, whose thicknesses fluctuate is used to suppress excitation of a high-order mode during sound wave propagation.

5 **[0007]** In this application, because the thicknesses of the part, of the dielectric layer in the bulk acoustic resonator, located between the at least one pair of adjacent first electrode and second electrode fluctuate, when a sound wave is propagated between the first electrode and the second electrode, the part, of the dielectric layer, whose thicknesses fluctuate may destroy a periodicity of sound wave propagation, change a sound wave transmission characteristic, and therefore can  
10 suppress excitation of the high-order mode during sound wave propagation. This can excite a main mode of the bulk acoustic resonator, suppress a high-order spurious mode, reduce or avoid spurious resonance, and help improve performance of the acoustic filter and facilitate design of the acoustic filter.

**[0008]** In a possible implementation, along a direction in which the plurality of first electrodes  
15 and the plurality of second electrodes are sequentially arranged in the staggered manner, the thicknesses of the part, of the dielectric layer, located between the at least one pair of adjacent first electrode and second electrode are first decreased and then increased. Because the thicknesses of the part, of the dielectric layer, located between the at least one pair of adjacent first electrode and second electrode are first decreased and then increased, the part, of the dielectric layer, whose  
20 thicknesses are first decreased and then increased may destroy the periodicity of sound wave propagation, and therefore suppress excitation of the high-order mode during sound wave propagation. This reduces or avoids spurious resonance, and helps improve performance of the acoustic filter and facilitates design of the acoustic filter. On this basis, because the thicknesses of the part, of the dielectric layer, whose thicknesses fluctuate are distributed according to a specific  
25 rule, an electromechanical coupling coefficient of the main mode of the bulk acoustic resonator can be increased, and design difficulty of the dielectric layer can be simplified. In addition, a change rule of thicknesses of the dielectric layer can be selected according to a requirement, so that a design freedom of the dielectric layer can be improved.

**[0009]** In a possible implementation, along a direction in which the plurality of first electrodes  
30 and the plurality of second electrodes are sequentially arranged in the staggered manner, the thicknesses of the part, of the dielectric layer, located between the at least one pair of adjacent first electrode and second electrode are first increased and then decreased. Because the thicknesses of the part, of the dielectric layer, located between the at least one pair of adjacent first electrode and second electrode are first increased and then decreased, the part, of the dielectric layer, whose  
35 thicknesses are first increased and then decreased may destroy a periodicity of sound wave

propagation, and therefore suppress excitation of the high-order mode during sound wave propagation. This reduces or avoids spurious resonance, and helps improve performance of the acoustic filter and facilitates design of the acoustic filter. On this basis, because the thicknesses of the part, of the dielectric layer, whose thicknesses fluctuate are distributed according to a specific rule, an electromechanical coupling coefficient of the main mode of the bulk acoustic resonator can be increased, and design difficulty of the dielectric layer can be simplified. In addition, a change rule of thicknesses of the dielectric layer can be selected according to a requirement, so that a design freedom of the dielectric layer can be improved.

**[0010]** In a possible implementation, the part, of the dielectric layer, located between the at least one pair of adjacent first electrode and second electrode includes a plurality of areas that are sequentially arranged along a direction in which the plurality of first electrodes and the plurality of second electrodes are sequentially arranged in the staggered manner, and along the direction in which the plurality of first electrodes and the plurality of second electrodes are sequentially arranged in the staggered manner, thicknesses of each area are first decreased and then increased, or first increased and then decreased. Because the thicknesses of each area in the part, of the dielectric layer, located between the at least one pair of adjacent first electrode and second electrode are first decreased and then increased, or first increased and then decreased, the dielectric layer of the plurality of areas may destroy a periodicity of sound wave propagation, and therefore suppress excitation of the high-order mode during sound wave propagation. This reduces or avoids spurious resonance, and helps improve performance of the acoustic filter and facilitates design of the acoustic filter. On this basis, because the thicknesses of each area in the part, of the dielectric layer, located between the at least one pair of adjacent first electrode and second electrode are distributed according to a specific rule, an electromechanical coupling coefficient of the main mode of the bulk acoustic resonator can be increased, and design difficulty of the dielectric layer can be simplified. In addition, a change rule of thicknesses of the dielectric layer can be selected according to a requirement, so that a design freedom of the dielectric layer can be improved.

**[0011]** In a possible implementation, a change rule of thicknesses of a part, of the dielectric layer, located between a first pair of adjacent first electrode and second electrode is different from a change rule of thicknesses of a part, of the dielectric layer, located between a second pair of adjacent first electrode and second electrode. This can further destroy the periodicity during sound wave propagation, suppress excitation of the high-order mode during sound wave propagation, and be more conducive to reducing or avoiding spurious resonance. In addition, a design freedom of the bulk acoustic resonator can be further enriched.

**[0012]** In a possible implementation, the first pair of adjacent first electrode and second electrode and the second pair of adjacent first electrode and second electrode share one of the first

electrode or the second electrode, that is, the first pair of adjacent first electrode and second electrode and the second pair of adjacent first electrode and second electrode are disposed adjacently.

**[0013]** In a possible implementation, the bulk acoustic resonator further includes a substrate, the substrate is disposed on a side that is of the piezoelectric material layer and that is away from the interdigital transducer, and the substrate is enclosed in a ring. The substrate may play a supporting role, and is configured to support the piezoelectric material layer, the interdigital transducer, and the dielectric layer. On this basis, because the substrate is enclosed in the ring, a cavity structure can be formed. In a sound wave propagation process, the cavity structure can concentrate energy on the piezoelectric material layer. This can improve a quality factor of the bulk acoustic resonator. In addition, the substrate disposed in the bulk acoustic resonator can also implement process compatibility. For example, the acoustic filter may be bound to another electronic component like an integrated circuit component via the substrate.

**[0014]** In a possible implementation, the bulk acoustic resonator further includes a substrate and a reflection layer that are disposed in a stacked manner, and the reflection layer is disposed between the substrate and the piezoelectric material layer; and the reflection layer includes a first acoustic impedance layer and a second acoustic impedance layer that are alternately disposed in a stacked manner, and acoustic impedance of the first acoustic impedance layer is different from acoustic impedance of the second acoustic impedance layer. Because the reflection layer may reflect the sound wave to the piezoelectric material layer, energy can be concentrated on the piezoelectric material layer in the sound wave propagation process. This can improve the quality factor of the bulk acoustic resonator. On this basis, the substrate in the bulk acoustic resonator plays a supporting role, and is configured to support the reflection layer, the piezoelectric material layer, the interdigital transducer, and the dielectric layer. In addition, the substrate in the bulk acoustic resonator can also implement process compatibility. For example, the acoustic filter may be bound to another electronic component like an IC component via the substrate.

**[0015]** In a possible implementation, a material of the substrate includes one or more of silicon, silicon carbide, diamond, sapphire, or aluminum nitride.

**[0016]** In a possible implementation, a material of the dielectric layer includes one or more of silicon oxide, silicon nitride, silicon oxynitride, or aluminum oxide.

**[0017]** According to a second aspect, an acoustic filter is provided. The acoustic filter includes a plurality of cascaded bulk acoustic resonators, and the bulk acoustic resonator is the bulk acoustic resonator provided in the first aspect. Because the acoustic filter has same technical effect as the bulk acoustic resonator provided in the first aspect, for details, refer to related descriptions of the first aspect. Details are not described herein again.

**[0018]** According to a third aspect, an electronic device is provided. The electronic device includes an acoustic filter, a processor, and a printed circuit board, both the acoustic filter and the processor are disposed on the printed circuit board; and the acoustic filter is the acoustic filter provided in the second aspect. Because the electronic device has same technical effect as the bulk acoustic resonator provided in the first aspect, for details, refer to related descriptions of the first aspect. Details are not described herein again.

## **BRIEF DESCRIPTION OF DRAWINGS**

**[0019]** FIG. 1 is a diagram of a structure of an electronic device according to an embodiment of this application;

**[0020]** FIG. 2 is a diagram of a structure of an acoustic filter according to an embodiment of this application;

**[0021]** FIG. 3a is a diagram of a structure of a bulk acoustic resonator according to an embodiment of this application;

**[0022]** FIG. 3b is a cross-sectional diagram along an AA direction in FIG. 3a;

**[0023]** FIG. 4 is a diagram of a structure of an interdigital transducer according to an embodiment of this application;

**[0024]** FIG. 5a is a diagram of a structure of a bulk acoustic resonator according to another embodiment of this application;

**[0025]** FIG. 5b is a diagram of a structure of a bulk acoustic resonator according to still another embodiment of this application;

**[0026]** FIG. 6 is a diagram of a structure of a bulk acoustic resonator according to the related technology;

**[0027]** FIG. 7 is a diagram of an admittance curve corresponding to the bulk acoustic resonator shown in FIG. 6;

**[0028]** FIG. 8 is a diagram of an admittance curve corresponding to the bulk acoustic resonator shown in FIG. 3b;

**[0029]** FIG. 9 is a diagram of a structure of a bulk acoustic resonator according to still another embodiment of this application;

**[0030]** FIG. 10a is a diagram of a structure of a bulk acoustic resonator according to still another embodiment of this application;

**[0031]** FIG. 10b is a diagram of a structure of a bulk acoustic resonator according to still another embodiment of this application;

**[0032]** FIG. 11a is a diagram of a structure of a bulk acoustic resonator according to still

another embodiment of this application;

**[0033]** FIG. 11b is a diagram of a structure of a bulk acoustic resonator according to still another embodiment of this application;

**[0034]** FIG. 12 is a diagram of a structure of a bulk acoustic resonator according to still another embodiment of this application;

**[0035]** FIG. 13 is a diagram of a structure of a bulk acoustic resonator according to still another embodiment of this application;

**[0036]** FIG. 14 is a diagram of a structure of a bulk acoustic resonator according to still another embodiment of this application;

**[0037]** FIG. 15 is a diagram of a structure of a bulk acoustic resonator according to still another embodiment of this application;

**[0038]** FIG. 16 is a diagram of a structure of a substrate according to an embodiment of this application; and

**[0039]** FIG. 17 is a diagram of a structure of a bulk acoustic resonator according to still another embodiment of this application.

**[0040]** Reference numerals: 1-electronic device; 10-acoustic filter; 11-cover; 12-display; 13-middle frame; 14-rear housing; 100-bulk acoustic resonator; 101-piezoelectric material layer; 102-interdigital transducer; 103-dielectric layer; 104-substrate; 105-reflection layer; 131-bearing plate; 132-bezel; 1021a-first busbar; 1021b-first electrode; 1022a-second busbar; 1022b-second electrode; 1031a-area; 1051-first acoustic impedance layer; and 1052-second acoustic impedance layer.

## DESCRIPTION OF EMBODIMENTS

**[0041]** The following describes technical solutions in embodiments of this application with reference to the accompanying drawings in embodiments of this application. It is clear that the described embodiments are merely some rather than all of embodiments of this application.

**[0042]** The terms such as "first" and "second", below are merely for convenience of description, and are not to be understood as indicating or implying relative importance or implicitly indicating a quantity of indicated technical features. Therefore, a feature limited by "first", "second", or the like may explicitly or implicitly include one or more features. In the descriptions of this application, unless otherwise stated, "a plurality of" means two or more than two.

**[0043]** In embodiments of this application, the word "example" or "for example" is used to represent giving an example, an illustration, or a description. Any embodiment or design scheme described as an "example" or "for example" in embodiments of this application should not be

explained as being more preferred or having more advantages than another embodiment or design scheme. Exactly, use of the terms such as "example" or "for example" is intended to present a related concept in a specific manner.

**[0044]** In embodiments of this application, "and/or" describes an association relationship between associated objects, and represents that three relationships may exist. For example, A and/or B may represent the following cases: Only A exists, both A and B exist, and only B exists, where A and B may be singular or plural. The character "/" generally indicates an "or" relationship between the associated objects.

**[0045]** Embodiments of this application provide an electronic device. The electronic device may be different types of user equipment or terminal devices, for example, a mobile phone (mobile phone), a tablet computer (pad), a personal digital assistant (personal digital assistant, PDA), a television, an intelligent wearable product (for example, a smartwatch or a smart band), a virtual reality (virtual reality, VR) terminal device, an augmented reality (augmented reality, AR) terminal device, a charging home small appliance (for example, a bean pulp machine or a ground-sweeping robot), an uncrewed aerial vehicle, a radar, an aerospace device, and a vehicle-mounted device. A specific form of the electronic device is not specially limited in embodiments of this application.

**[0046]** For ease of description, the following uses an example in which the electronic device is a mobile phone for description. As shown in FIG. 1, an electronic device 1 mainly includes a cover 11, a display 12, a middle frame 13, and a rear housing 14. The rear housing 14 and the display 12 are respectively located on two sides of the middle frame 13, the middle frame 13 and the display 12 are disposed in the rear housing 14, the cover 11 is disposed on a side that is of the display 12 and that is away from the middle frame 13, and a display surface of the display 12 faces the cover 11.

**[0047]** The display 12 may be a liquid crystal display (liquid crystal display, LCD). In this case, the liquid crystal display includes a liquid crystal display panel and a backlight module. The liquid crystal display panel is disposed between the cover 11 and the backlight module, and the backlight module is configured to provide a light source for the liquid crystal display panel. The display 12 may alternatively be an organic light emitting diode (organic light emitting diode, OLED) display. Because the OLED display is a self-luminous display, no backlight module needs to be disposed.

**[0048]** The middle frame 13 includes a bearing plate 131 and a bezel 132 around the bearing plate 131. The electronic device 1 may further include electronic components such as a printed circuit board (printed circuit board, PCB), a battery, and a camera. The electronic components such as the printed circuit board, the battery, and the camera may be disposed on the bearing plate 131.

**[0049]** The electronic device 1 may further include a system on chip (system on chip, SoC), a

radio frequency chip, and the like that are disposed on the PCB. The PCB is configured to carry the system on chip, the radio frequency chip, and the like, and is electrically connected to the system on chip, the radio frequency chip, and the like. The radio frequency chip may include parts such as an acoustic filter and a processor. The processor is configured to process various signals.

5 The acoustic filter is an important part of radio frequency signal processing, and is configured to block a signal of another frequency through a signal of a specific frequency.

**[0050]** Embodiments of this application provide an acoustic filter. The acoustic filter may be used in the electronic device 1, for example, used in the radio frequency chip in the electronic device 1. The acoustic filter provided in embodiments of this application may be, for example, a  
10 low-pass acoustic filter, a high-pass acoustic filter, a band-pass acoustic filter, a band-stop acoustic filter, or an active acoustic filter.

**[0051]** A ladder-type acoustic filter is of a topology structure commonly used by current acoustic filters. The acoustic filter provided in this embodiment of this application is the ladder-type acoustic filter. As shown in FIG. 2, an acoustic filter 10 provided in embodiments of this  
15 application includes a plurality of cascaded bulk acoustic wave (bulk acoustic wave, BAW) resonators 100. The plurality of bulk acoustic resonators 100 may have different resonance frequencies, and may be cascaded together in a serial-parallel manner. Refer to FIG. 2. When the plurality of bulk acoustic resonators 100 are cascaded together in the serial-parallel manner, FIG. 2 further shows a signal input end  $V_{in}$ , a signal output end  $V_{out}$ , and a ground end GND of the  
20 acoustic filter 10.

**[0052]** Here, the acoustic filter formed by the resonators that are cascaded in the serial-parallel manner and that are with the different resonance frequencies has advantages such as small passband insertion loss, high out-of-band steepness, and strong power tolerance.

**[0053]** Embodiments of this application further provide a bulk acoustic resonator. The bulk  
25 acoustic resonator 100 may be used in the acoustic filter 10. The following describes a structure of the bulk acoustic resonator 100 by using several embodiments as examples.

### **Embodiment 1**

**[0054]** In Embodiment 1, as shown in FIG. 3a and FIG. 3b, a bulk acoustic resonator 100 includes a piezoelectric material layer 101, an interdigital transducer (interdigital transducer, IDT)  
30 102, and a dielectric layer 103.

**[0055]** FIG. 3a is a diagram of a top view of a structure of the bulk acoustic resonator 100. FIG. 3b is a cross-sectional diagram along an AA direction in FIG. 3a.

**[0056]** Herein, a material of the piezoelectric material layer 101 may include, for example, one or more of  $\text{LiNbO}_3$  (lithium niobate),  $\text{LiTaO}_3$  (lithium tantalate), and the like.

**[0057]** It should be understood that a function of the piezoelectric material layer 101 is to generate inverse piezoelectric effect by converting electric energy into mechanical energy in a form of a sound wave. A resonator provided in this embodiment of this application is the bulk acoustic resonator. For the bulk acoustic resonator, a sound wave is mainly propagated in the piezoelectric material layer 101. Compared with that of a surface acoustic wave (surface acoustic wave, SAW) resonator, a thickness of the piezoelectric material layer 101 in the bulk acoustic resonator is smaller. In this embodiment of this application, for example, the thickness of the piezoelectric material layer 101 may be (0, 2]  $\mu\text{m}$ , that is, the thickness of the piezoelectric material layer 101 is greater than 0  $\mu\text{m}$  and less than or equal to 2  $\mu\text{m}$ .

**[0058]** Refer to FIG. 3b. The interdigital transducer 102 is disposed on the piezoelectric material layer 101. As shown in FIG. 4, the interdigital transducer 102 includes a first busbar (busbar) 1021a and a second busbar 1022a that are disposed opposite to each other, a plurality of first electrodes (IDT electrodes) 1021b, and a plurality of second electrodes 1022b. The plurality of first electrodes 1021b sequentially protrude from the first busbar 1021a to the second busbar 1022a along an extension direction of the first busbar 1021a. The plurality of second electrodes 1022b sequentially protrude from the second busbar 1022a to the first busbar 1021a along an extension direction of the second busbar 1022a. The plurality of first electrodes 1021b and the plurality of second electrodes 1022b are sequentially arranged in a staggered manner between the first busbar 1021a and the second busbar 1022a, and the first electrode 1021b and the second electrode 1022b are not in contact with each other.

**[0059]** That "the plurality of first electrodes 1021b and the plurality of second electrodes 1022b are sequentially arranged in the staggered manner between the first busbar 1021a and the second busbar 1022a" indicates that one first electrode 1021b, one second electrode 1022b, one first electrode 1021b, one second electrode 1022b, one first electrode 1021b, one second electrode 1022b, and so on are sequentially disposed between the first busbar 1021a and the second busbar 1022a.

**[0060]** A quantity of first electrodes 1021b and a quantity of second electrodes 1022b in the interdigital transducer 102 are not limited, and may be set according to a requirement. The plurality of first electrodes 1021b may be arranged at an equal spacing, or may be arranged at a non-equal spacing. Similarly, the plurality of second electrodes 1022b may be arranged at an equal spacing, or may be arranged at a non-equal spacing. The first electrode 1021b is used as an example. That the plurality of first electrodes are 1021b are arranged at a non-equal spacing means that a spacing between at least one pair of adjacent first electrodes 1021b is different from a spacing between another pair of adjacent first electrodes 1021b.

**[0061]** In addition, that the plurality of first electrodes 1021b and the plurality of second

electrodes 1022b are sequentially arranged in the staggered manner may indicate that spacings between the adjacent first electrodes 1021b and the second electrodes 1022b are the same; or may indicate that spacings between a plurality of pairs of adjacent first electrodes 1021b and second electrodes 1022b are not totally the same, that is, a spacing between at least one pair of adjacent first electrode 1021b and second electrode 1022b is different from a spacing between another pair of adjacent first electrode 1021b and second electrode 1022b.

**[0062]** It should be noted that the first busbar 1021a, the first electrode 1021b, the second busbar 1022a, and the second electrode 1022b may be manufactured simultaneously; or the first busbar 1021a and the first electrode 1021b may be manufactured first, and then the second busbar 1022a and the second electrode 1022b are manufactured; or the second busbar 1022a and the second electrode 1022b are first manufactured, and then the first busbar 1021a and the first electrode 1021b are manufactured.

**[0063]** In addition, a material of the interdigital transducer 102 may be, for example, one or more of Al (aluminum), Mo (molybdenum), W (tungsten), Ru (ruthenium), Cu (copper), and Pt (platinum).

**[0064]** Still refer to FIG. 3b. The dielectric layer 103 covers the piezoelectric material layer 101 and the interdigital transducer 102, thicknesses of a part, of the dielectric layer 103, located between at least one pair of adjacent first electrode 1021b and second electrode 1022b fluctuate, and the part, of the dielectric layer 103, whose thicknesses fluctuate is used to suppress excitation of a high-order mode during sound wave propagation. As shown in FIG. 3b, the part, of the dielectric layer 103, whose thicknesses fluctuate and that is located between the pair of adjacent first electrode 1021b and second electrode 1022b is marked by 1031.

**[0065]** A material of the dielectric layer 103 may include, for example, one or more of silicon oxide (SiOx), silicon nitride (SiNx), silicon oxynitride (SiOxNy), or aluminum oxide (Al<sub>2</sub>O<sub>3</sub>).

**[0066]** It should be noted that, it may be that thicknesses of the dielectric layer 103 between one pair of adjacent first electrode 1021b and second electrode 1022b fluctuate; or thicknesses of the dielectric layer 103 between a plurality of pairs of adjacent first electrodes 1021b and second electrodes 1022b fluctuate.

**[0067]** Herein, that the thicknesses of the part, of the dielectric layer 103, located between the pair of adjacent first electrode 1021b and second electrode 1022b fluctuate may be understood as that all thicknesses of the dielectric layer 103 located between the pair of adjacent first electrode 1021b and second electrode 1022b fluctuate; or may be understood as that some thicknesses of the dielectric layer 103 located between the pair of adjacent first electrode 1021b and second electrode 1022b fluctuate, and some thicknesses remain unchanged. On this basis, if the thicknesses of the part, of the dielectric layer 103, located between the pair of adjacent first electrode 1021b and

second electrode 1022b fluctuate, the fluctuated part may be a continuous whole, or may include a plurality of discontinuous sub-parts.

**[0068]** On this basis, in addition to the part located between the adjacent first electrode 1021b and the second electrode 1022b, the dielectric layer 103 further includes a part located above the first electrode 1021b or the second electrode 1022b. In FIG. 3b, the part located above the first electrode 1021b or the second electrode 1022b in the dielectric layer 103 is illustrated by 1032. The part 1032 located above the first electrode 1021b or the second electrode 1022b overlaps with the first electrode 1021b or the second electrode 1022b in a direction perpendicular to the piezoelectric material layer 101.

**[0069]** Herein, thicknesses of the part 1032 above the first electrode 1021b or the second electrode 1022b may be totally the same, that is, the thicknesses do not fluctuate; or thicknesses of the part 1032 above the first electrode 1021b or the second electrode 1022b may fluctuate. It should be understood that, in some examples, as shown in FIG. 5a, thicknesses of a part, of the dielectric layer 103, located between each pair of adjacent first electrode 1021b and second electrode 1022b fluctuate. In some other examples, as shown in FIG. 5b, thicknesses of a part, of the dielectric layer 103, located between at least one pair of adjacent first electrode 1021b and second electrode 1022b fluctuate, and thicknesses of a part, of the dielectric layer 103, located between at least one pair of first electrode 1021b and second electrode 1022b do not fluctuate, that is, thicknesses of a part, of the dielectric layer 103, located in at least one pair of adjacent first electrode 1021b and second electrode 1022b are the same. As shown in FIG. 5b, the part, of the dielectric layer 103, whose thicknesses do not fluctuate and that is located between one pair of adjacent first electrode 1021b and second electrode 1022b is marked by 1033.

**[0070]** Herein, it may be that thicknesses of the dielectric layer 103 between one pair of adjacent first electrode 1021b and second electrode 1022b do not fluctuate; or thicknesses of the dielectric layer 103 between a plurality of pairs of adjacent first electrodes 1021b and second electrodes 1022b do not fluctuate.

**[0071]** As mobile communication develops and people's requirements for a communication speed are increasing, more and more frequency bands are used in a mobile communication system. As important frequency bands in a 5G communication system, N77 and N79 have characteristics such as a high working frequency and a large bandwidth. In particular, an operating frequency band of the N77 frequency band is 3300 MHz to 4200 MHz, and a bandwidth ratio is 24%. A ladder-type filter has advantages of small passband insertion loss, high out-of-band steepness, and strong power tolerance. Therefore, a filter of a frequency band below 3 GHz is mainly of a ladder-type topology formed by cascading a surface acoustic wave (surface acoustic wave, SAW) resonator, a film bulk acoustic resonator (film bulk acoustic resonator, FBAR), or a solidly

mounted bulk acoustic resonator (solidly mounted resonator, SMR). In view of the bandwidth ratio 24% of the N77 frequency band, the resonator that forms the ladder-type filter needs to have an electromechanical coupling coefficient of more than 25% to meet a bandwidth requirement of the N77 frequency band.

5 **[0072]** However, a current surface acoustic wave resonator, film bulk acoustic resonator, and solidly mounted bulk acoustic resonator cannot meet the requirement. The bulk acoustic resonator provided in this embodiment of this application is an excited film bulk acoustic resonator (excited film bulk acoustic resonator, XBAR). The bulk acoustic resonator provided in this embodiment of this application excites an A1 lamb (lam) wave on the piezoelectric material layer 101, so that the  
10 bulk acoustic resonator 100 has an electromechanical coupling coefficient of 25%, and therefore can meet the requirement of the N77 frequency band.

**[0073]** FIG. 6 shows the XBAR resonator according to a related technology. As shown in FIG. 6, the XBAR resonator includes the piezoelectric material layer 101, the interdigital transducer 102, and the dielectric layer 103. The interdigital transducer 102 is disposed on the piezoelectric  
15 material layer 101. For a structure of the interdigital transducer 102, refer to the foregoing descriptions. Details are not described herein again. The dielectric layer 103 covers the interdigital transducer 102. Thicknesses of the dielectric layer 103 located between the adjacent first electrode index 1021b and the second electrode index 1022b do not fluctuate, that is, thicknesses are the same. The bulk acoustic resonator 100 provided in FIG. 6 excites the A1 lamb wave on the  
20 piezoelectric material layer 101. Although the bulk acoustic resonator 100 may have the electromechanical coupling coefficient of 25%, and can therefore meet the requirement of the N77 frequency band, the bulk acoustic resonator 100 provided in FIG. 6 has another body wave mode and an A1 high-order mode, which causes a severe spurious mode and severely restricts design and performance of the acoustic filter 10. After preparation of the bulk acoustic resonator 100 in  
25 the acoustic filter 10 is completed, an admittance (admittance) curve corresponding to the bulk acoustic resonator 100 is usually obtained through testing via a vector network analyzer, and the admittance curve may represent various indexes of the bulk acoustic resonator 100.

**[0074]** FIG. 7 shows the admittance curve corresponding to the bulk acoustic resonator 100 shown in FIG. 6. In FIG. 7, a horizontal coordinate represents a frequency, and a vertical coordinate  
30 represents admittance. Refer to FIG. 7, a resonance frequency and an anti-resonance frequency of the bulk acoustic resonator 100 are 3960 MHz and 4940 MHz respectively. Refer to FIG. 7. It can be further seen that, in addition to including main resonance, the bulk acoustic resonator 100 further has other spurious resonance. For example, spurious resonance exists at a frequency of 5160 MHz. FIG. 7 further shows a displacement field distribution diagram corresponding to a  
35 main mode and a spurious mode. It can be seen from the displacement field distribution diagram

that the main mode is an A1 lamb mode, namely, a low-order mode. In FIG. 7, the spurious mode at the frequency of 5160 MHz is caused by a high-order mode formed by a sound wave between the first electrode 1021b and the second electrode 1022b. The spurious resonance severely affects performance of the acoustic filter 10 and restricts design of the acoustic filter 10. It can be seen from the displacement field distribution diagram corresponding to the spurious mode in FIG. 7 that the high-order mode formed between the first electrode 1021b and the second electrode 1022b is excited because the sound wave meets a high-order resonance condition when being propagated in a constant transmission characteristic area between the first electrode 1021b and the second electrode 1022b.

**[0075]** However, in this Embodiment 1, because the thicknesses of the part, of the dielectric layer 103 in the bulk acoustic resonator 100, located between the at least one pair of adjacent first electrode 1021b and second electrode 1022b fluctuate, when the sound wave is propagated between the first electrode 1021b and the second electrode 1022b, the part, of the dielectric layer 103, whose thicknesses fluctuate may destroy a periodicity of propagation, change a sound wave transmission characteristic, and therefore can suppress excitation of the high-order mode during sound wave propagation. This can excite the main mode of the bulk acoustic resonator 100, suppress the high-order spurious mode, reduce or avoid spurious resonance, and help improve performance of the acoustic filter 10 and facilitate design of the acoustic filter 10.

**[0076]** FIG. 8 shows an admittance curve corresponding to the bulk acoustic resonator 100 provided in FIG. 3b according to this embodiment of this application. In FIG. 8, a horizontal coordinate represents a frequency, and a vertical coordinate represents admittance. Refer to FIG. 8, a resonance frequency and an anti-resonance frequency of the bulk acoustic resonator 100 are 3960 MHz and 4940 MHz respectively. It can be seen from comparison between FIG. 7 and FIG. 8 that the admittance curve shown in FIG. 8 has no other spurious resonance except the main mode. FIG. 8 further shows a displacement field distribution diagram corresponding to the main mode and a spurious resonance frequency in FIG. 7. It can be seen from the displacement field distribution diagram in FIG. 8 that, in the bulk acoustic resonator 100 provided in FIG. 3b, only the main mode is excited, and a high-order spurious mode is suppressed.

**[0077]** On this basis, it should be noted that, when the thicknesses of the part, of the dielectric layer 103, located between the at least one pair of adjacent first electrode 1021b and second electrode 1022b fluctuate, the thicknesses of the part, of the dielectric layer 103, located between the at least one pair of adjacent first electrode 1021b and second electrode 1022b may be distributed according to a specific change rule, or may be distributed irregularly. When the thicknesses of the part, of the dielectric layer 103, located between the at least one pair of adjacent first electrode 1021b and second electrode 1022b are distributed according to the specific change

rule, the following provides several implementations by using examples.

**[0078]** In a first implementation, as shown in FIG. 3b, along a direction in which the plurality of first electrodes 1021b and the plurality of second electrodes 1022b are sequentially arranged in the staggered manner, the thicknesses of the part, of the dielectric layer 103, located between the at least one pair of adjacent first electrode 1021b and second electrode 1022b are first decreased and then increased.

**[0079]** In a second implementation, as shown in FIG. 9, along a direction in which the plurality of first electrodes 1021b and the plurality of second electrodes 1022b are sequentially arranged in the staggered manner, the thicknesses of the part, of the dielectric layer 103, located between the at least one pair of first electrode 1021b and second electrode 1022b are first increased and then decreased.

**[0080]** In a third implementation, as shown in FIG. 10a, the part, of the dielectric layer 103, located between the at least one pair of adjacent first electrode 1021b and second electrode 1022b includes a plurality of areas 1031a that are sequentially arranged along a direction in which the plurality of first electrodes 1021b and the plurality of second electrodes 1022b are sequentially arranged in the staggered manner, and along the direction in which the plurality of first electrodes 1021b and the plurality of second electrodes 1022b are sequentially arranged in the staggered manner, thicknesses of each area 1031a are first decreased and then increased.

**[0081]** In a fourth implementation, as shown in FIG. 10b, the part, of the dielectric layer 103, located between the at least one pair of adjacent first electrode 1021b and second electrode 1022b includes a plurality of areas 1031a that are sequentially arranged along a direction in which the plurality of first electrodes 1021b and the plurality of second electrodes 1022b are sequentially arranged in the staggered manner, and along the direction in which the plurality of first electrodes 1021b and the plurality of second electrodes 1022b are sequentially arranged in the staggered manner, thicknesses of each area 1031a are first increased and then decreased.

**[0082]** In the third implementation and the fourth implementation, a quantity of areas 1031a included in the part, of the dielectric layer 103, located between the at least one pair of adjacent first electrode 1021b and second electrode 1022b may be set according to a requirement. In FIG. 10a and FIG. 10b, an example in which the part, of the dielectric layer 103, located between the at least one pair of adjacent first electrode 1021b and second electrode 1022b includes two areas 1031a is used for illustration.

**[0083]** It should be noted that, in the first, second, third, and fourth implementations, a surface that is of the part, of the dielectric layer 103, whose thicknesses fluctuate and that is far away from the piezoelectric material layer 101 may be formed by a plurality of planes, or may be formed by a plurality of arc surfaces, or certainly may be formed by at least one plane and at least one arc

surface. The first implementation is used as an example. In FIG. 3b, the surface that is of the part, of the dielectric layer 103, whose thicknesses fluctuate and that is far away from the piezoelectric material layer 101 is formed by a plurality of planes. In FIG. 11a, a surface that is of the part, of the dielectric layer 103, whose thicknesses fluctuate and that is far away from the piezoelectric material layer 101 is formed by a plurality of arc surfaces. In FIG. 11b, a surface that is of the part, of the dielectric layer 103, whose thicknesses fluctuate and that is far away from the piezoelectric material layer 101 is formed by at least one plane and at least one arc surface.

**[0084]** In addition, when a surface that is of the part, of the dielectric layer 103, whose thicknesses fluctuate and that is far away from the piezoelectric material layer 101 includes an arc surface, the arc surface may protrude in a direction away from the piezoelectric material layer 101, or may be concave in a direction close to the piezoelectric material layer 101. In FIG. 11a and FIG. 11b, an example in which the arc is concave in a direction close to the piezoelectric material layer 101 is used for illustration.

**[0085]** When the thicknesses of the part, of the dielectric layer 103, located between the at least one pair of adjacent first electrode 1021b and second electrode 1022b are changed in a rule in the first, second, third, or fourth implementation, because the thicknesses of the part, of the dielectric layer 103, located between the at least one pair of adjacent first electrode 1021b and second electrode 1022b fluctuate, the part, of the dielectric layer 103, whose thicknesses change may destroy a periodicity of sound wave propagation, and therefore suppress excitation of the high-order mode during sound wave propagation. This reduces or avoids spurious resonance, and helps improve performance of the acoustic filter 10 and facilitates design of the acoustic filter 10. On this basis, because the thicknesses of the part, of the dielectric layer 103, whose thicknesses change are distributed according to a specific rule, an electromechanical coupling coefficient of the main mode of the bulk acoustic resonator 100 can be increased, and design difficulty of the dielectric layer 103 can be simplified. In addition, the first, the second, the third, or the fourth implementation may be selected according to a requirement, so that a design freedom of the dielectric layer 103 can be improved.

**[0086]** When the thicknesses of the part, of the dielectric layer 103, located between the at least one pair of adjacent first electrode 1021b and second electrode 1022b are changed according to the specific rule, when the dielectric layer 103 is designed, a point in the bulk acoustic resonator 101 may be used as an origin, and a thickness at a position of the dielectric layer 103 is a function of a distance  $x$  from the position to the origin along the direction in which the plurality of first electrodes 1021b and the plurality of second electrodes 1022b are sequentially arranged in the staggered manner, that is,  $f_h=f(x)$ , where  $f_h$  is the thickness at the position of the dielectric layer 103.

**[0087]** When thicknesses of parts, of the dielectric layer 103, located between a plurality of pairs of adjacent first electrodes 1021b and second electrodes 1022b fluctuate, in some examples, change rules of the thicknesses of the dielectric layer 103 between the plurality of pairs of adjacent first electrodes 1021b and second electrodes 1022b are the same. For example, as shown in FIG. 12, a change rule of thicknesses of a part 1031, of the dielectric layer 103, located between the adjacent first electrode 1021b and the second electrode 1022b is the same as a change rule of thicknesses of a part 1031', of the dielectric layer 103, located between a first electrode 1021b' and the second electrode 1022b that are adjacent. Along the direction in which the plurality of first electrodes 1021b and the plurality of second electrodes 1022b are sequentially arranged in the staggered manner, the change rule of the thicknesses of the part 1031, of the dielectric layer 103, located between the adjacent first electrode 1021b and the second electrode 1022b and the change rule of the thicknesses of the part 1031', of the dielectric layer 103, located between the adjacent first electrode 1021b' and the second electrode 1022b are first decreased and then increased.

**[0088]** When the change rules of the thicknesses of the dielectric layer 103 between the plurality of pairs of adjacent first electrodes 1021b and second electrodes 1022b are the same, a manufacturing process of the dielectric layer 103 can be simplified, and further, a manufacturing process of the bulk acoustic resonator 100 can be simplified.

**[0089]** In some other examples, change rules of the thicknesses of the dielectric layer 103 between the plurality of pairs of adjacent first electrodes 1021b and second electrodes 1022b are not totally the same. It should be understood that, "the change rules of the thicknesses of the dielectric layer 103 between the plurality of pairs of adjacent first electrodes 1021b and second electrodes 1022b are not totally the same" may be that the change rules of the thicknesses of the dielectric layer 103 between the plurality of pairs of adjacent first electrodes 1021b and second electrodes 1022b are not the same; or change rules of thicknesses of the dielectric layer 103 between some pairs of adjacent first electrodes 1021b and second electrodes 1022b are the same, and change rules of thicknesses of the dielectric layer 103 between some pairs of adjacent first electrodes 1021b and second electrodes 1022b are different. When the change rules of the thicknesses of the dielectric layer 103 between the plurality of pairs of adjacent first electrodes 1021b and second electrodes 1022b are not totally the same, a change rule of thicknesses of a part, of the dielectric layer 103, located between a first pair of adjacent first electrode 1021b and second electrode 1022b is different from a change rule of thicknesses of a part, of the dielectric layer 103, located between a second pair of adjacent first electrode 1021b and second electrode 1022b.

**[0090]** For example, as shown in FIG. 13, a change rule of thicknesses of a part, of the dielectric layer 103, located between a first pair of adjacent first electrode 1021b and second electrode 1022b, namely, 1031 in FIG. 13, is that, along the direction in which the plurality of first

electrodes 1021b and the plurality of second electrodes 1022b are sequentially arranged in the staggered manner, the thicknesses of the part, of the dielectric layer 103, located between the first pair of adjacent first electrode 1021b and second electrode 1022b, namely, 1031 in FIG. 13, are first decreased and then increased. A change rule of thicknesses of a part, of the dielectric layer 103, located between a second pair of adjacent first electrode 1021b` and second electrode 1022b, namely, 1031` in FIG. 13, is that, along the direction in which the plurality of first electrodes 1021b and the plurality of second electrodes 1022b are sequentially arranged in the staggered manner, the thicknesses of the part, of the dielectric layer 103, located between the second pair of adjacent first electrode 1021b` and second electrode 1022b, namely, 1031` in FIG. 13, are first increased and then decreased.

**[0091]** For another example, as shown in FIG. 14, a change rule of thicknesses of a part, of the dielectric layer 103, located between a first pair of adjacent first electrode 1021b and second electrode 1022b, namely, 1031 in FIG. 14, is that, along the direction in which the plurality of first electrodes 1021b and the plurality of second electrodes 1022b are sequentially arranged in the staggered manner, the thicknesses of the part, of the dielectric layer 103, located between the first pair of adjacent first electrode 1021b and second electrode 1022b, namely, 1031 in FIG. 14, are first decreased and then increased. A part, of the dielectric layer 103, located between a second pair of adjacent first electrode 1021b` and second electrode 1022b, namely, 1031` in FIG. 14, includes a plurality of areas 1031a that are sequentially arranged along the direction in which the plurality of first electrodes 1021b and the plurality of second electrodes 1022b are sequentially arranged in the staggered manner, and along the direction in which the plurality of first electrodes 1021b and the plurality of second electrodes 1022b are sequentially arranged in the staggered manner, thicknesses of each area 1031a are first decreased and then increased.

**[0092]** When the change rule of the thicknesses of the part, of the dielectric layer 103, located between the first pair of adjacent first electrode 1021b and second electrode 1022b is different from the change rule of the thicknesses of the part, of the dielectric layer 103, located between the second pair of adjacent first electrode 1021b and second electrode 1022b, this can further destroy the periodicity during sound wave propagation, suppress excitation of the high-order mode during sound wave propagation, and be more conducive to reducing or avoiding spurious resonance. In addition, the design freedom of the bulk acoustic resonator 100 can be further enriched.

**[0093]** On this basis, it may be that the first pair of adjacent first electrode 1021b and second electrode 1022b and the second pair of adjacent first electrode 1021b and second electrode 1022b share one of the first electrode 1021b or the second electrode 1022b, that is, the first pair of adjacent first electrode 1021b and second electrode 1022b and the second pair of adjacent first electrode 1021b and second electrode 1022b are disposed adjacently. It may alternatively be that the first

pair of adjacent first electrode 1021b and second electrode 1022b and the second pair of adjacent first electrode 1021b and second electrode 1022b do not share one of the first electrode 1021b or the second electrode 1022b, that is, the first pair of adjacent first electrode 1021b and second electrode 1022b and the second pair of adjacent first electrode 1021b and second electrode 1022b are not disposed adjacently.

## **Embodiment 2**

**[0094]** A difference between Embodiment 2 and Embodiment 1 lies in that, compared with that in Embodiment 1, a substrate is added in Embodiment 2.

**[0095]** A bulk acoustic resonator 100 provided in Embodiment 2 includes a piezoelectric material layer 101, an interdigital transducer 102, a dielectric layer 103, and the substrate, where the piezoelectric material layer 101, the interdigital transducer 102, and the dielectric layer 103 are fastened to the substrate. For the piezoelectric material layer 101, the interdigital transducer 102, and the dielectric layer 103, refer to related descriptions in Embodiment 1. Details are not described again in Embodiment 2. Embodiment 2 describes only a part different from that in Embodiment 1, that is, only the substrate is described.

**[0096]** In Embodiment 2, as shown in FIG. 15, the bulk acoustic resonator 100 further includes the substrate 104, where the substrate 104 is disposed on a side that is of the piezoelectric material layer 101 and that is away from the interdigital transducer 102. As shown in FIG. 16, the substrate 104 is enclosed in a ring. FIG. 15 is a sectional view of the bulk acoustic resonator 100. FIG. 16 is a top view of the substrate 104.

**[0097]** Herein, the ring enclosed by the substrate 104 may be, for example, a circular ring, a square ring, another regular ring, or an irregular ring. It may be understood that a specific shape of the ring may be designed based on a shape of a graph enclosed by a boundary of the piezoelectric material layer 101.

**[0098]** In some examples, a material of the substrate 104 is a high sound rate material, and the high sound rate material may include, for example, one or more of silicon (Si), silicon carbide (SiC), diamond (diamond), sapphire (sapphire), aluminum nitride (AlN), and the like.

**[0099]** The substrate 104 is disposed in the bulk acoustic resonator 100, and the substrate 104 may play a supporting role, and is configured to support the piezoelectric material layer 101, the interdigital transducer 102, and the dielectric layer 103. On this basis, because the substrate 104 is enclosed in the ring, a cavity structure can be formed. In a sound wave propagation process, the cavity structure can concentrate energy on the piezoelectric material layer 101. This can improve a quality factor of the bulk acoustic resonator 100. In addition, the substrate 104 disposed in the bulk acoustic resonator 100 can also implement process compatibility. For example, the acoustic

filter 10 may be bound (bonding) to another electronic component like an integrated circuit (integrated circuit, IC) component via the substrate 104.

### Embodiment 3

**[00100]** A difference between Embodiment 3 and Embodiment 1 lies in that, compared with that in Embodiment 1, a substrate and a reflection layer are added in Embodiment 3.

**[00101]** A bulk acoustic resonator 100 provided in Embodiment 3 includes a piezoelectric material layer 101, an interdigital transducer 102, a dielectric layer 103, the substrate, and the reflection layer, where the piezoelectric material layer 101, the interdigital transducer 102, and the dielectric layer 103 are fastened to the substrate and the reflection layer. For the piezoelectric material layer 101, the interdigital transducer 102, and the dielectric layer 103, refer to related descriptions in Embodiment 1. Details are not described again in Embodiment 3. Embodiment 3 describes only a part different from that in Embodiment 1, that is, only the substrate and the reflection layer are described.

**[00102]** In Embodiment 3, as shown in FIG. 17, the bulk acoustic resonator 100 further includes the substrate 104 and the reflection layer 105 that are disposed in a stacked manner, and the reflection layer 105 is disposed between the substrate 104 and the piezoelectric material layer 101; and the reflection layer 105 includes a first acoustic impedance layer 1051 and a second acoustic impedance layer 1052 that are alternately disposed in a stacked manner, and acoustic impedance of the first acoustic impedance layer 1051 is different from acoustic impedance of the second acoustic impedance layer 1052.

**[00103]** Herein, a quantity of disposed layers of both the first acoustic impedance layer 1051 and the second acoustic impedance layer 1052 in the reflection layer 105 is not limited, and may be set according to a requirement. A quantity of layers of the first acoustic impedance layer 1051 may be the same as or different from a quantity of layers of the second acoustic impedance layer 1052. In addition, in the reflection layer 105, the first acoustic impedance layer 1051 or the second acoustic impedance layer 1052 may be closest to the piezoelectric material layer 101. Similarly, in the reflection layer 105, the first acoustic impedance layer 1051 or the second acoustic impedance layer 1052 may be closest to the substrate 104.

**[00104]** In addition, acoustic impedance of the first acoustic impedance layer 1051 may be greater than acoustic impedance of the second acoustic impedance layer 1052, or acoustic impedance of the second acoustic impedance layer 1052 may be greater than acoustic impedance of the first acoustic impedance layer 1051.

**[00105]** In some examples, a material of the substrate 104 is a high sound rate material, and the high sound rate material may include, for example, one or more of silicon, silicon carbide, diamond,

sapphire, aluminum nitride, and the like.

**[00106]** In Embodiment 3, the bulk acoustic resonator 100 includes the reflection layer 105. Because the reflection layer 105 may reflect a sound wave to the piezoelectric material layer 101, energy can be concentrated on the piezoelectric material layer 101 in the sound wave propagation process. This can improve a quality factor of the bulk acoustic resonator 100. On this basis, the substrate 104 in the bulk acoustic resonator 100 plays a supporting role, and is configured to support the reflection layer 105, the piezoelectric material layer 101, the interdigital transducer 102, and the dielectric layer 103. In addition, the substrate 104 in the bulk acoustic resonator 100 can also implement process compatibility. For example, the acoustic filter 10 may be bound to another electronic component like an IC component via the substrate 104.

**[00107]** The foregoing descriptions are merely specific implementations of this application, but are not intended to limit the protection scope of this application. Any variation or replacement readily figured out by a person skilled in the art within the technical scope disclosed in this application shall fall within the protection scope of this application. Therefore, the protection scope of this application shall be subject to the protection scope of the claims.

## CLAIMS

What is claimed is:

1. A bulk acoustic resonator, comprising: a piezoelectric material layer, an interdigital transducer, and a dielectric layer, wherein the interdigital transducer is disposed on the piezoelectric material layer, and the interdigital transducer comprises:

a first busbar and a second busbar that are disposed opposite to each other;

a plurality of first electrodes, wherein the plurality of first electrodes sequentially protrude from the first busbar to the second busbar along an extension direction of the first busbar; and

a plurality of second electrodes, wherein the plurality of second electrodes sequentially protrude from the second busbar to the first busbar along an extension direction of the second busbar;

the plurality of first electrodes and the plurality of second electrodes are sequentially arranged in a staggered manner between the first busbar and the second busbar; and

the dielectric layer covers the piezoelectric material layer and the interdigital transducer, and thicknesses of a part, of the dielectric layer, located between at least one pair of adjacent first electrode and second electrode fluctuate.

2. The bulk acoustic resonator according to claim 1, wherein along a direction in which the plurality of first electrodes and the plurality of second electrodes are sequentially arranged in the staggered manner, the thicknesses of the part, of the dielectric layer, located between the at least one pair of adjacent first electrode and second electrode are first decreased and then increased.

3. The bulk acoustic resonator according to claim 1, wherein along a direction in which the plurality of first electrodes and the plurality of second electrodes are sequentially arranged in the staggered manner, the thicknesses of the part, of the dielectric layer, located between the at least one pair of adjacent first electrode and second electrode are first increased and then decreased.

4. The bulk acoustic resonator according to claim 1, wherein the part, of the dielectric layer, located between the at least one pair of adjacent first electrode and second electrode comprises a plurality of areas that are sequentially arranged along a direction in which the plurality of first electrodes and the plurality of second electrodes are sequentially arranged in the staggered manner, and along the direction in which the plurality of first electrodes and the plurality of second electrodes are sequentially arranged in the staggered manner, thicknesses of each area are first decreased and then increased, or first increased and then decreased.

5. The bulk acoustic resonator according to any one of claims 2 to 4, wherein a change rule of thicknesses of a part, of the dielectric layer, located between a first pair of adjacent first electrode and second electrode is different from a change rule of thicknesses of a part, of the dielectric layer,

located between a second pair of adjacent first electrode and second electrode.

6. The bulk acoustic resonator according to claim 5, wherein the first pair of adjacent first electrode and second electrode and the second pair of adjacent first electrode and second electrode share one of the first electrode or the second electrode.

5        7. The bulk acoustic resonator according to any one of claims 1 to 6, wherein the bulk acoustic resonator further comprises a substrate, the substrate is disposed on a side that is of the piezoelectric material layer and that is away from the interdigital transducer, and the substrate is enclosed in a ring.

10       8. The bulk acoustic resonator according to any one of claims 1 to 6, wherein the bulk acoustic resonator further comprises a substrate and a reflection layer that are disposed in a stacked manner, and the reflection layer is disposed between the substrate and the piezoelectric material layer; and  
the reflection layer comprises a first acoustic impedance layer and a second acoustic impedance layer that are alternately disposed in a stacked manner, and acoustic impedance of the first acoustic impedance layer is different from acoustic impedance of the second acoustic  
15       impedance layer.

9. The bulk acoustic resonator according to claim 7 or 8, wherein a material of the substrate comprises one or more of silicon, silicon carbide, diamond, sapphire, or aluminum nitride.

10       10. The bulk acoustic resonator according to any one of claims 1 to 9, wherein a material of the dielectric layer comprises one or more of silicon oxide, silicon nitride, silicon oxynitride, or  
20       aluminum oxide.

11. An acoustic filter, comprising a plurality of cascaded bulk acoustic resonators, wherein the bulk acoustic resonator is the bulk acoustic resonator according to any one of claims 1 to 10.

12. An electronic device, comprising an acoustic filter, a processor, and a printed circuit board, wherein both the acoustic filter and the processor are disposed on the printed circuit board; and  
25       the acoustic filter is the acoustic filter according to claim 11.