

ELECTRODE ARRANGEMENT FOR PLASMA DISPLAY DEVICE**ABSTRACT OF THE DISCLOSURE**

To suppress the discharge current in a plasma cell incorporated in a display device, thereby extending the useful service life of the plasma cell and stabilizing the operation characteristics of the plasma cell, the display device includes a plasma cell 2, which includes a pair of substrates 3 and 8 bonded together with a space defined therebetween, discharge electrodes 9 and 9a formed on at least the substrate 8, and an ionizable gas sealed in this space. The discharge electrodes 9 and 9a are grouped into plural pairs of anodes A and cathodes K parallel to each other, thus forming a plurality of discharge channels 12 extending in lines. At least each cathode K is partially covered with insulators 13 periodically arranged, thereby limiting the effective area of each discharge electrode 9a to suppress the discharge current.

FIGURE 1A & 1B