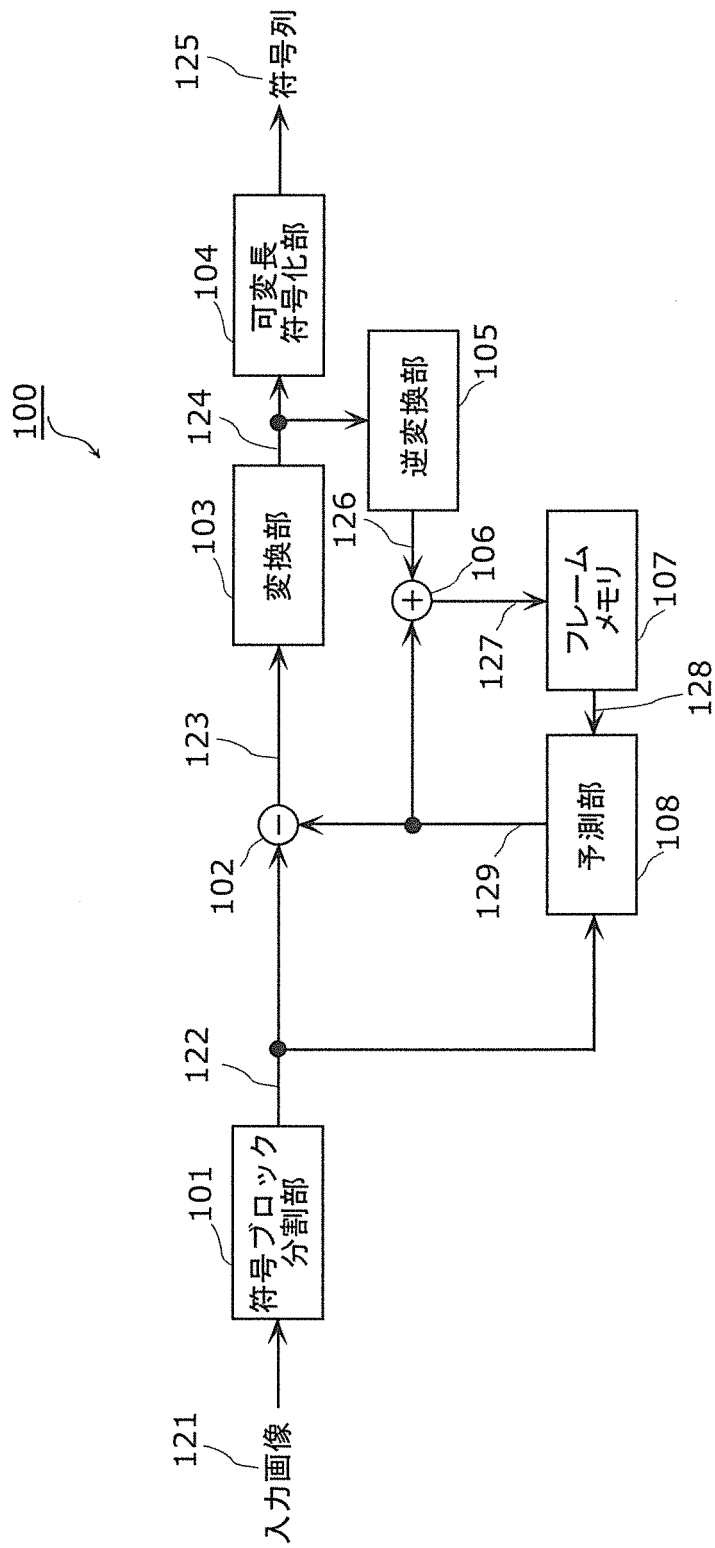
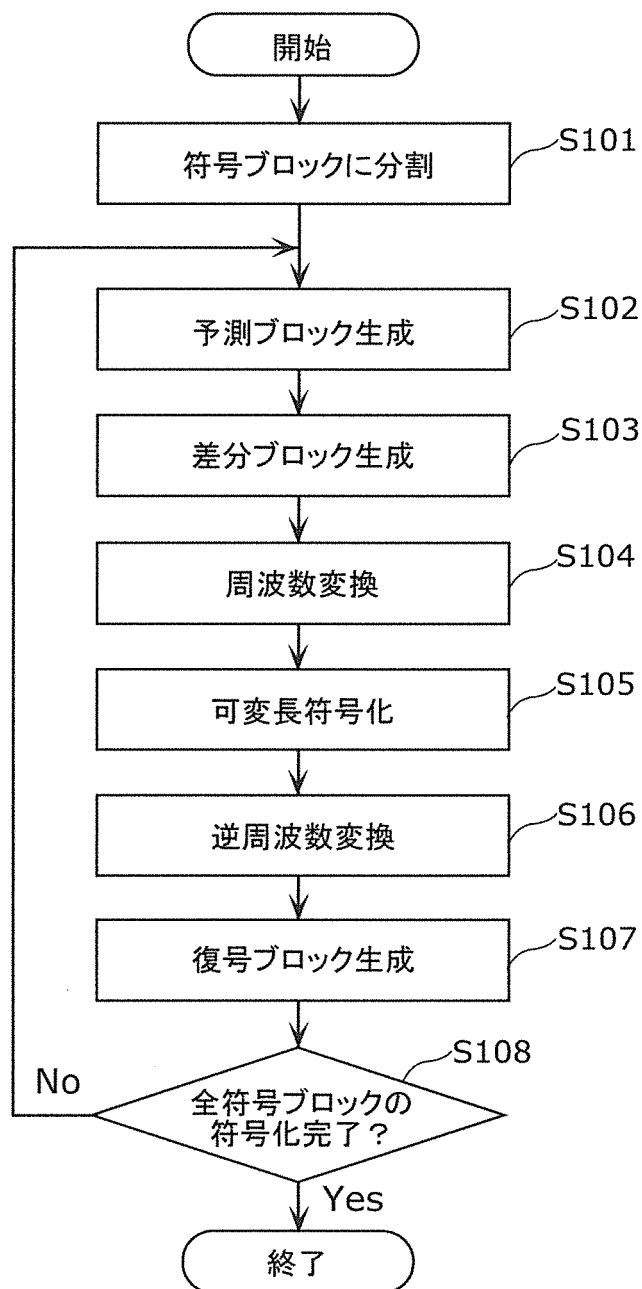


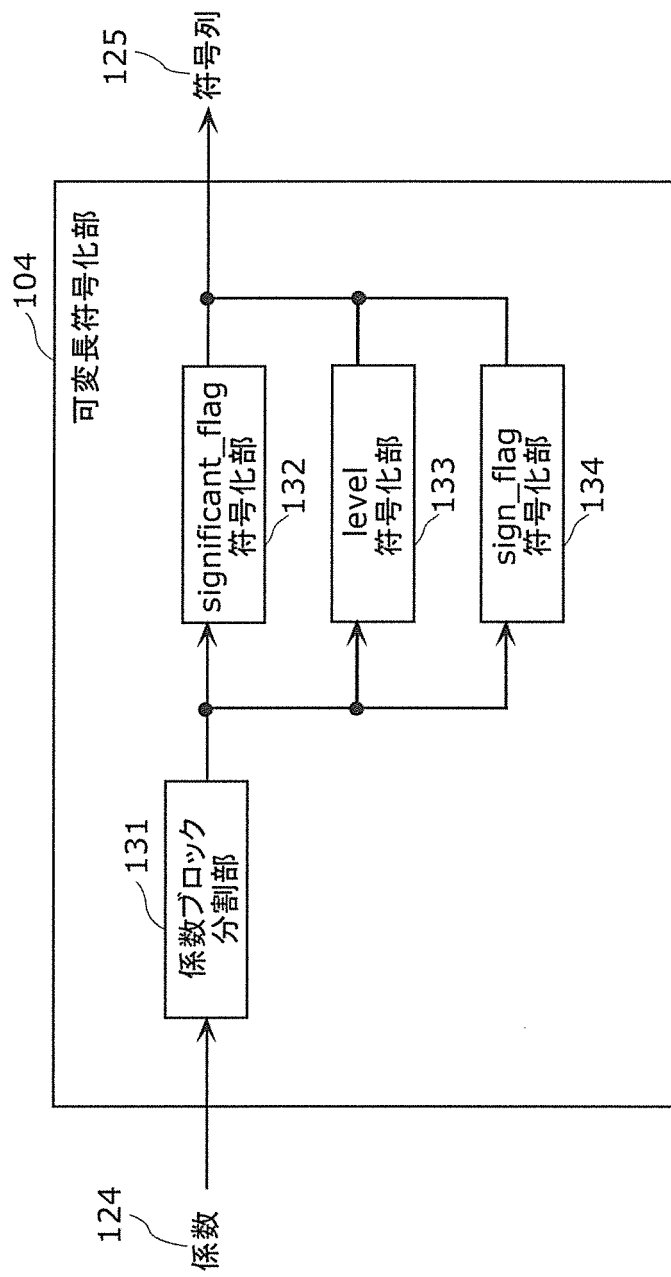
[図1]



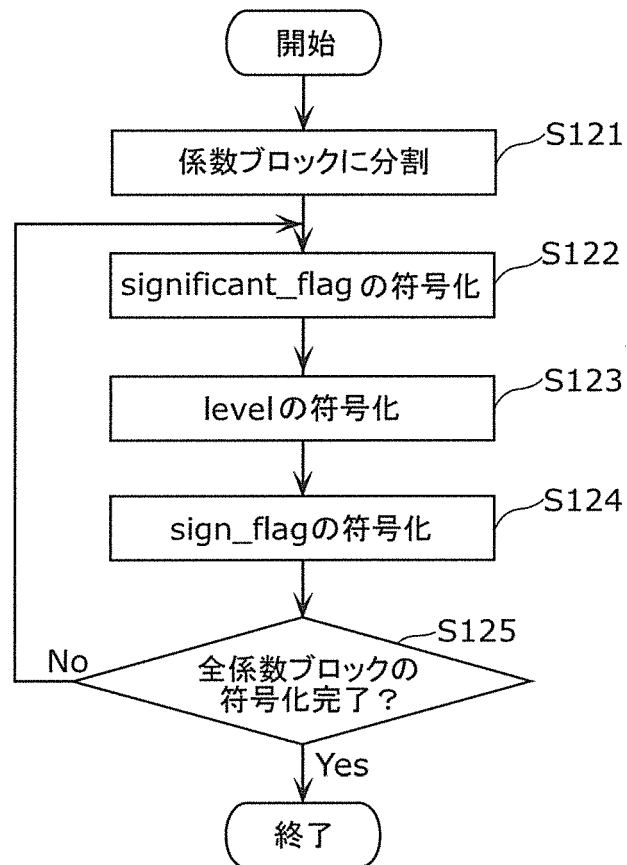
[図2]



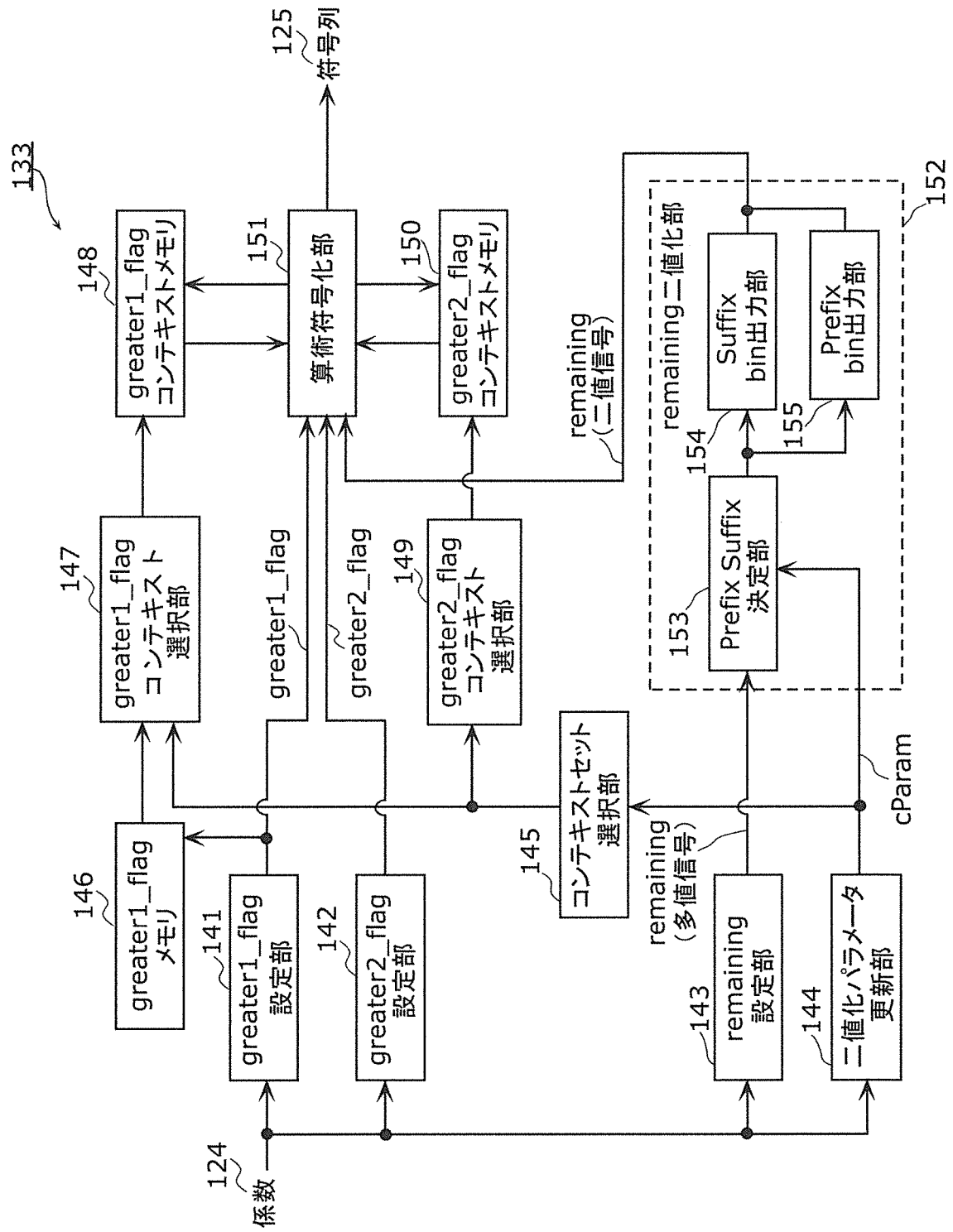
[図3]



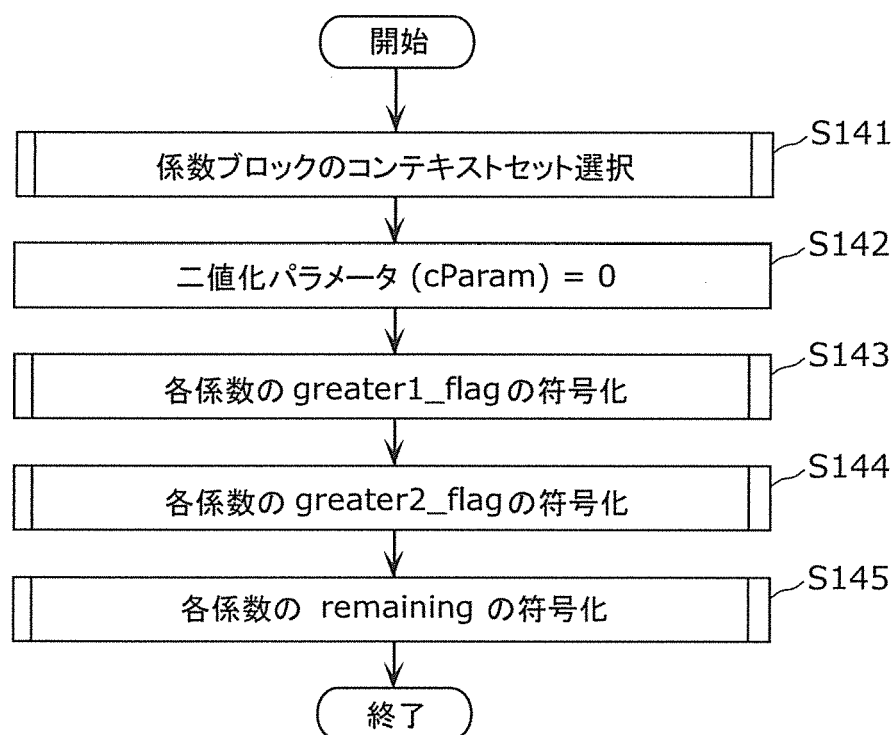
[図4]



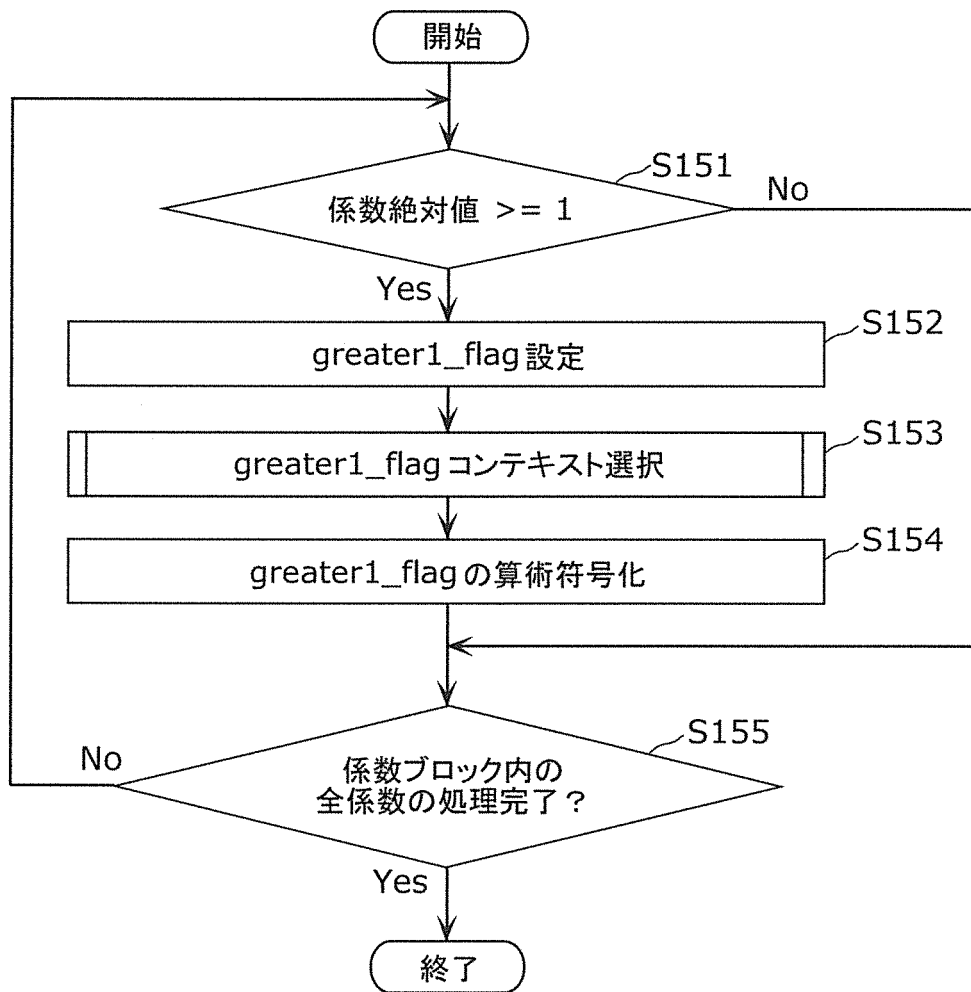
[図5]



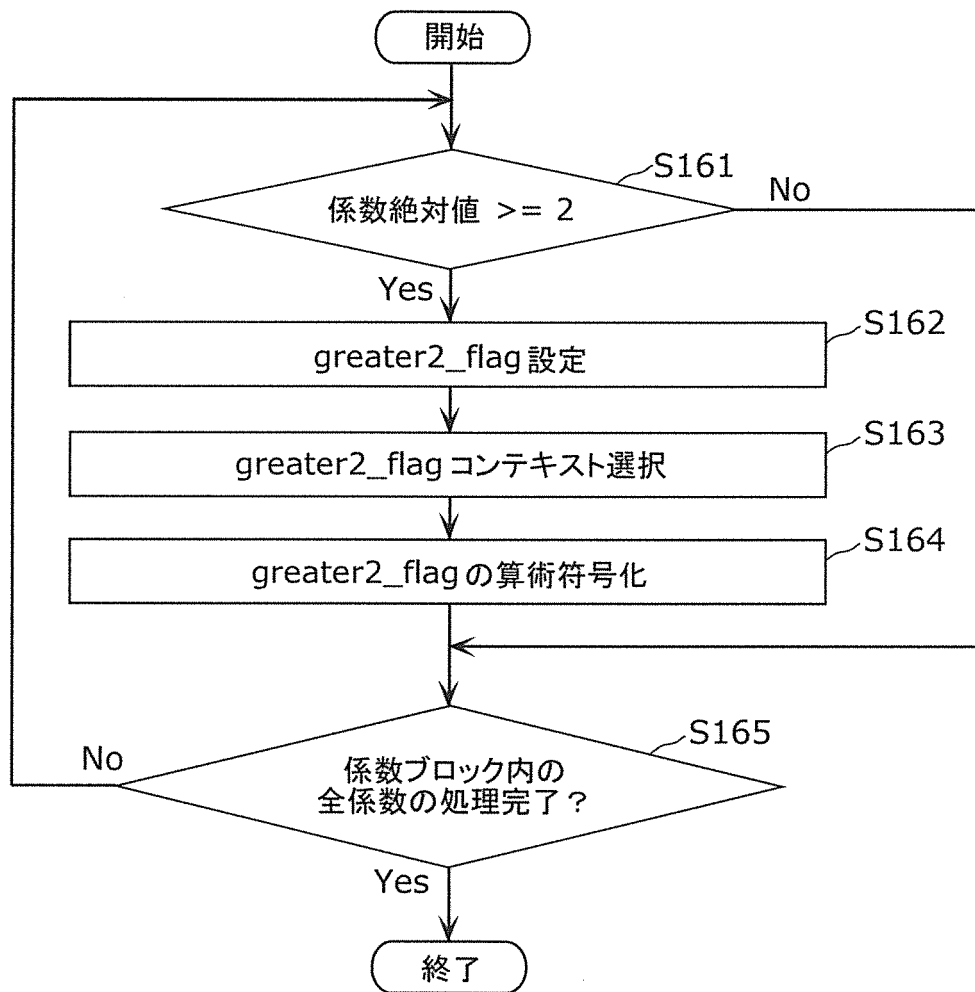
[図6]



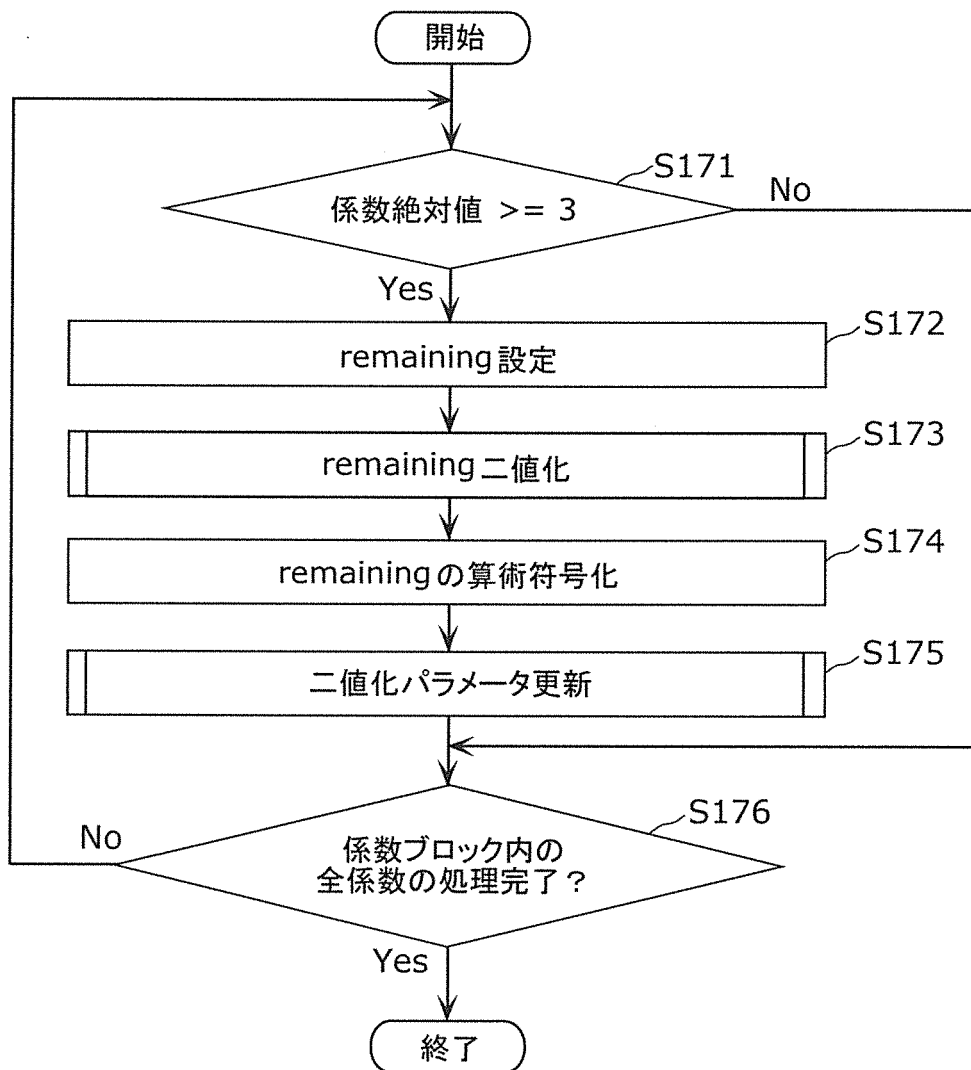
[図7]



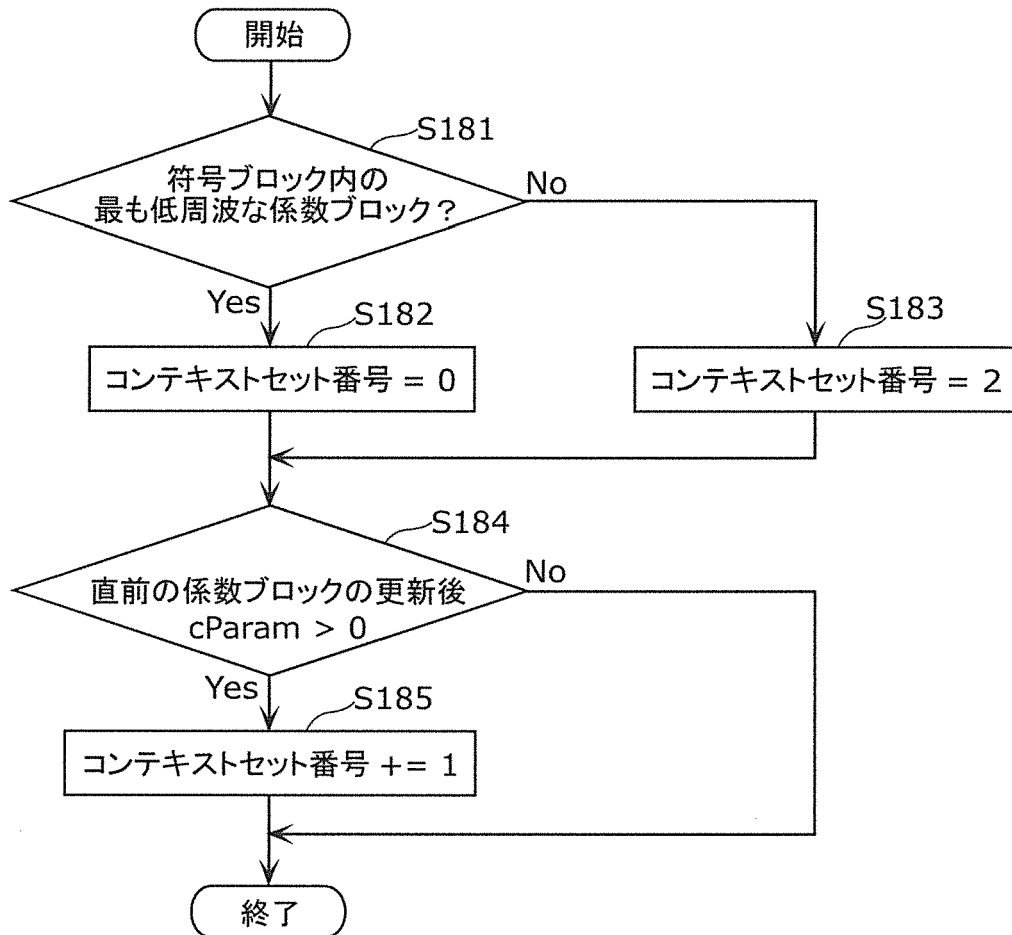
[図8]



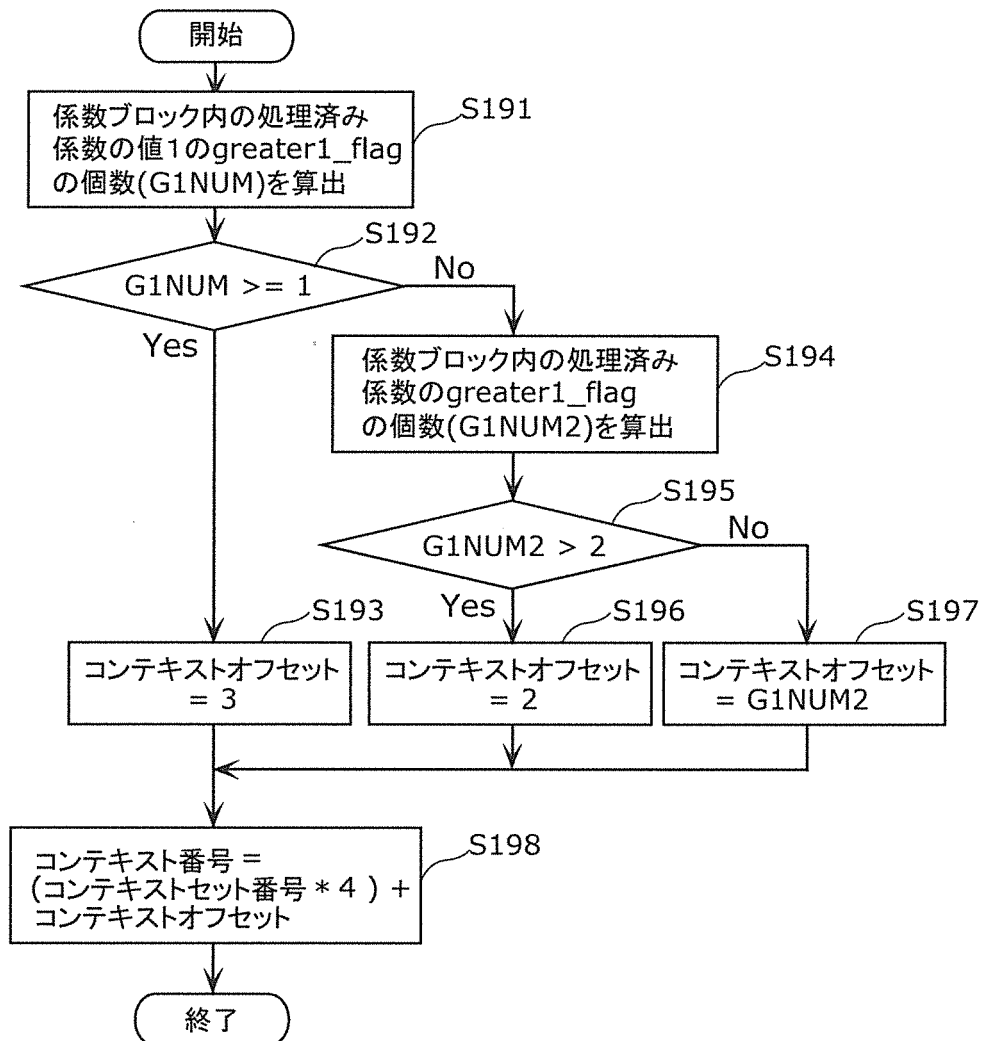
[図9]



[図10]



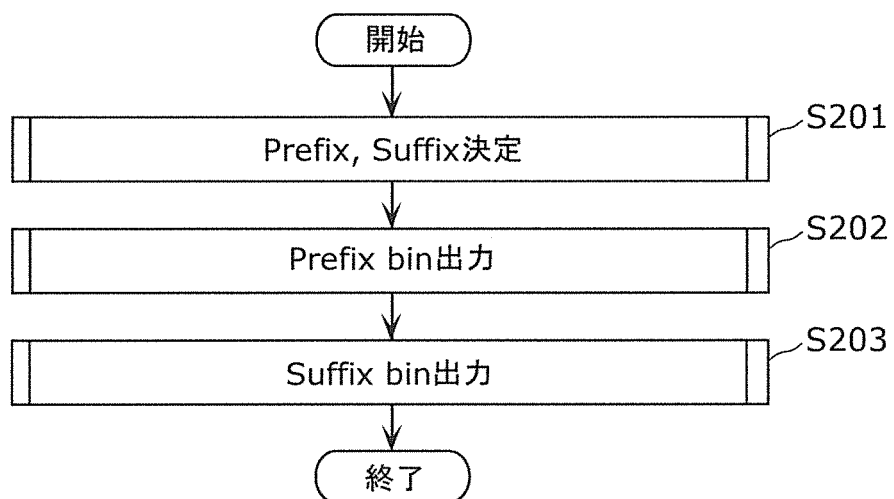
[図11]



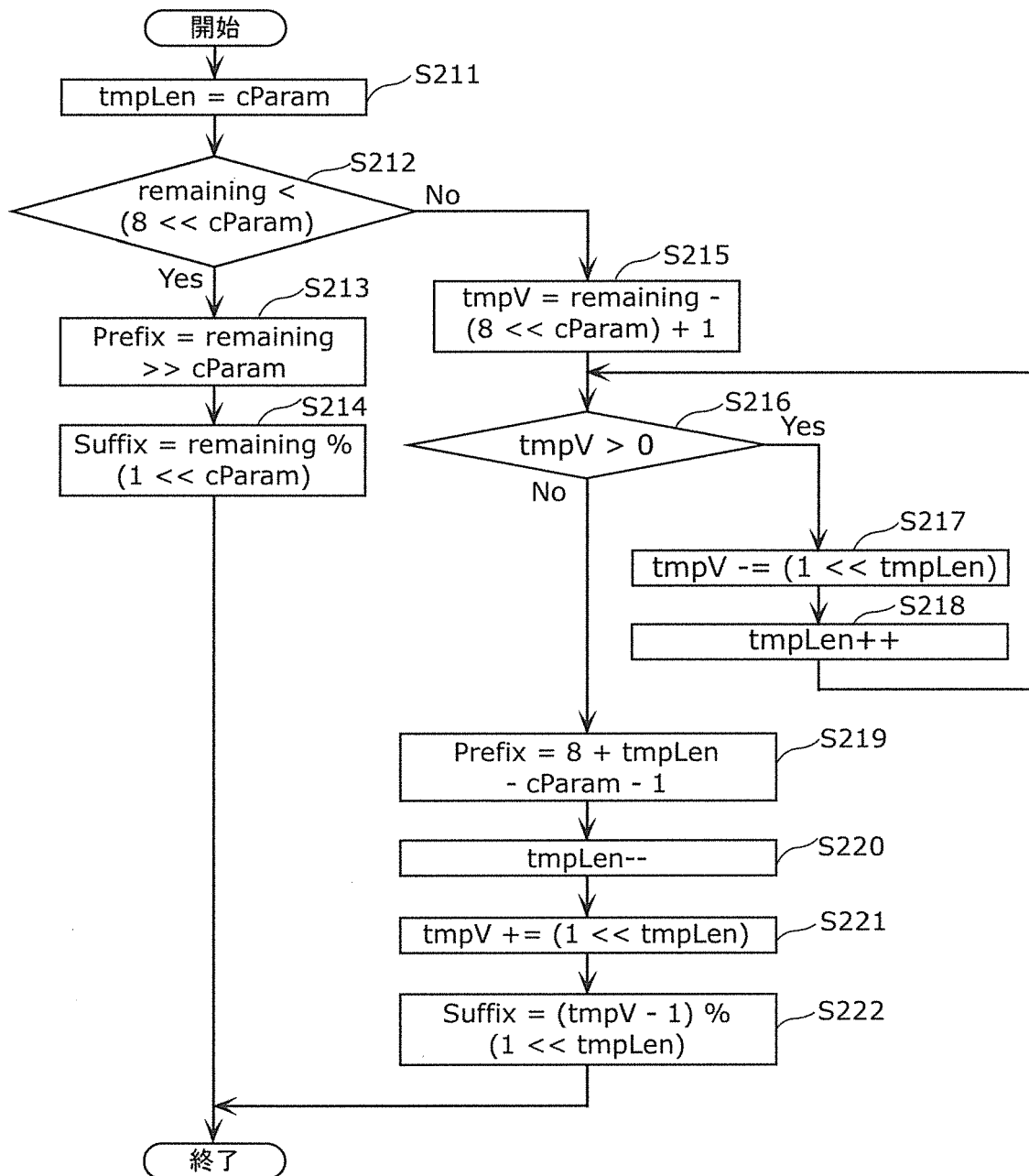
[図12]

コンテキストアセット番号	0			1			2			3						
	0	1	2	3	0	1	2	3	0	1	2	3				
コンテキストアセット	0	1	2	3	0	1	2	3	0	1	2	3				
コンテキストアセット番号	0	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15

[図13]



[図14]



[図15A]

remaining	cParam 0			cParam 1		
	Prefix	Suffix	bin長	Prefix	Suffix	bin長
0	0		1	0	0	2
1	10		2	0	1	2
2	110		3	10	0	3
3	1110		4	10	1	3
4	11110		5	110	0	4
5	111110		6	110	1	4
6	1111110		7	1110	0	5
7	11111110		8	1110	1	5
8	111111110		9	11110	0	6
9	1111111110	0	11	11110	1	6
10	11111111110	1	11	111110	0	7
11	111111111110	00	13	111110	1	7
12	1111111111110	01	13	1111110	0	8
13	11111111111110	10	13	1111110	1	8
14	111111111111110	11	13	11111110	0	9
15	1111111111111110	000	15	11111110	1	9
16	11111111111111110	001	15	111111110	0	10
17	111111111111111110	010	15	111111110	1	10
18	1111111111111111110	011	15	1111111110	00	12
19	11111111111111111110	100	15	1111111110	01	12
20	111111111111111111110	101	15	1111111110	10	12
21	1111111111111111111110	110	15	1111111110	11	12
22	11111111111111111111110	111	15	11111111110	000	14
23	111111111111111111111110	0000	17	11111111110	001	14
24	1111111111111111111111110	0001	17	11111111110	010	14
25	11111111111111111111111110	0010	17	11111111110	011	14
26	111111111111111111111111110	0011	17	11111111110	100	14
27	1111111111111111111111111110	0100	17	11111111110	101	14
28	11111111111111111111111111110	0101	17	11111111110	110	14
29	111111111111111111111111111110	0110	17	11111111110	111	14
30	1111111111111111111111111111110	0111	17	111111111110	0000	16
31	11111111111111111111111111111110	1000	17	111111111110	0001	16
32	111111111111111111111111111111110	1001	17	111111111110	0010	16
33	1111111111111111111111111111111110	1010	17	111111111110	0011	16
34	11111111111111111111111111111111110	1011	17	111111111110	0100	16
35	111111111111111111111111111111111110	1100	17	111111111110	0101	16
36	1111111111111111111111111111111111110	1101	17	111111111110	0110	16
37	11111111111111111111111111111111111110	1110	17	111111111110	0111	16
38	111111111111111111111111111111111111110	1111	17	111111111110	1000	16
39	1111111111111111111111111111111111111110	00000	19	111111111110	1001	16
40	110	00001	19	111111111110	1010	16
41	1110	00010	19	111111111110	1011	16
42	110	00011	19	111111111110	1100	16
43	1110	00100	19	111111111110	1101	16
44	110	00101	19	111111111110	1110	16
45	1110	00110	19	111111111110	1111	16
46	110	00111	19	1111111111110	00000	18
47	1110	01000	19	1111111111110	00001	18
48	110	01001	19	1111111111110	00010	18

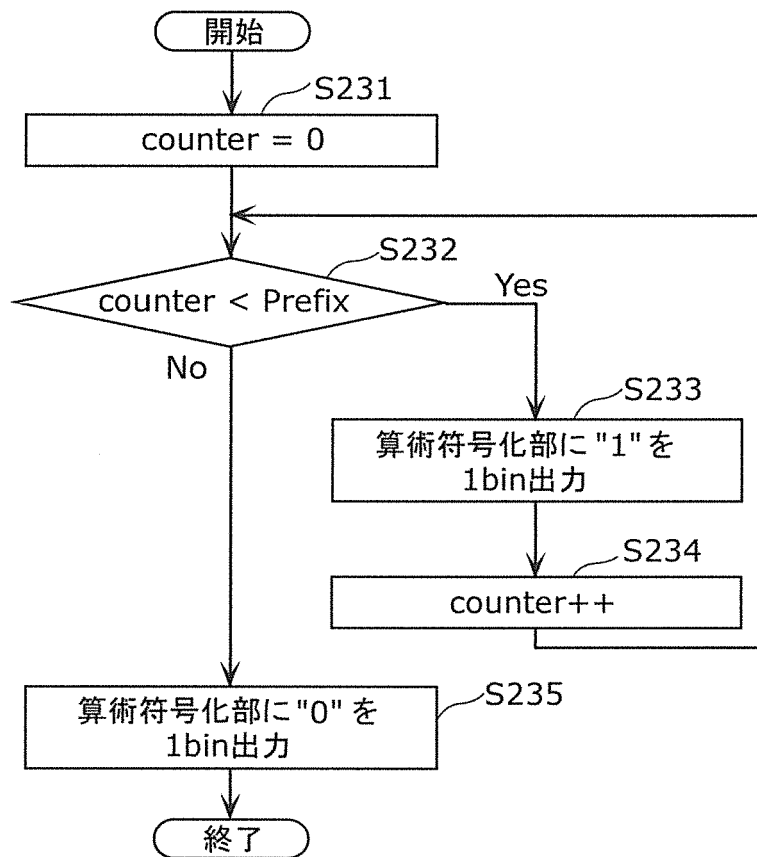
⋮

remaining	cParam 2			cParam 3		
	Prefix	Suffix	bin長	Prefix	Suffix	bin長
0	0	00	3	0	000	4
1	0	01	3	0	001	4
2	0	10	3	0	010	4
3	0	11	3	0	011	4
4	10	00	4	0	100	4
5	10	01	4	0	101	4
6	10	10	4	0	110	4
7	10	11	4	0	111	4
8	110	00	5	10	000	5
9	110	01	5	10	001	5
10	110	10	5	10	010	5
11	110	11	5	10	011	5
12	1110	00	6	10	100	5
13	1110	01	6	10	101	5
14	1110	10	6	10	110	5
15	1110	11	6	10	111	5
16	11110	00	7	110	000	6
17	11110	01	7	110	001	6
18	11110	10	7	110	010	6
19	11110	11	7	110	011	6
20	111110	00	8	110	100	6
21	111110	01	8	110	101	6
22	111110	10	8	110	110	6
23	111110	11	8	110	111	6
24	1111110	00	9	1110	000	7
25	1111110	01	9	1110	001	7
26	1111110	10	9	1110	010	7
27	1111110	11	9	1110	011	7
28	11111110	00	10	1110	100	7
29	11111110	01	10	1110	101	7
30	11111110	10	10	1110	110	7
31	11111110	11	10	1110	111	7
32	111111110	00	11	11110	000	8
33	111111110	01	11	11110	001	8
34	111111110	10	11	11110	010	8
35	111111110	11	11	11110	011	8
36	1111111110	000	13	11110	100	8
37	1111111110	001	13	11110	101	8
38	1111111110	010	13	11110	110	8
39	1111111110	011	13	11110	111	8
40	11111111110	100	13	111110	000	9
41	11111111110	101	13	111110	001	9
42	11111111110	110	13	111110	010	9
43	11111111110	111	13	111110	011	9
44	111111111110	0000	15	1111110	100	9
45	111111111110	0001	15	1111110	101	9
46	111111111110	0010	15	1111110	110	9
47	111111111110	0011	15	1111110	111	9
48	1111111111110	0100	15	11111110	100	10

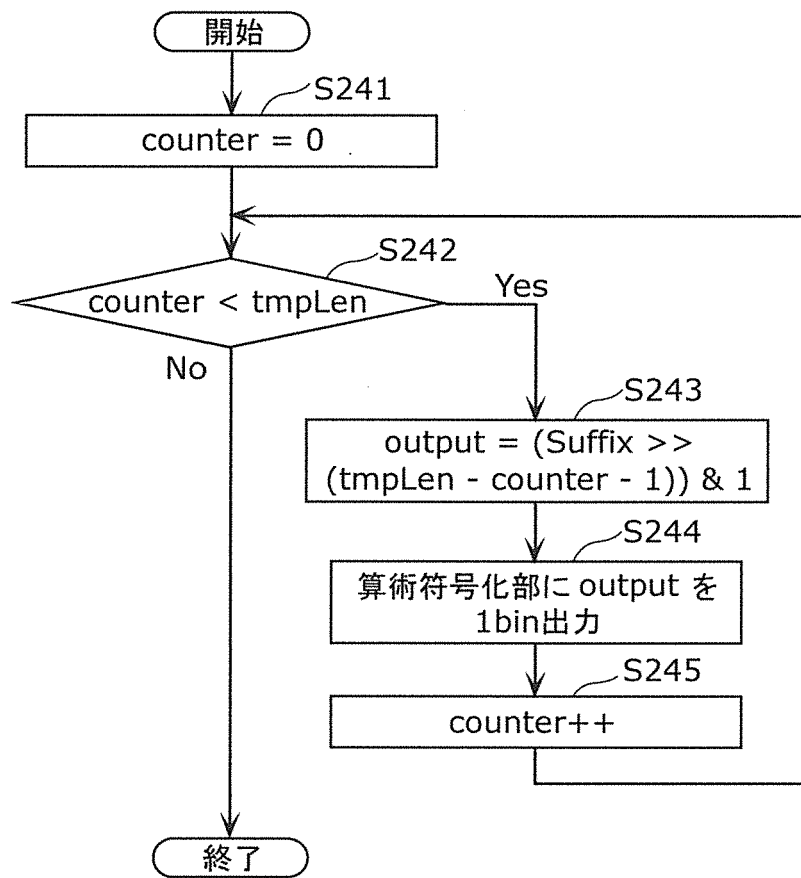
[図15C]

remaining	cParam 4		
	Prefix	Suffix	bin長
0	0	0000	5
1	0	0001	5
2	0	0010	5
3	0	0011	5
4	0	0100	5
5	0	0101	5
6	0	0110	5
7	0	0111	5
8	0	1000	5
9	0	1001	5
10	0	1010	5
11	0	1011	5
12	0	1100	5
13	0	1101	5
14	0	1110	5
15	0	1111	5
16	10	0000	6
17	10	0001	6
18	10	0010	6
19	10	0011	6
20	10	0100	6
21	10	0101	6
22	10	0110	6
23	10	0111	6
24	10	1000	6
25	10	1001	6
26	10	1010	6
27	10	1011	6
28	10	1100	6
29	10	1101	6
30	10	1110	6
31	10	1111	6
32	110	0000	7
33	110	0001	7
34	110	0010	7
35	110	0011	7
36	110	0100	7
37	110	0101	7
38	110	0110	7
39	110	0111	7
40	110	1000	7
41	110	1001	7
42	110	1010	7
43	110	1011	7
44	110	1100	7
45	110	1101	7
46	110	1110	7
47	110	1111	7
48	1110	0000	8
⋮			

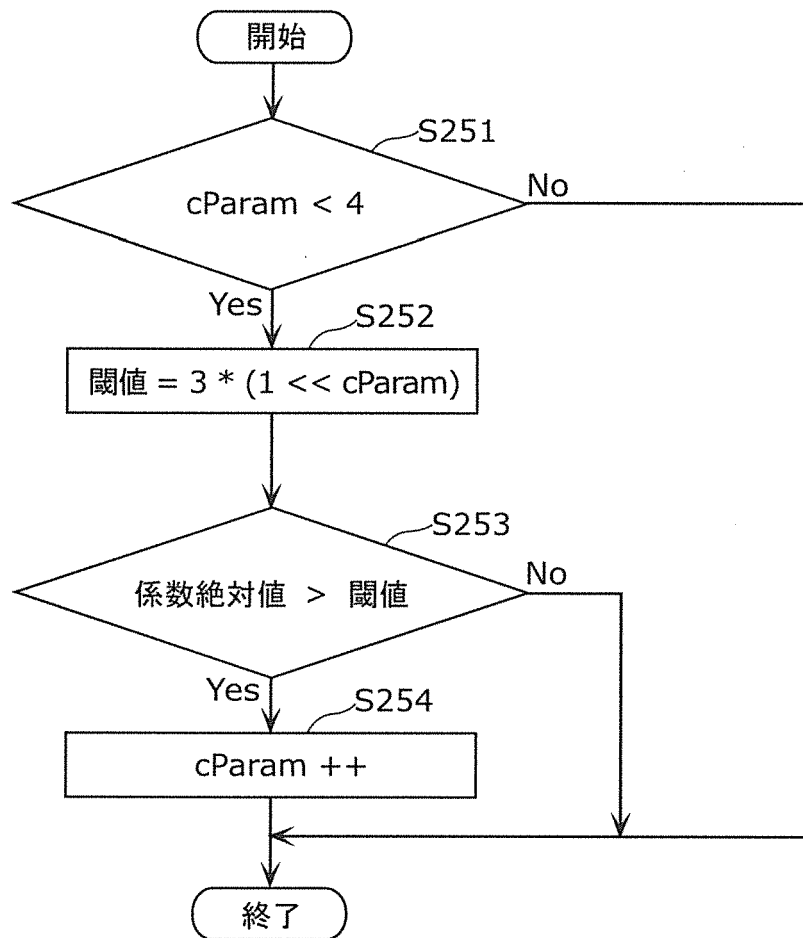
[図16]



[図17]



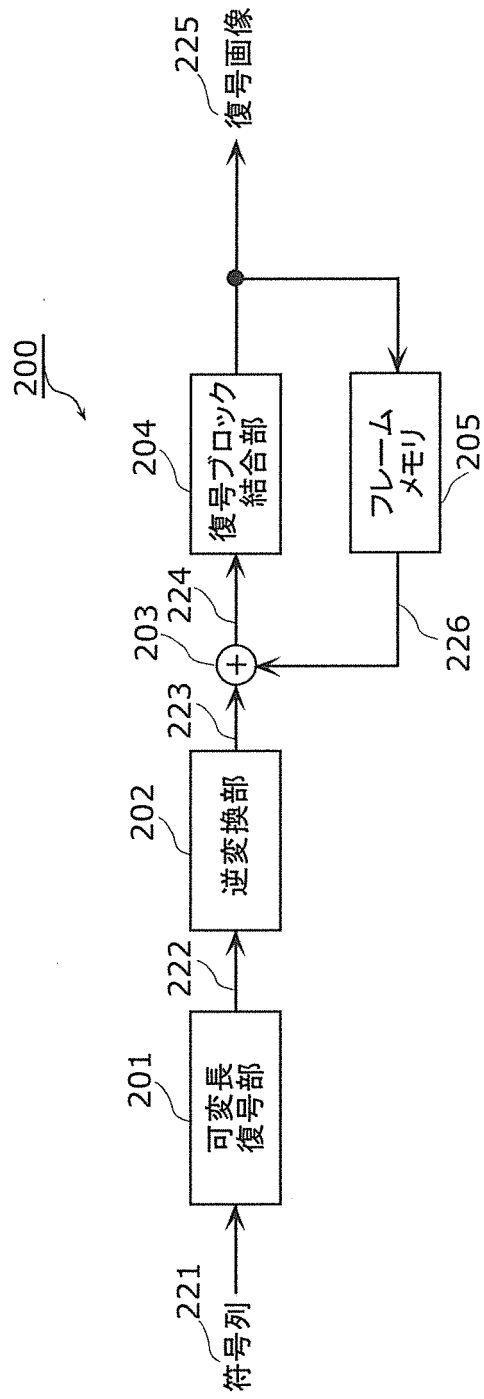
[図18]



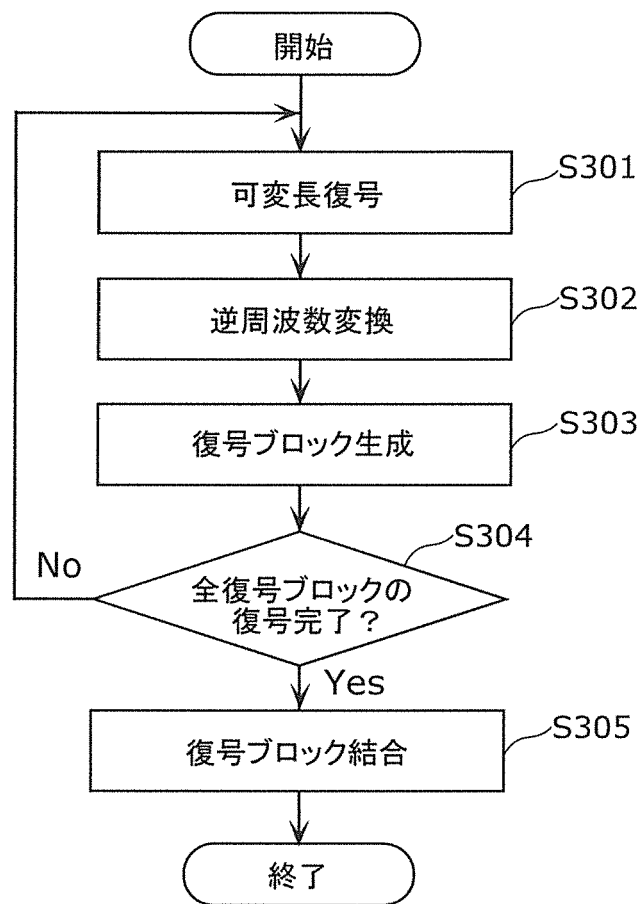
[図19]

	Main			HE10		
	AI	RA	LB	AI	RA	LB
実施の形態1	0.03	0.02	-0.01	0.03	0.03	-0.01

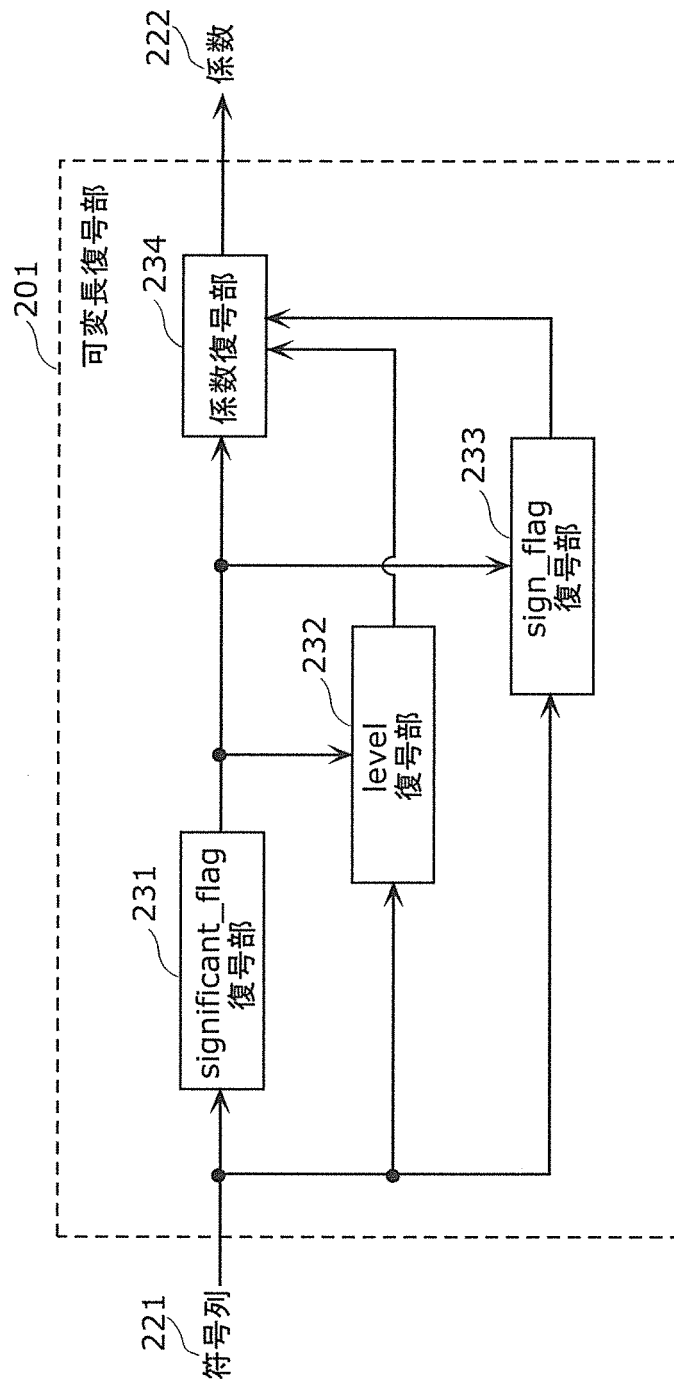
[図20]



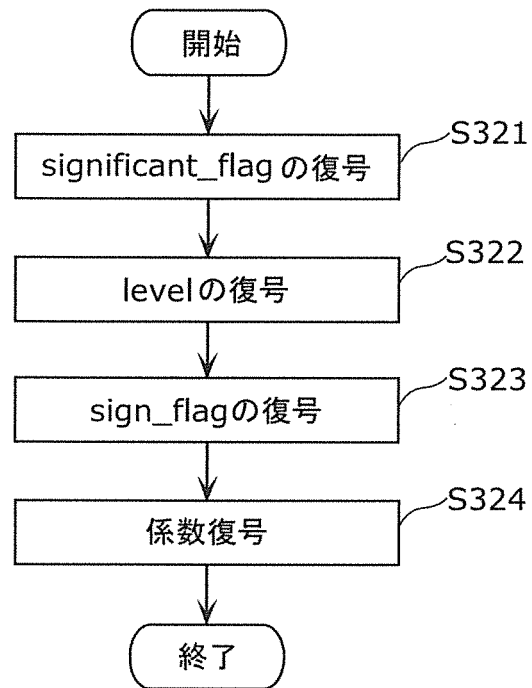
[図21]



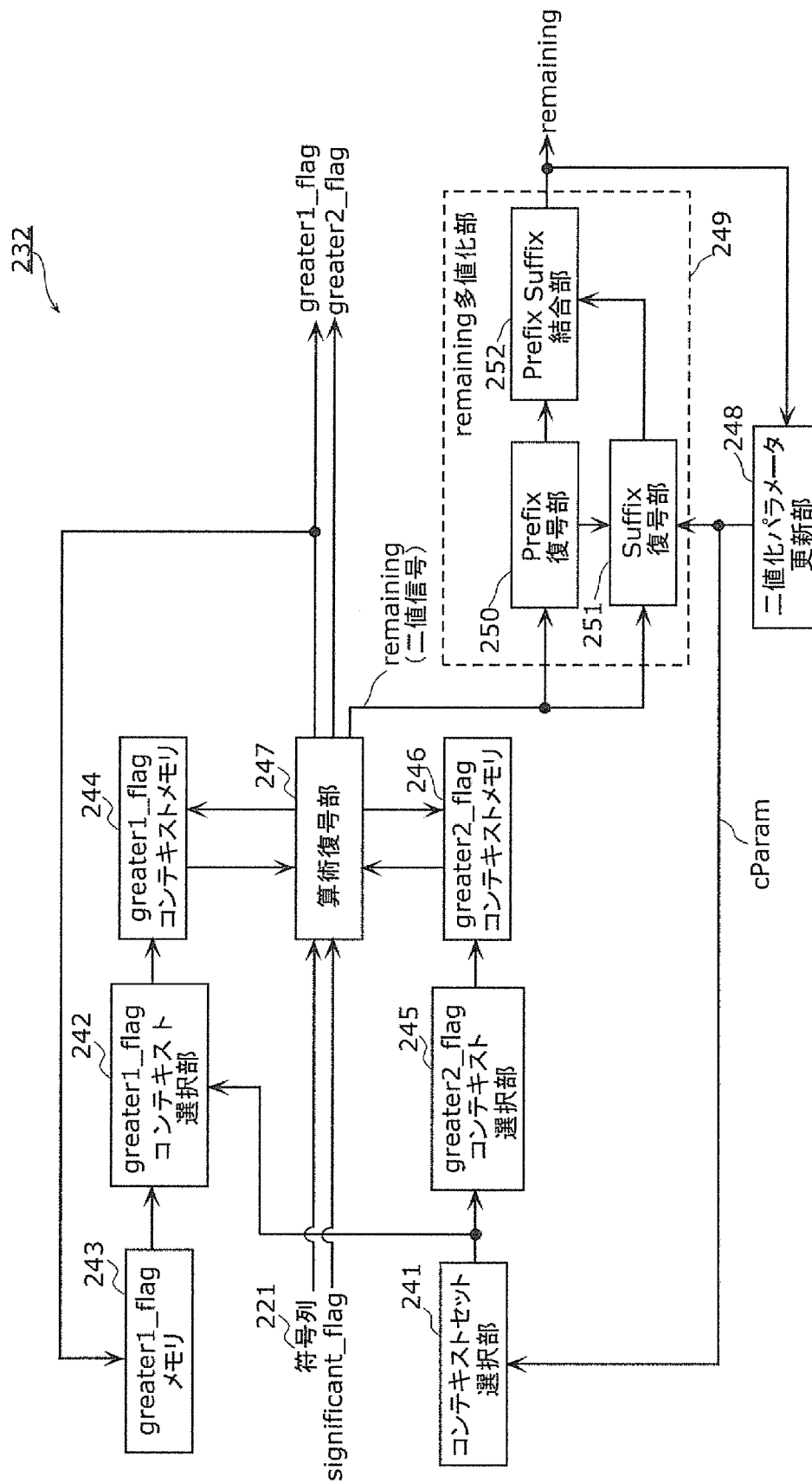
[図22]



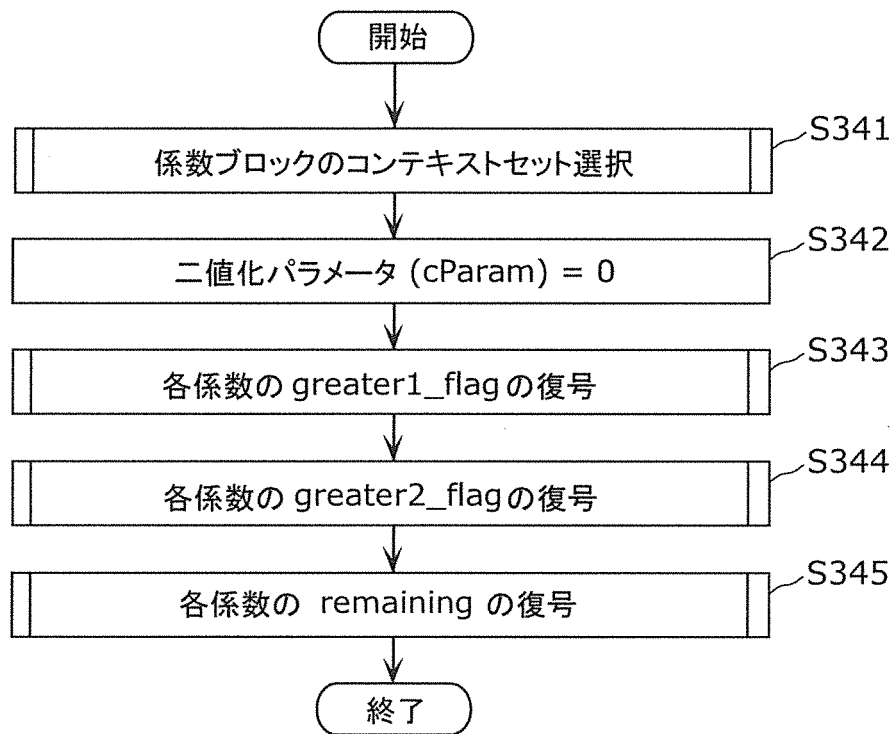
[図23]



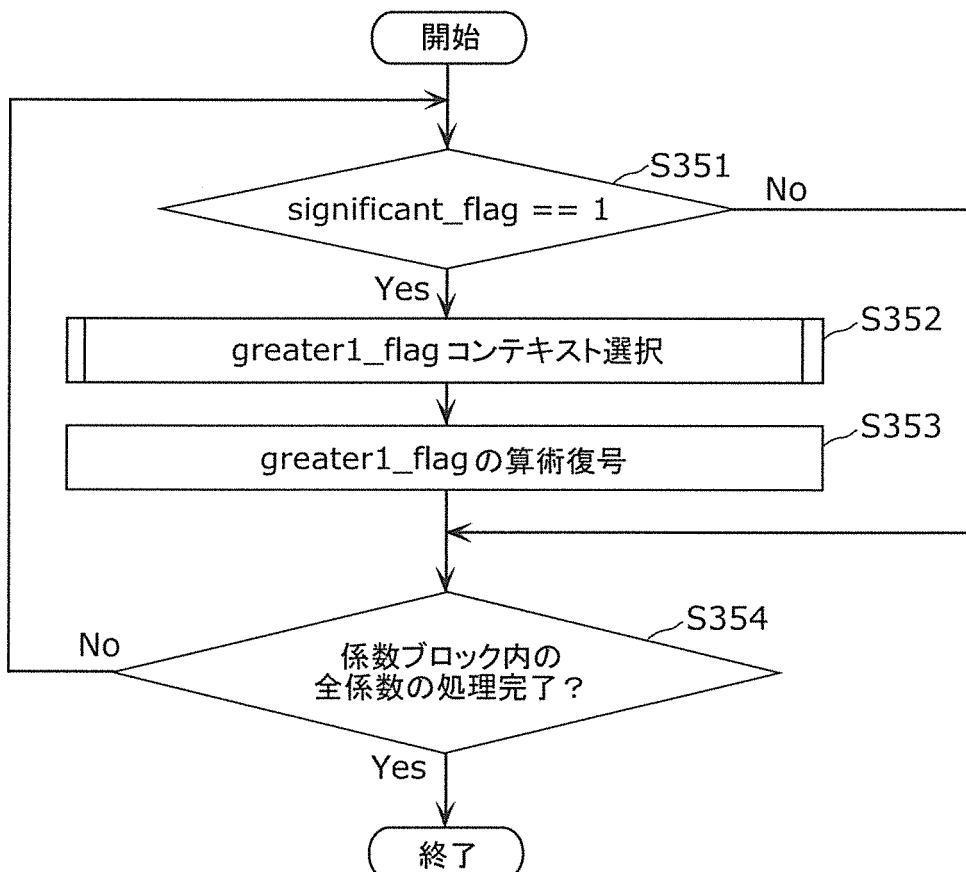
[図24]



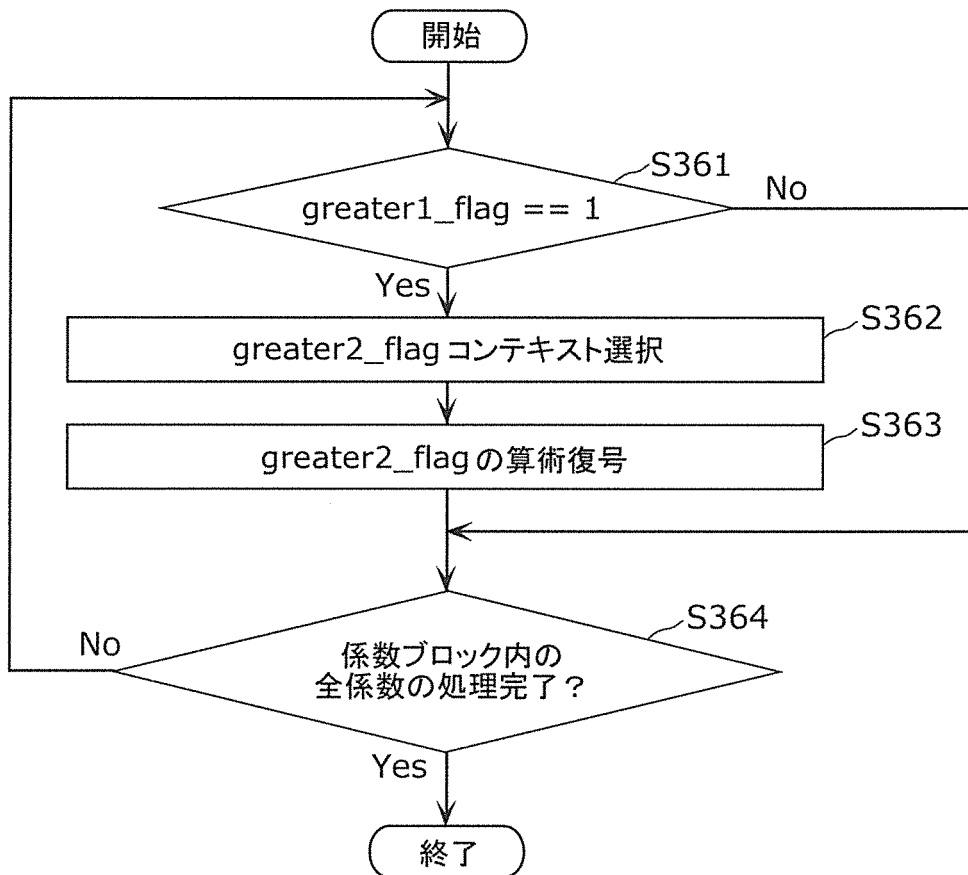
[図25]



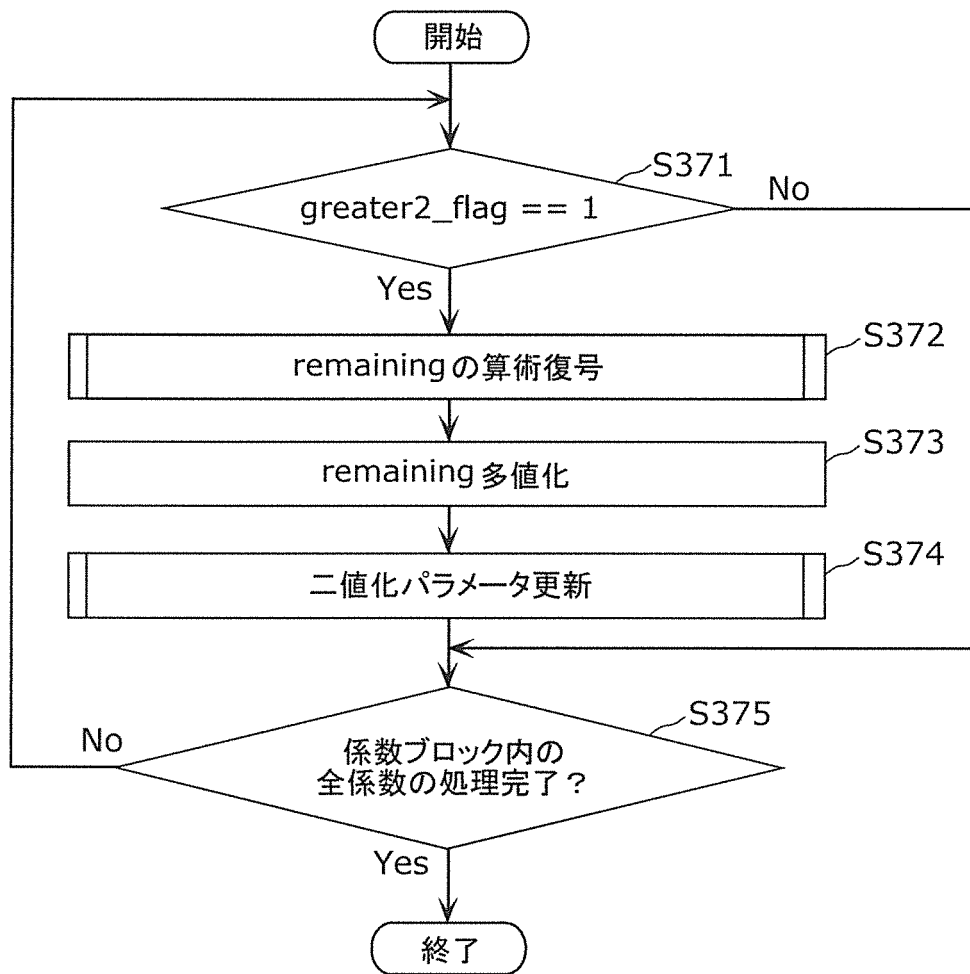
[図26]



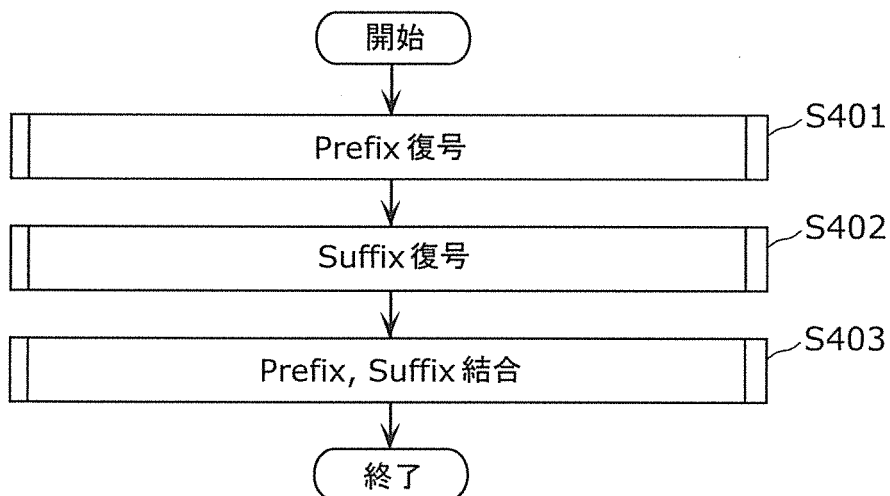
[図27]



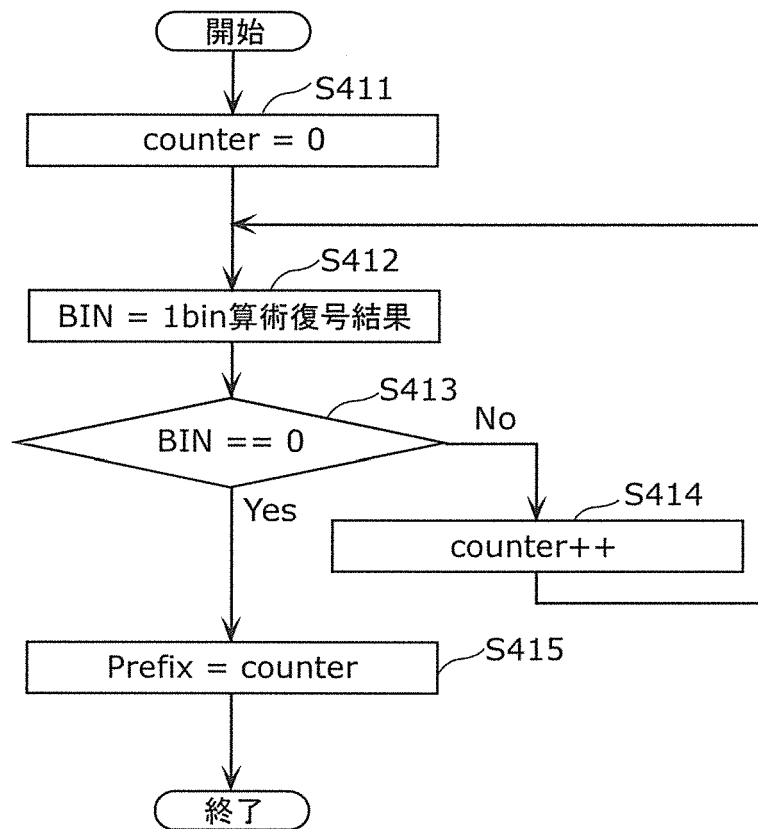
[図28]



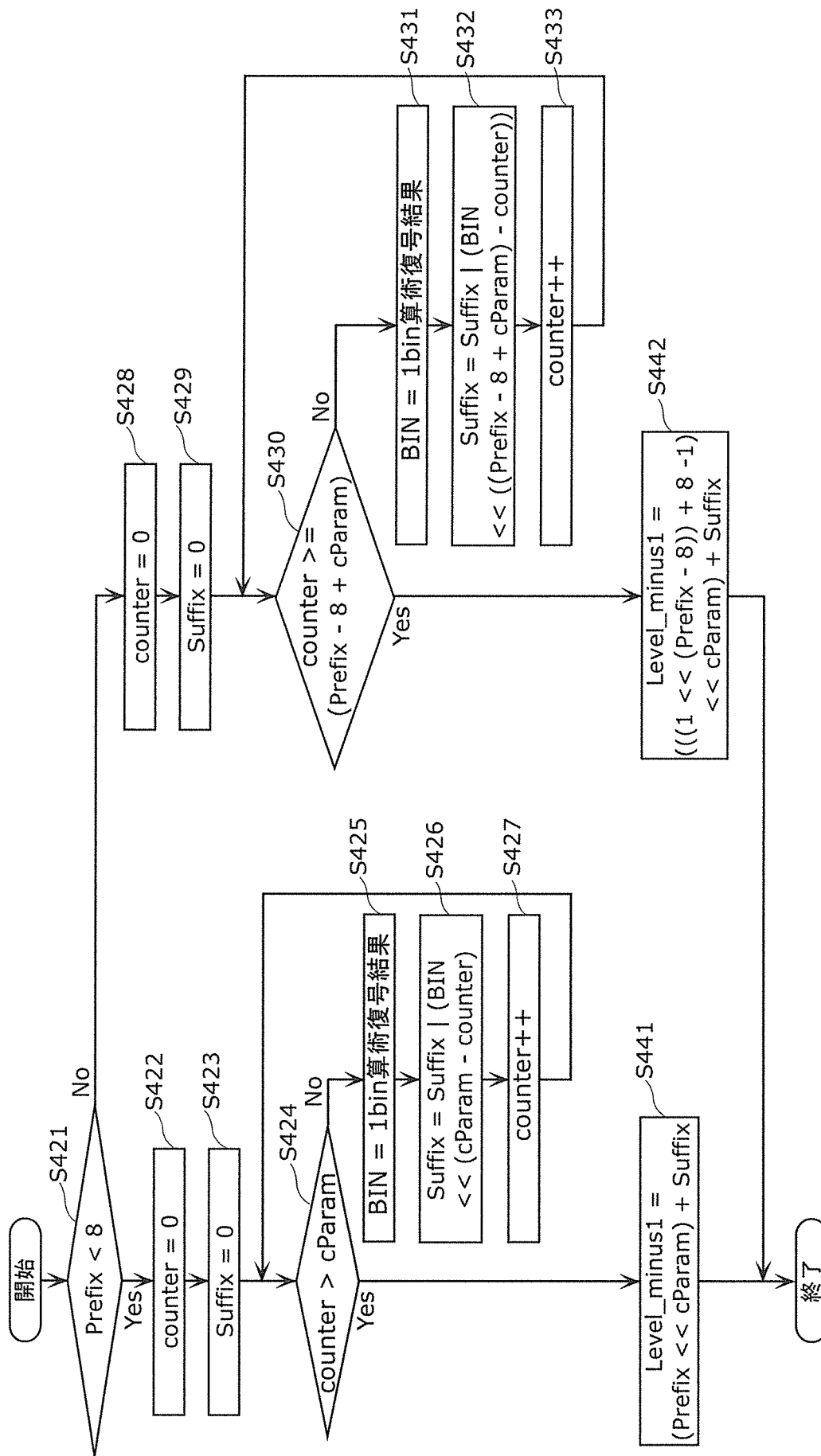
[図29]



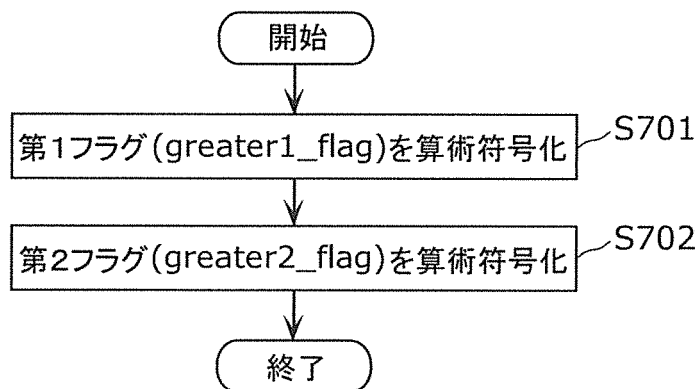
[図30]



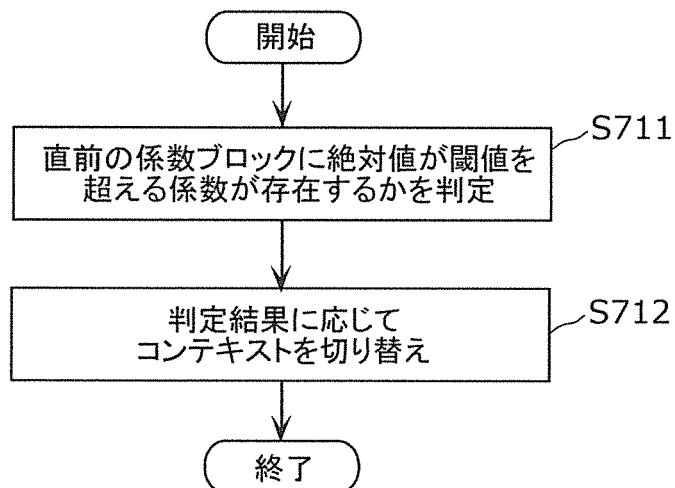
[図31]



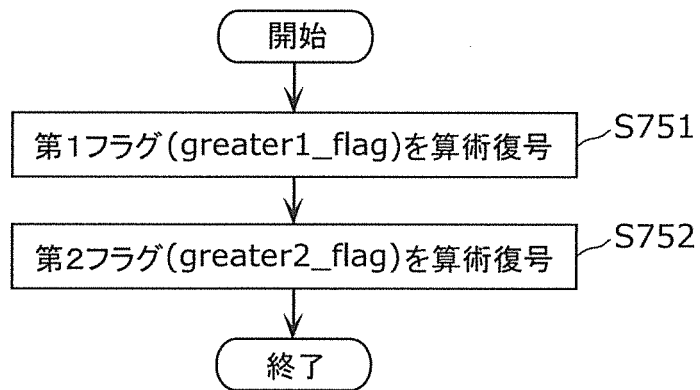
[図32A]



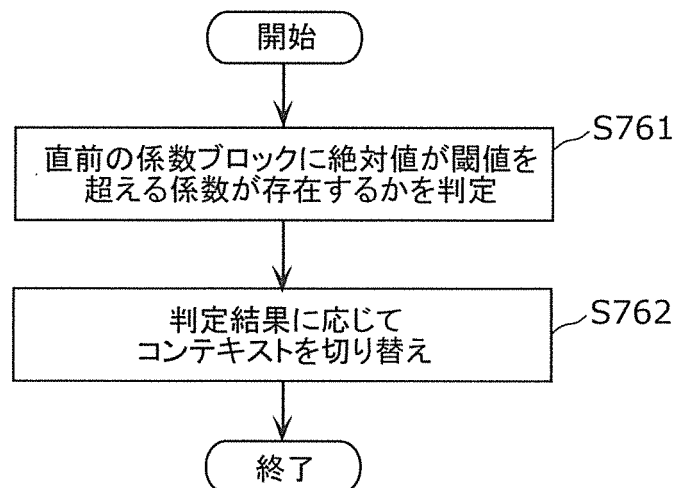
[図32B]



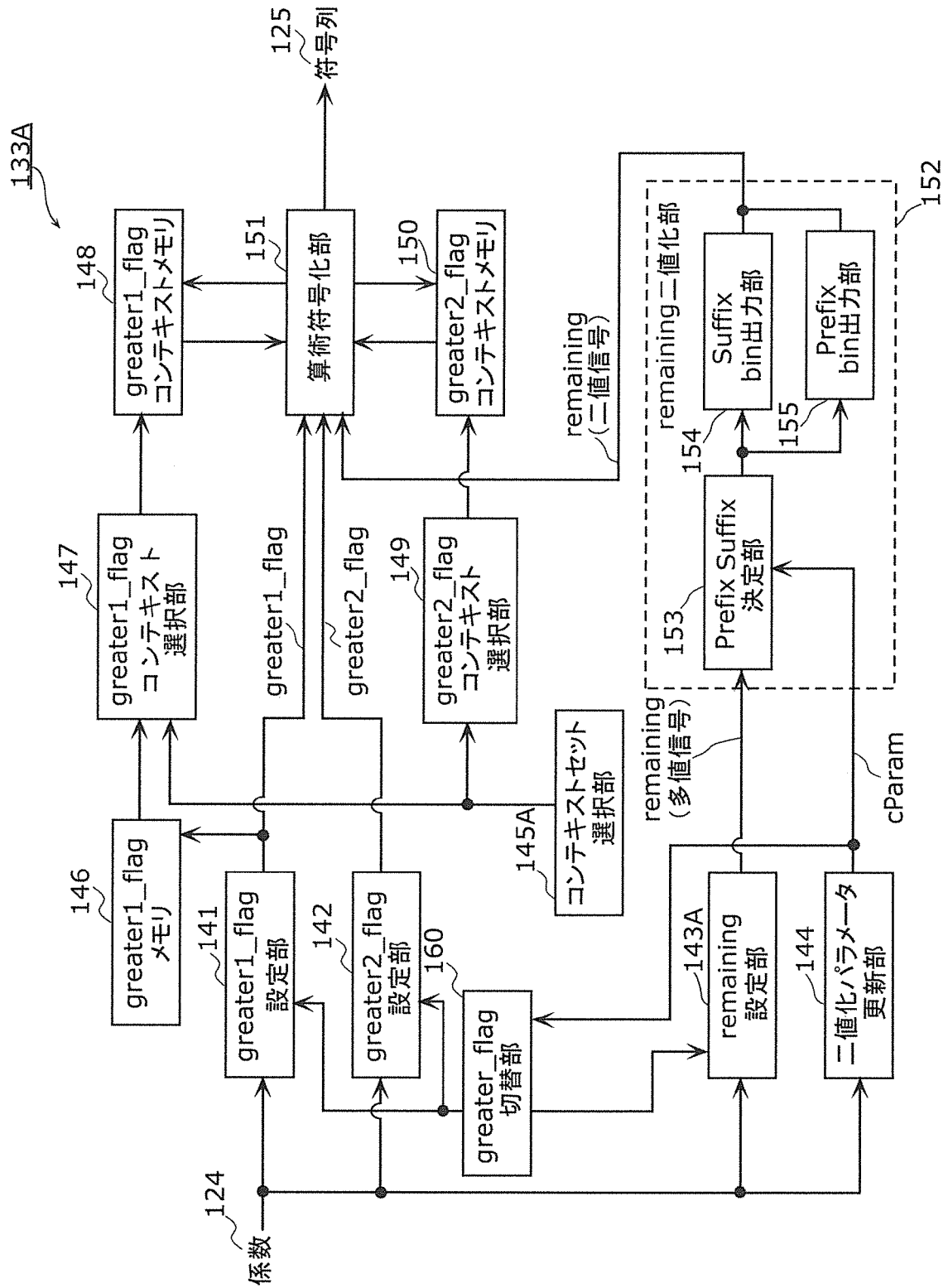
[図33A]



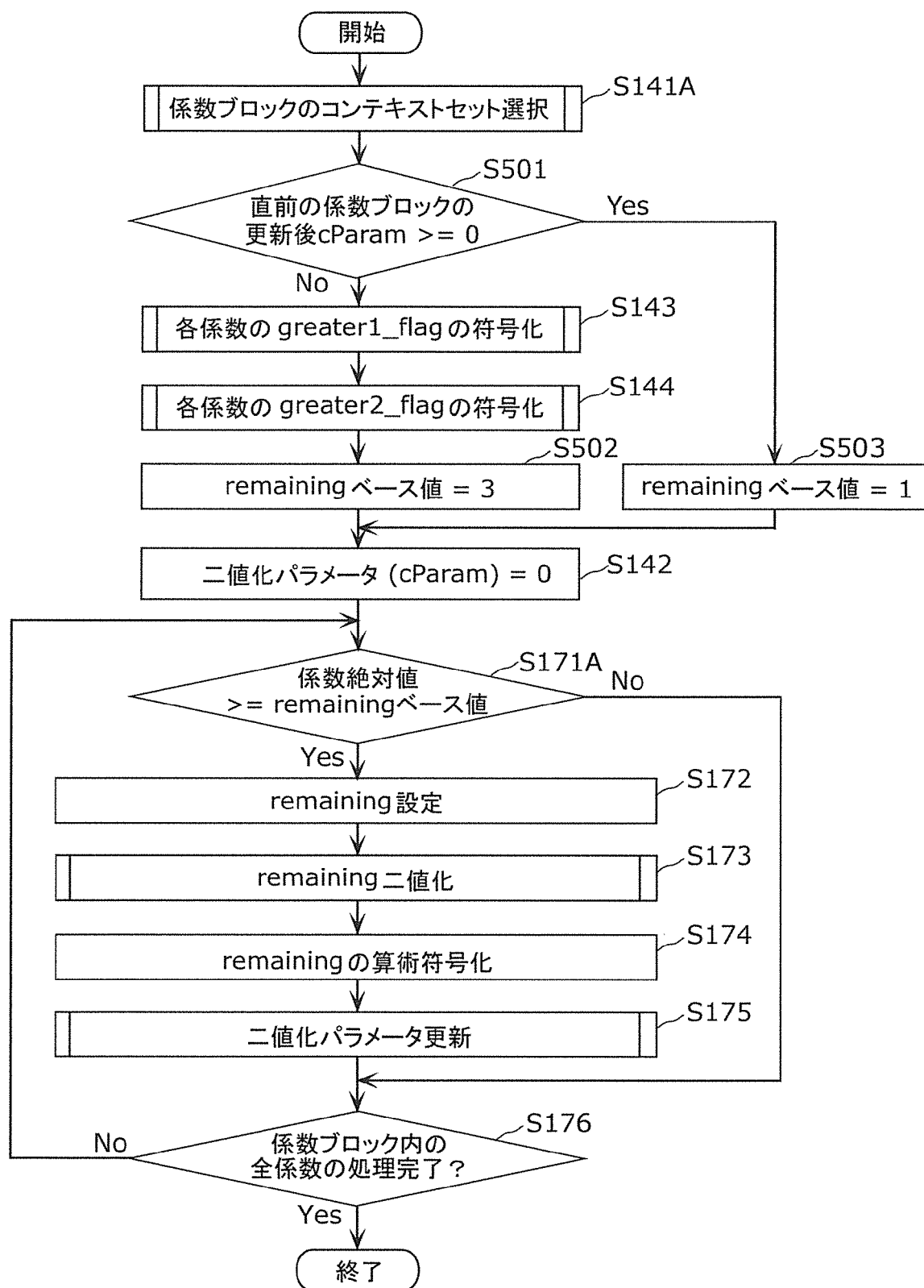
[図33B]



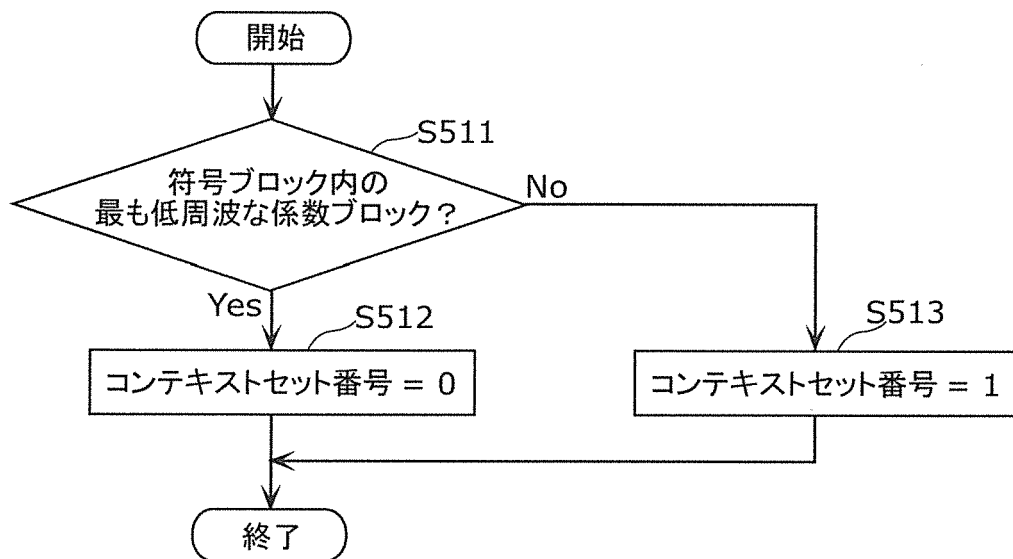
[図34]



[図35]



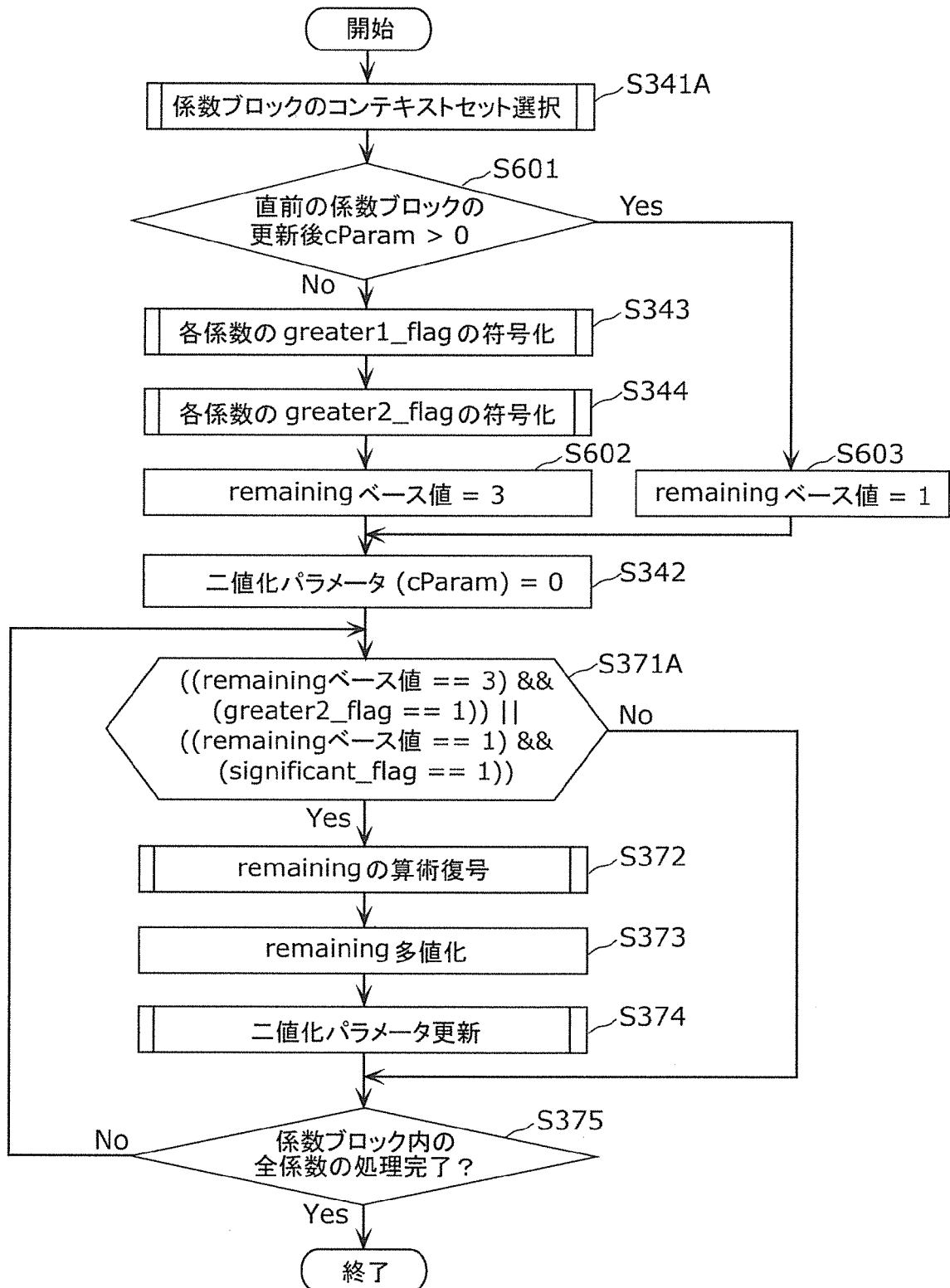
[図36]



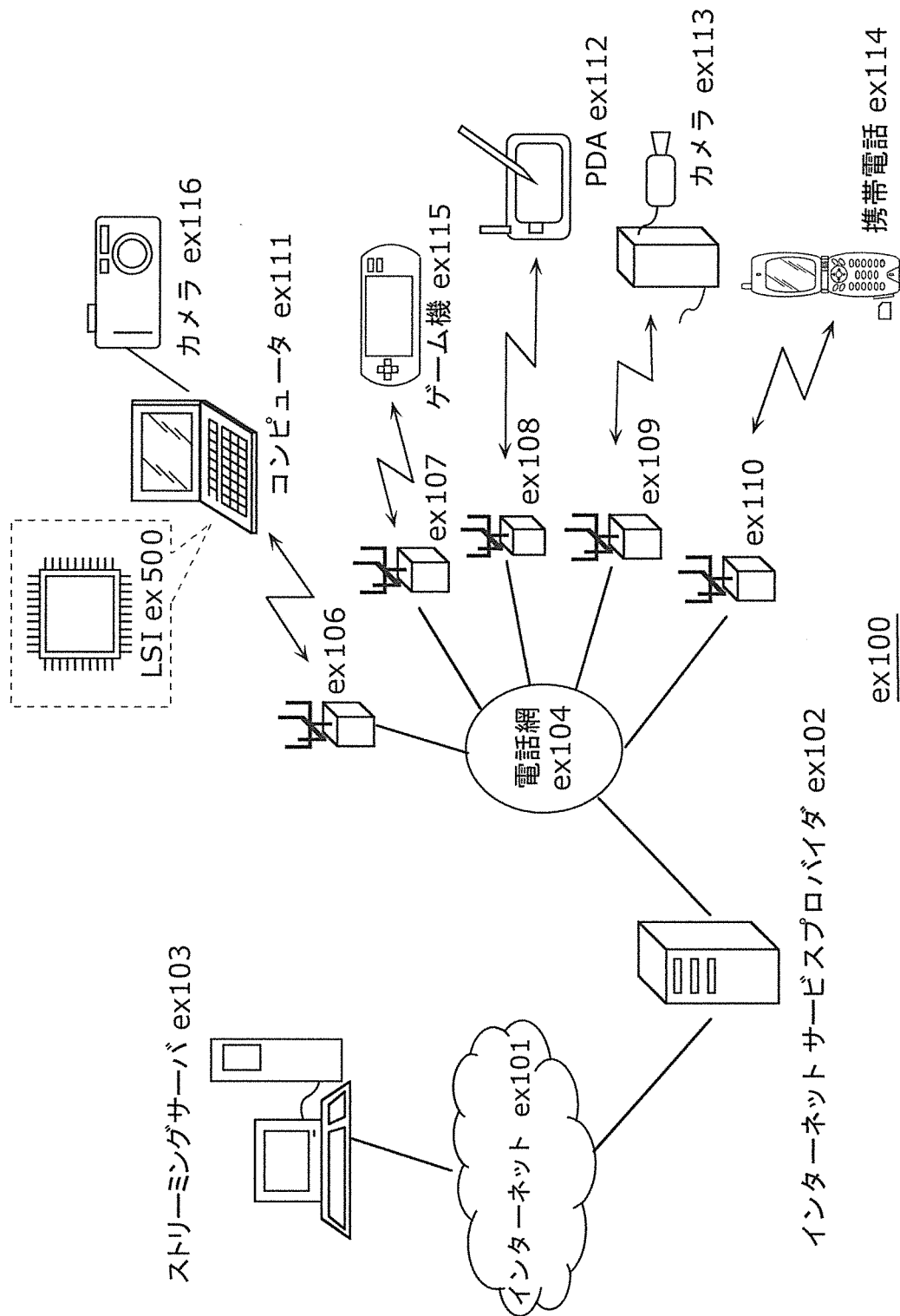
[図37]

	Main			HE10		
	AI	RA	LB	AI	RA	LB
実施の形態3	0.06	0.04	0.00	0.04	0.01	0.00

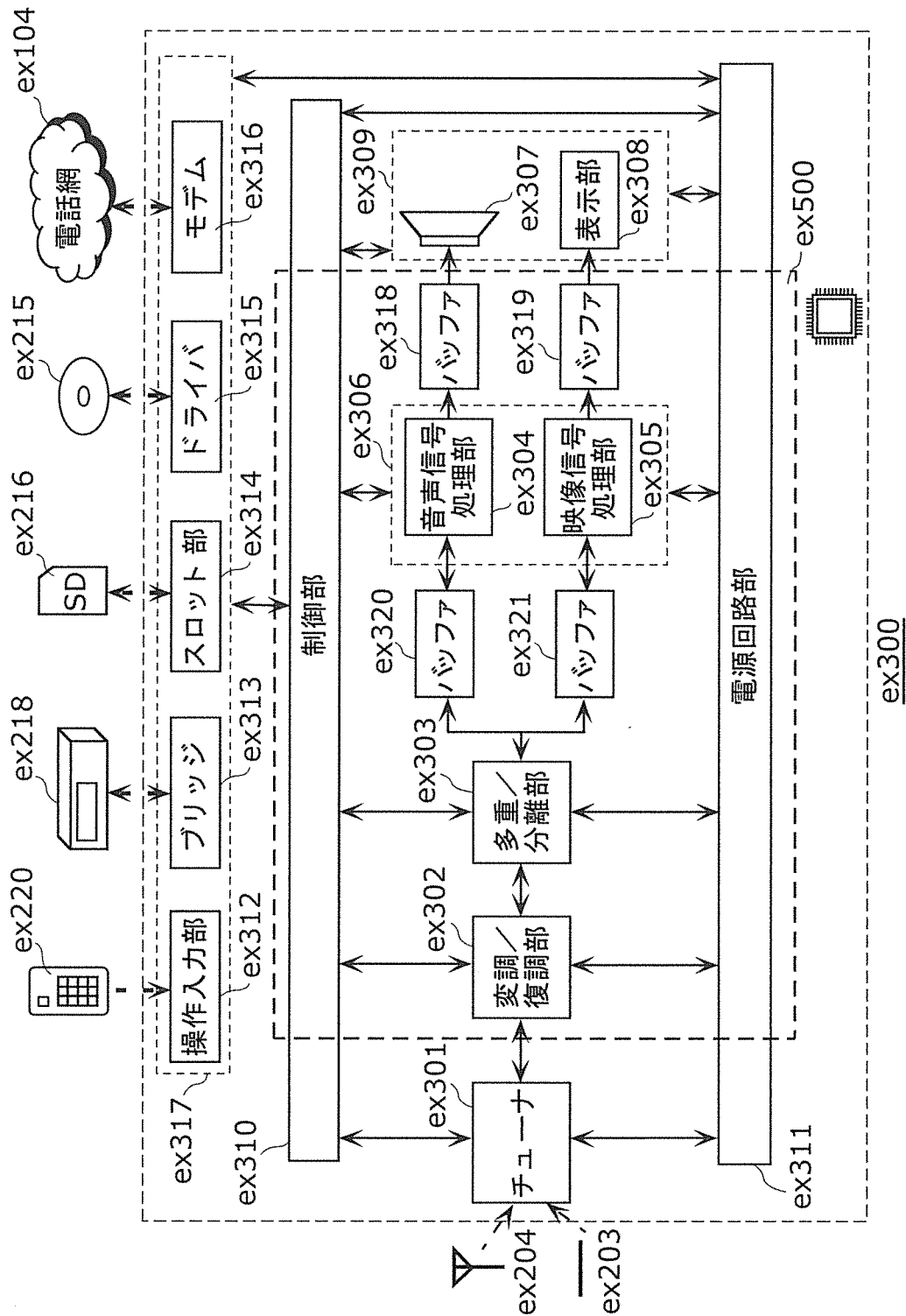
[図39]



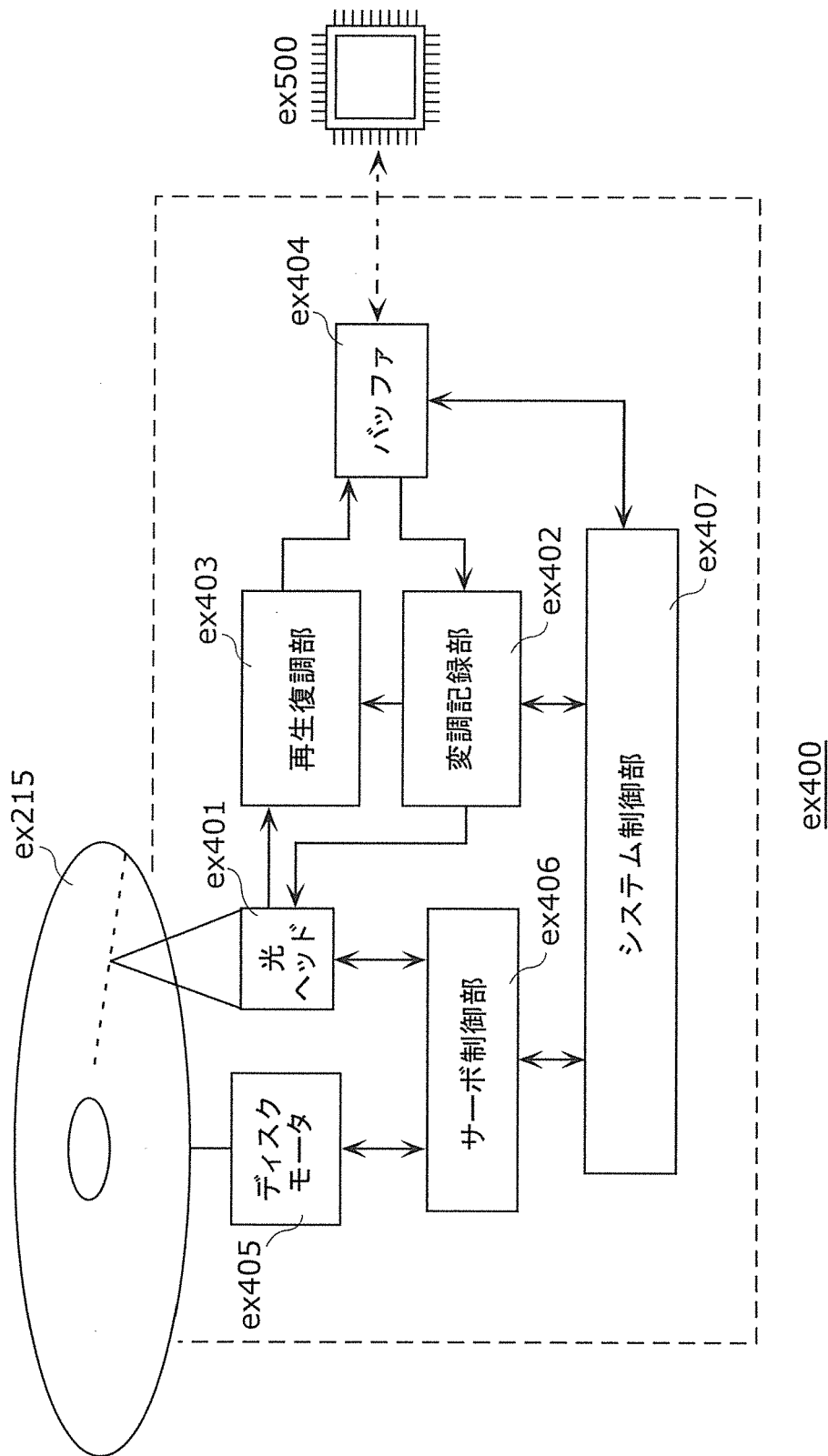
[図40]



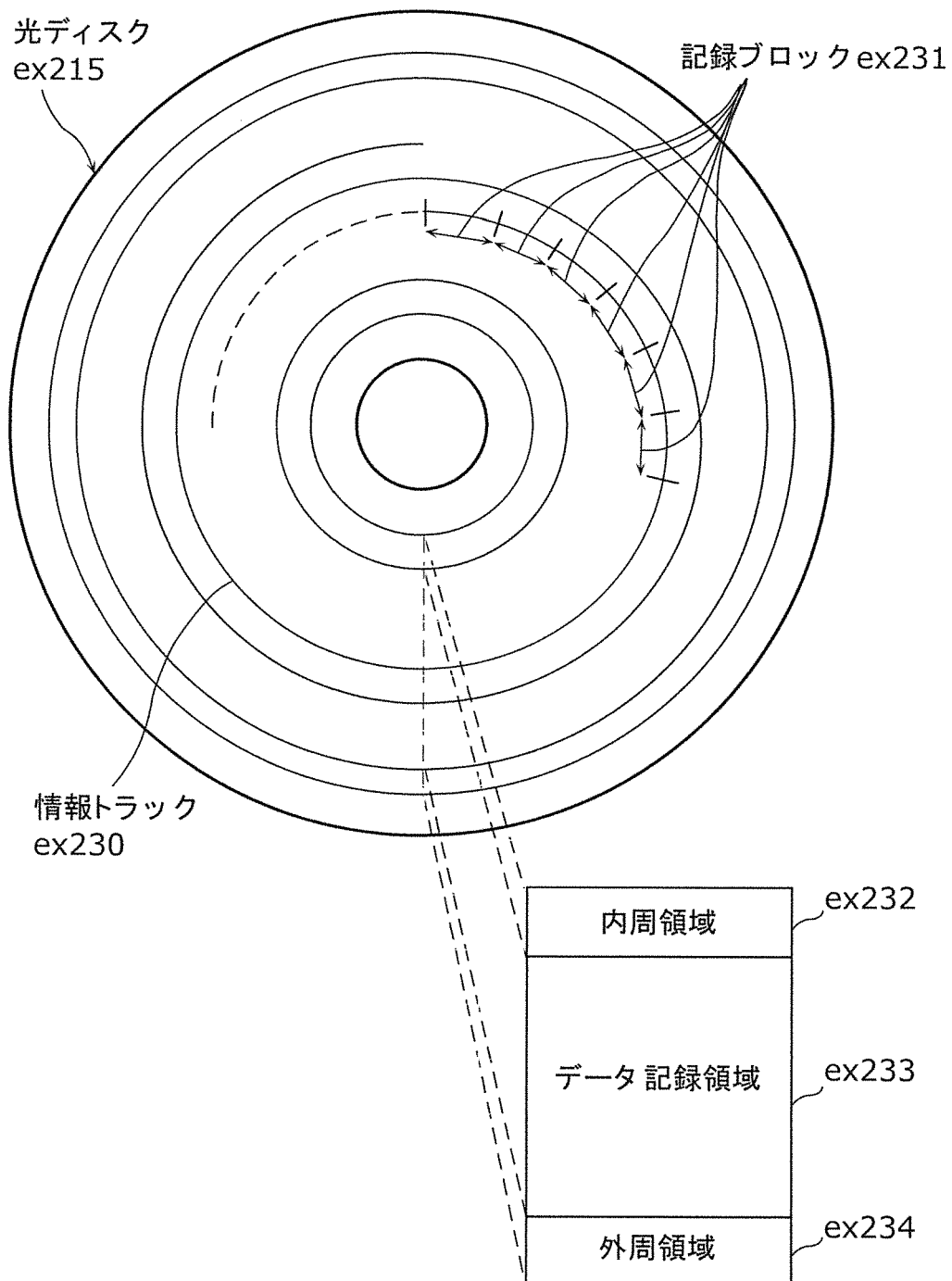
[図42]



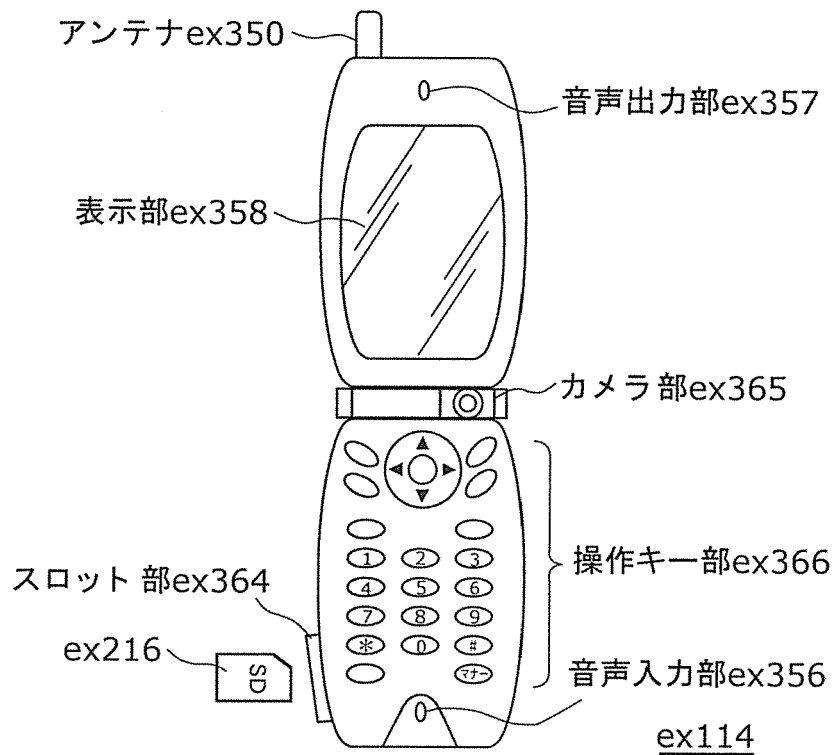
[図43]



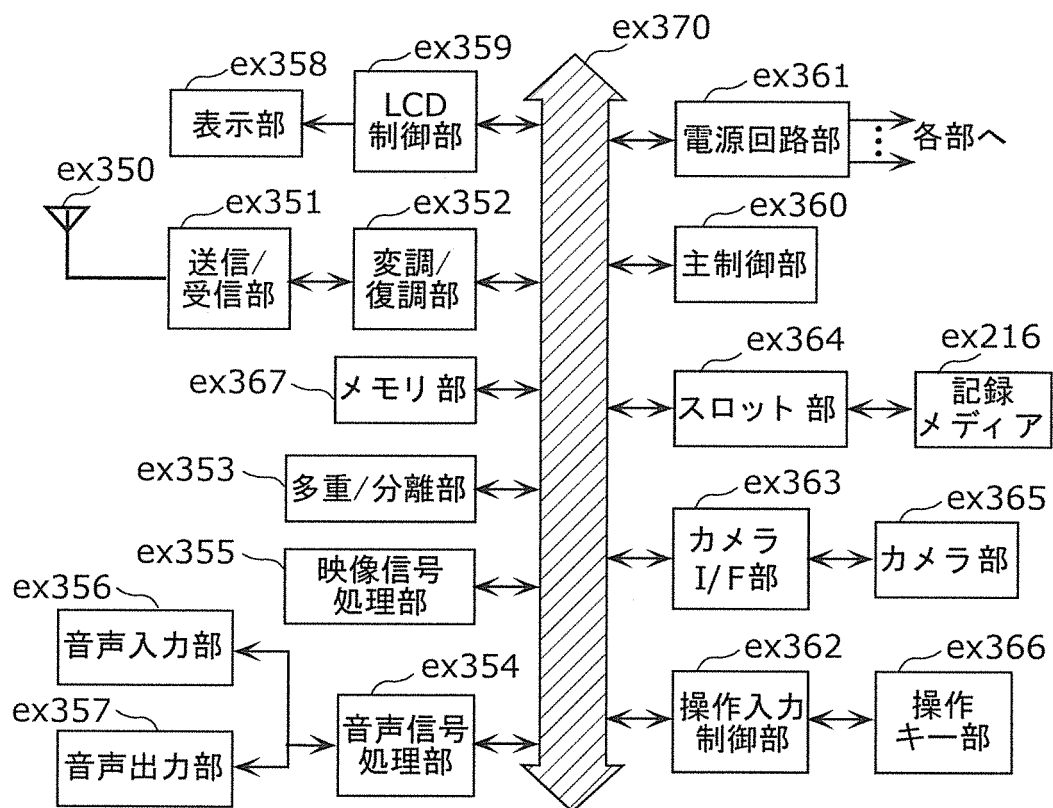
[図44]



[図45A]



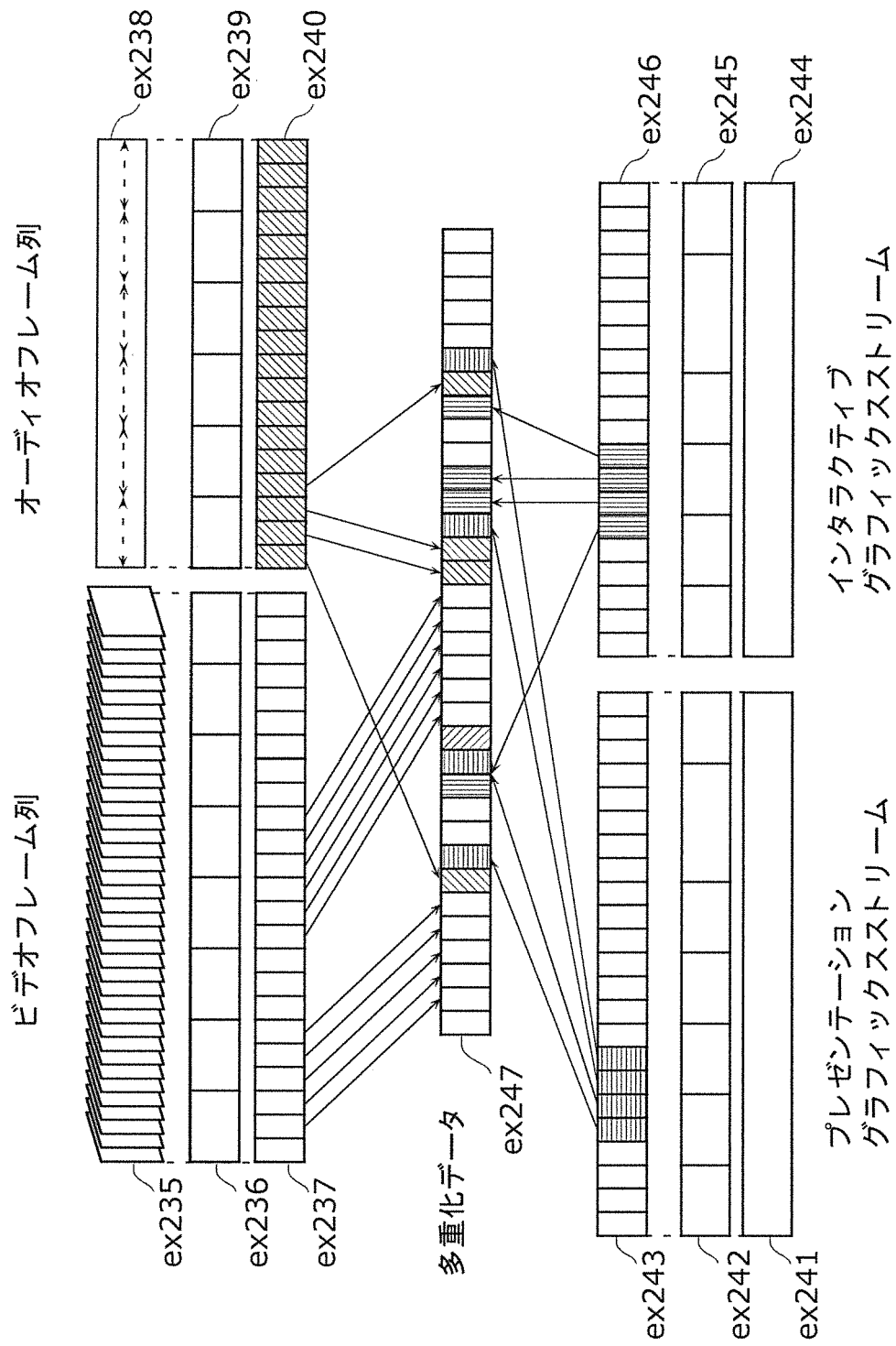
[図45B]



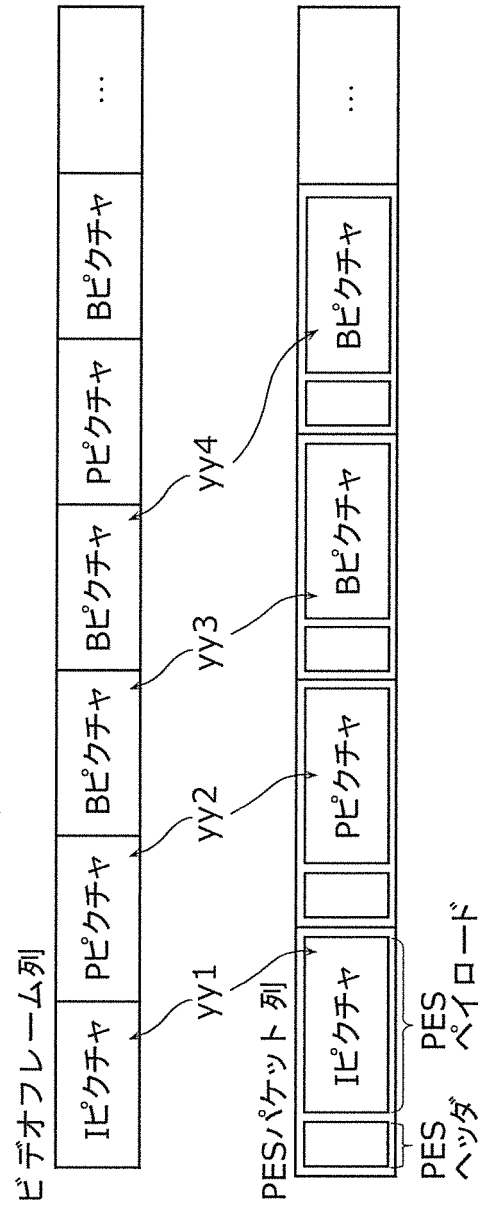
[図46]

ビデオストリーム(PID=0x1011 主映像)
オーディオストリーム(PID=0x1100)
オーディオストリーム(PID=0x1101)
プレゼンテーショングラフィックスストリーム(PID=0x1200)
プレゼンテーショングラフィックスストリーム(PID=0x1201)
インタラクティブグラフィックスストリーム(PID=0x1400)
ビデオストリーム(PID=0x1B00 副映像)
ビデオストリーム(PID=0x1B01 副映像)

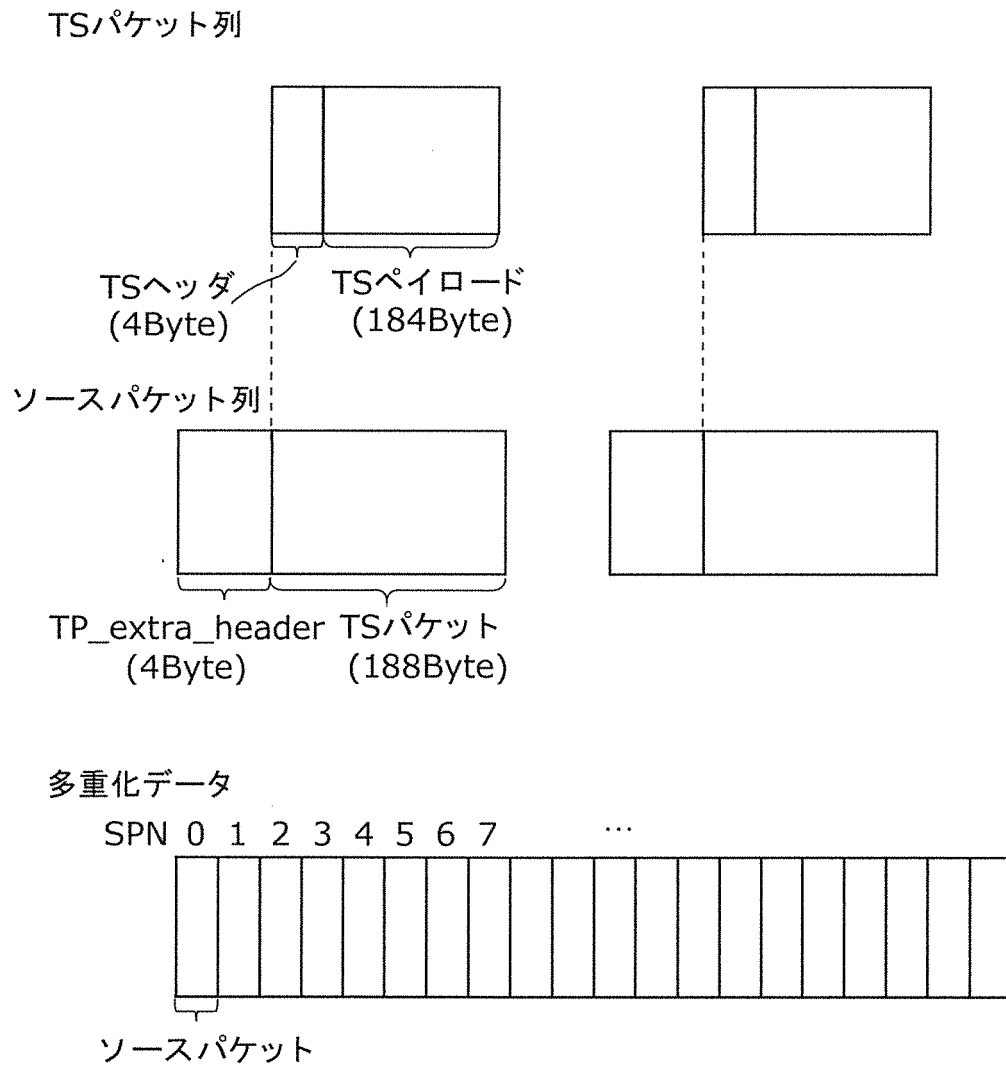
[図47]



[図48]

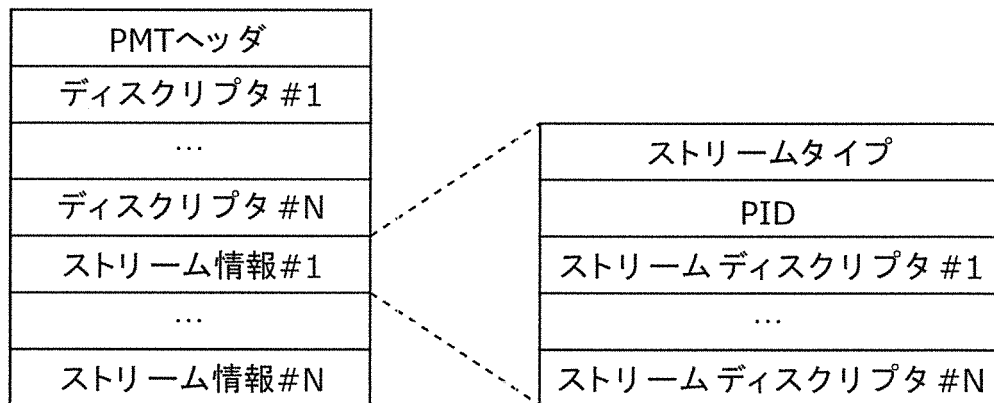


[図49]

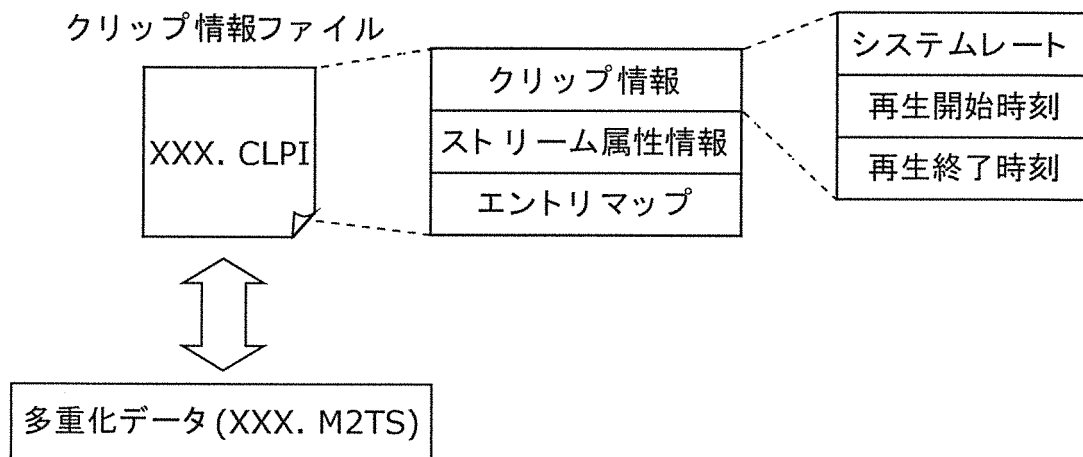


[図50]

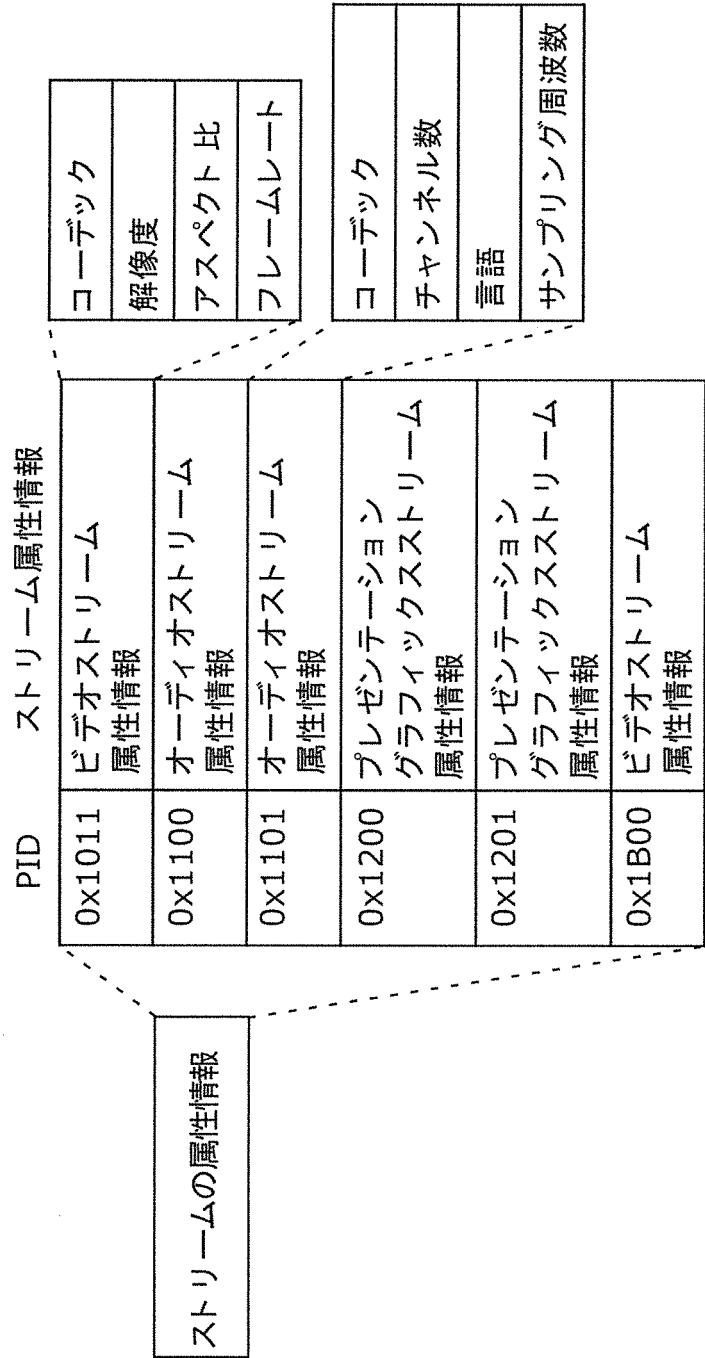
PMTのデータ構造



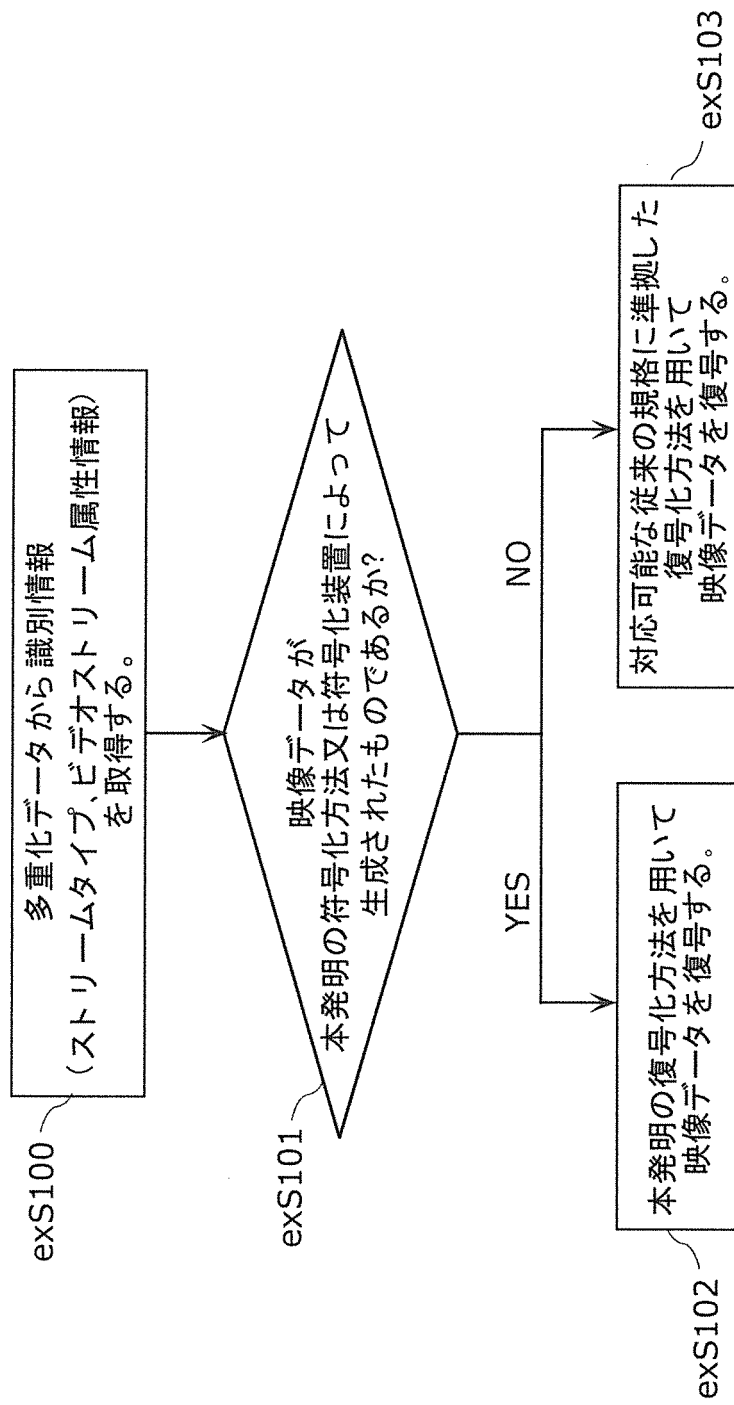
[図51]



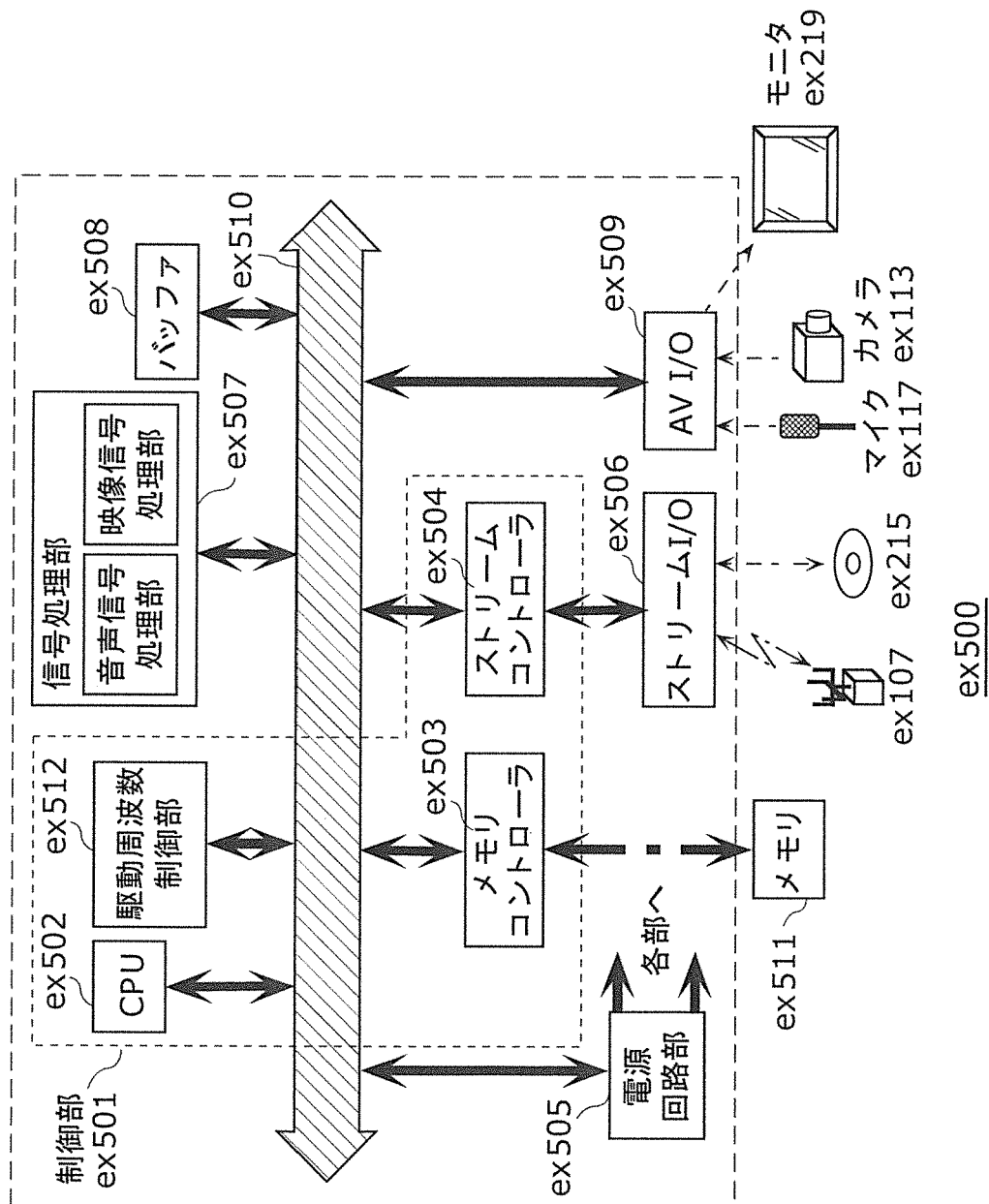
[図52]



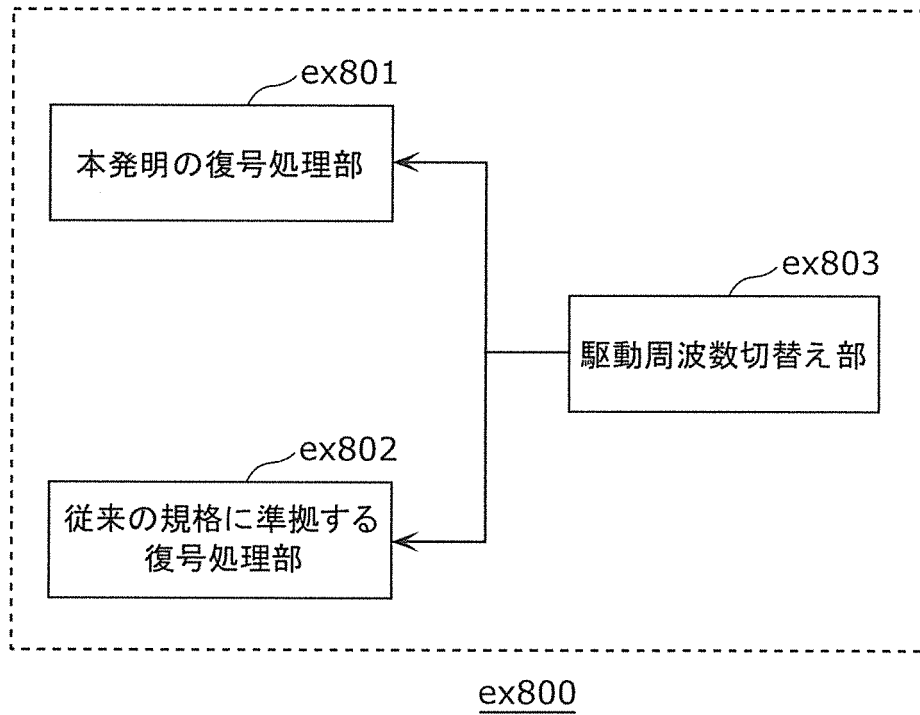
[図53]



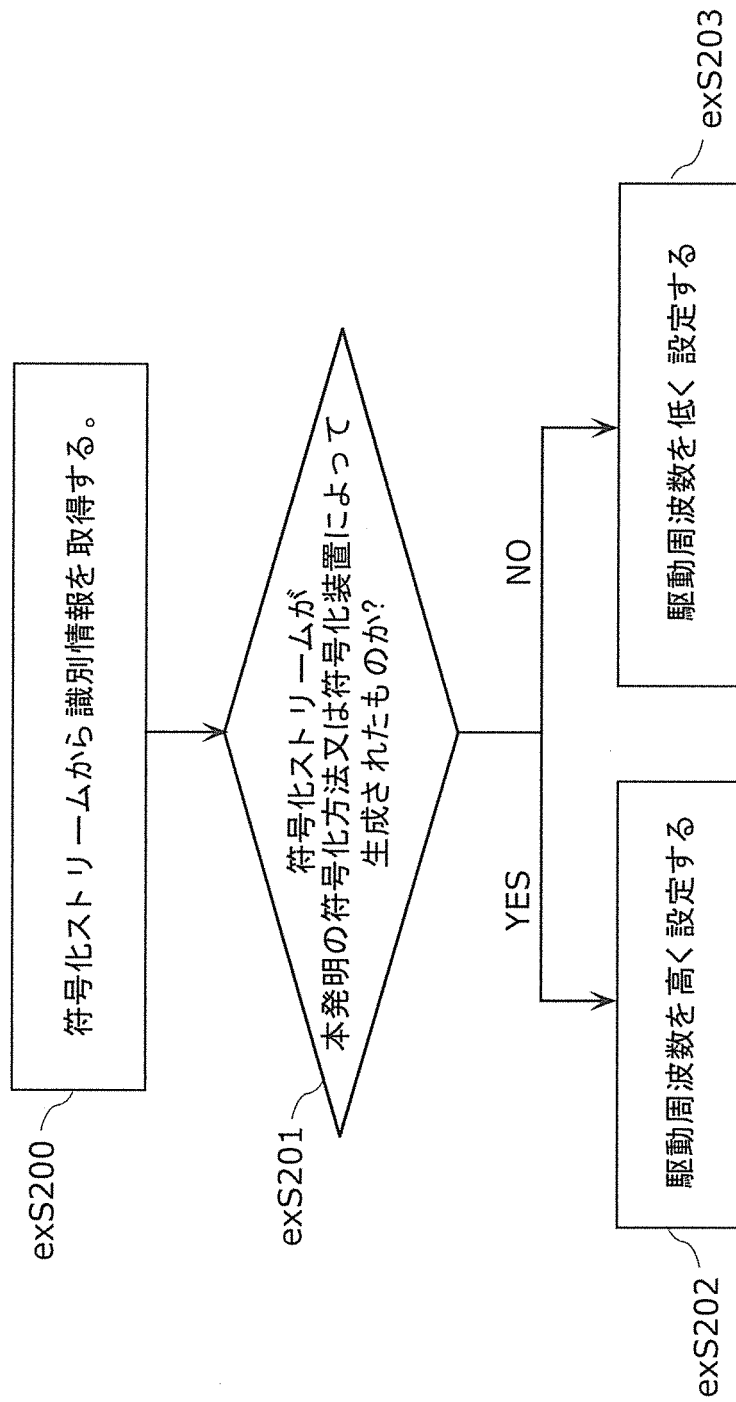
[図54]



[図55]



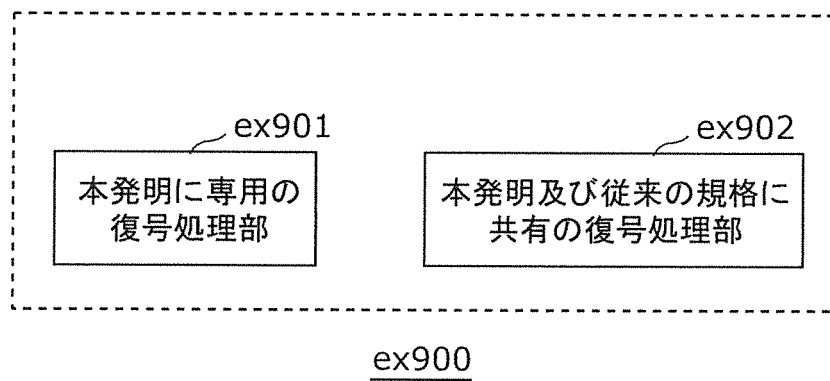
[図56]



[図57]

対応規格	駆動周波数
MPEG4.AVC	500MHz
MPEG2	350MHz
...	...

[図58A]



[図58B]

