

LOW POWER SELF-LIMITING INPUT CIRCUIT

ABSTRACT

An input circuit (60) for an input module (30) having a plurality of inputs (40) is disclosed. Each input circuit (60) has a reduced number of components, providing for an increased number of inputs (40) within an input module (30) of comparable size to prior art input modules (30). Alternately, the input module (30) may have a like number of inputs (40) in a reduced size in comparison to prior art input modules (30). The reduced number of components similarly reduces the power dissipated by each input circuit (60).

Fig. 4