ABSTRACT

A COMPUTER SYSTEM HAVING SOFTWARE INTERRUPT (INTn) INSTRUCTIONS SELECTIVELY OPERATING IN A VIRTUAL MODE. (Figure 3)

A computer system is disclosed herein including a given microprocessor specifically designed to operate in a virtual operating mode that allows a software program previously written for an earlier designed single program microprocessor to execute in a protected, paged, multi-tasking environment under a particularly designed host operating software program. The system also includes means for executing software interrupt (INTn) instructions, using emulation software forming part of the host program in order to emulate the way in which these instructions would have been executed by the earlier microprocessor. As a unique improvement to this overall computer system, certain ones of the INTn instructions are executed by means of emulation software while others are executed by means of the previously written program in cooperation with the given microprocessor and its host operating software program.

This Sheet Has Been Replaced

