Abstract

In the digital part of a receiver, the symbols are processed in a first phase in a manner known per se by means of a DFE (decision feedback equalizer) and a decoder (7). In a subsequent, second phase, the symbols are tapped off from the output side of the decoder (7) and are fed back to the feedback filter (5), excluding the decision maker (3) contained in the DFE. In other words, the feedback no longer takes place within the DFE but includes the channel decoder. In the second phase, iterations are carried out until a predetermined termination criterion is satisfied. The fed back symbols are generally more reliable than those which are generated within the DFE by the decision maker.

(Figure 1)

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