What is claimed is:

1. A method comprising:
determining whether compression is successful; and
recording whether compression was successful on a control surface in a memory accessible by a graphics processing unit and a central processing unit.

2. The method of claim 1 including, if compression is successful, recording a codec used for compression in the control surface.

3. The method of claim 1 including reading data by accessing the control surface.

4. The method of claim 1 including using a single bit per compressible unit to indicate whether the unit is compressed.

5. The method of claim 1 including using the same control surface coding for both central processing and graphics processing units.

6. The method of claim 4 including providing $(L/M)\ x\ log_2 M$ compression control bits per entry, where L is total number of memory lines in a page and M is the number of memory lines in the compressible unit.

7. The method of claim 1 including coding codecs in said surface by type including one or more of color, depth or video codecs.

8. The method of claim 1 including storing the surface on a translation lookaside buffer.

9. The method of claim 1 including storing the surface for control by a page table.

10. The method of claim 9 including storing the surface in the page table.

11. One or more non-transitory computer readable media storing instructions executed by a processor to perform a sequence comprising:

recording whether compression is successful and if so, recording a codec used for compression of data in a control surface; and

writing the data to memory.

12. The media of claim 11, said sequence including reading data by accessing the control surface.

13. The media of claim 11, said sequence including using a single bit per compressible unit to indicate whether the unit is compressed.

14. The media of claim 11, said sequence including using the same control surface coding for both central processing and graphics processing units.

15. The media of claim 13, said sequence including providing $(L/M)\ x\ log_2 M$ compression control bits per entry, where L is total number of memory lines in a page and M is the number of memory lines in the compressible unit.

16. The media of claim 11, said sequence including coding codecs in said surface by type including one or more of color, depth or video codecs.

17. The media of claim 11, said sequence including storing the surface on a translation lookaside buffer.

18. An apparatus comprising:

a processor to record whether compression is successful and if so, record a codec used for compression of data in a control surface, and write the data to memory; and

a memory coupled to said processor.

19. The apparatus of claim 18, said processor to read data by accessing the control surface.

20. The apparatus of claim 18, said processor to use a single bit per compressible unit to indicate whether the unit is compressed.

21. The apparatus of claim 18, said processor to use the same control surface coding for both central processing and graphics processing units.

22. The apparatus of claim 20, said processor to provide $(L/M)\ x\ log_2 M$ compression control bits per entry, where L is total number of memory lines in a page and M is the number of memory lines in the compressible unit.

23. The apparatus of claim 18, said processor to code codecs in said surface by type including one or more of color, depth or video codecs.

24. The apparatus of claim 18, said processor to store the surface on a translation lookaside buffer.

25. The apparatus of claim 18 including a display communicatively coupled to the processor.

What is claimed is:

1. A method comprising:

determining whether compression is successful; and

recording whether compression was successful on a control surface in a memory accessible by a graphics processing unit and a central processing unit.

2. The method of claim 1 including, if compression is successful, recording a codec used for compression in the control surface.

3. The method of claim 1 including reading data by accessing the control surface.

4. The method of claim 1 including using a single bit per compressible unit to indicate whether the unit is compressed.

5. The method of claim 1 including using the same control surface coding for both central processing and graphics processing units.

6. The method of claim 4 including providing $(L/M)\ x\ log_2 M$ compression control bits per entry, where L is total number of memory lines in a page and M is the number of memory lines in the compressible unit.

7. The method of claim 1 including coding codecs in said surface by type including one or more of color, depth or video codecs.

8. The method of claim 1 including storing the surface on a translation lookaside buffer.

9. The method of claim 1 including storing the surface for control by a page table.

10. The method of claim 9 including storing the surface in the page table.

11. One or more non-transitory computer readable media storing instructions executed by a processor to perform a sequence comprising:

recording whether compression is successful and if so, recording a codec used for compression of data in a control surface; and

writing the data to memory.

12. The media of claim 11, said sequence including reading data by accessing the control surface.

13. The media of claim 11, said sequence including using a single bit per compressible unit to indicate whether the unit is compressed.

14. The media of claim 11, said sequence including using the same control surface coding for both central processing and graphics processing units.

15. The media of claim 13, said sequence including providing $(L/M)\ x\ log_2 M$ compression control bits per entry, where L is total number of memory lines in a page and M is the number of memory lines in the compressible unit.

16. The media of claim 11, said sequence including coding codecs in said surface by type including one or more of color, depth or video codecs.

17. The media of claim 11, said sequence including storing the surface on a translation lookaside buffer.

18. An apparatus comprising:

a processor to record whether compression is successful and if so, record a codec used for compression of data in a control surface, and write the data to memory; and

a memory coupled to said processor.

19. The apparatus of claim 18, said processor to read data by accessing the control surface.

20. The apparatus of claim 18, said processor to use a single bit per compressible unit to indicate whether the unit is compressed.

21. The apparatus of claim 18, said processor to use the same control surface coding for both central processing and graphics processing units.

22. The apparatus of claim 20, said processor to provide $(L/M)\ x\ log_2 M$ compression control bits per entry, where L is total number of memory lines in a page and M is the number of memory lines in the compressible unit.

23. The apparatus of claim 18, said processor to code codecs in said surface by type including one or more of color, depth or video codecs.

24. The apparatus of claim 18, said processor to store the surface on a translation lookaside buffer.

25. The apparatus of claim 18 including a display communicatively coupled to the processor.

26. The apparatus of claim 18 including a battery coupled to the processor.