

FIG. 1

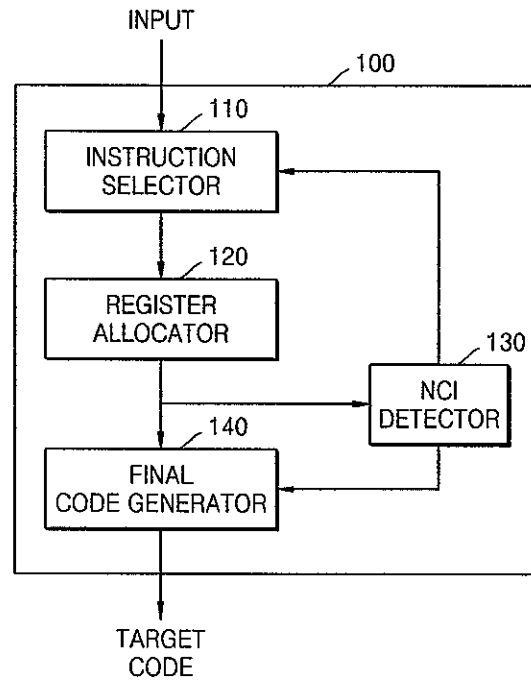


FIG. 2

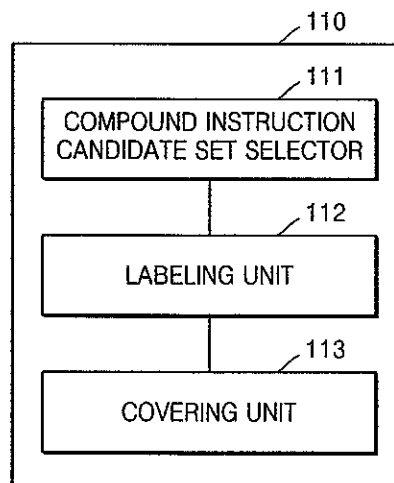


FIG. 3

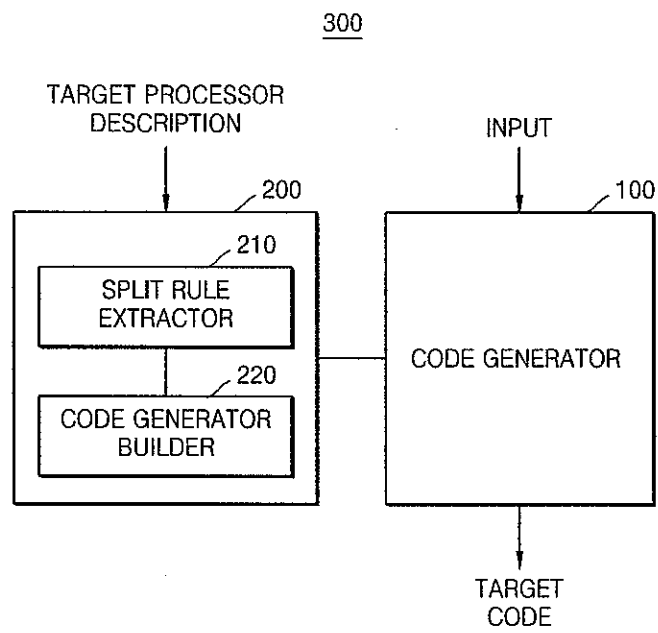


FIG. 4A

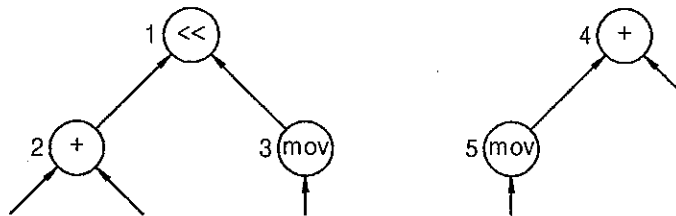


FIG. 4B

~~A(1, 2)~~, B(1, 4), ~~C(4, 3)~~, D(1, 5),
 E(2, 3), F(2, 5), G(3, 4), ~~H(4, 5)~~

FIG. 5A

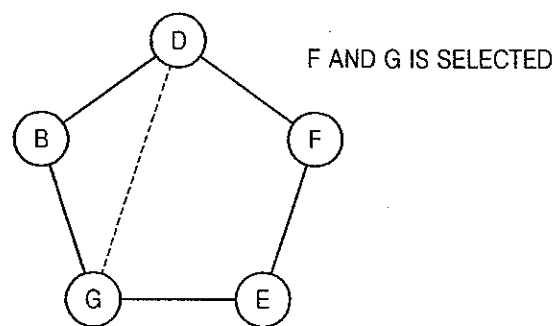


FIG. 5B

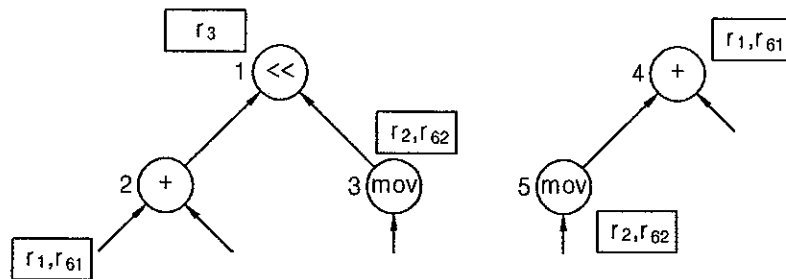


FIG. 5C

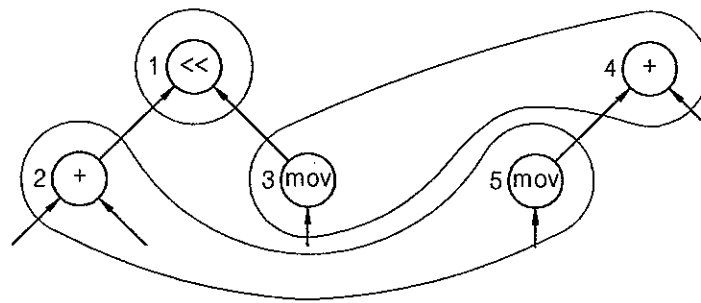


FIG. 5D

```

addmov t23, t21, t22, t28, t27
addmov t30, t28, t29, t25, t24
sll t26, t23, t25

```

t24 and t25 are pre-assigned
to argument register r1 and r2

FIG. 5E

```

addmov r3, r2, r3, r4, r4
addmov r1, r4, r5, r2, r1
sll r4, r3, r2

```

FIG. 6A

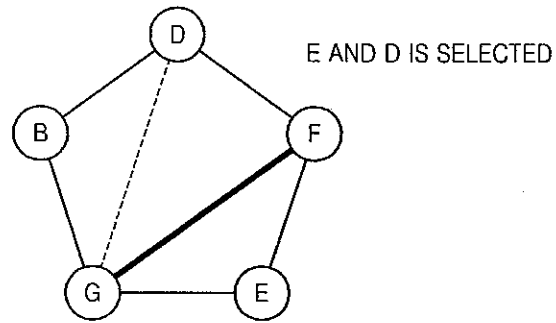


FIG. 6B

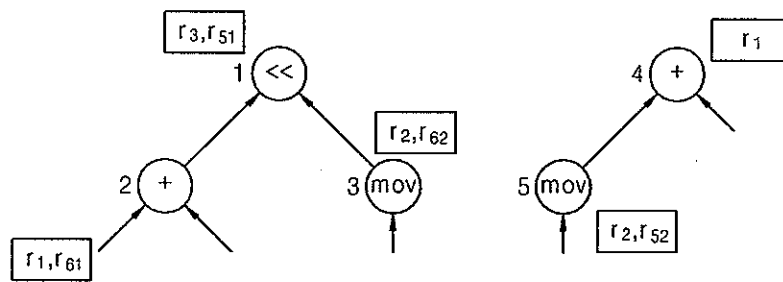


FIG. 6C

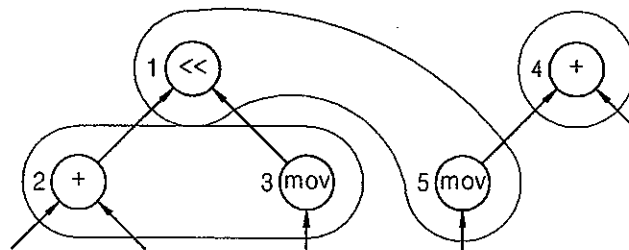


FIG. 6D

```
addmov t23, t21, t22, t25, t24  
sllmov t26, t23, t25, t28, t27  
add t30, t28, t29
```

t24 and t25 are pre-assigned
to argument register r1 and r2

FIG. 6E

```
addmov r1, r2, r3, r2, r1  
sllmov r1, r1, r2, r4, r4  
add r2, r4, r5
```

FIG. 7A

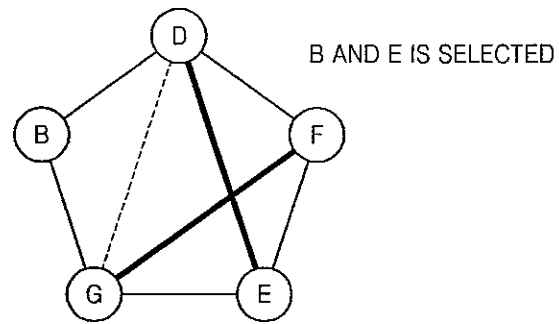


FIG. 7B

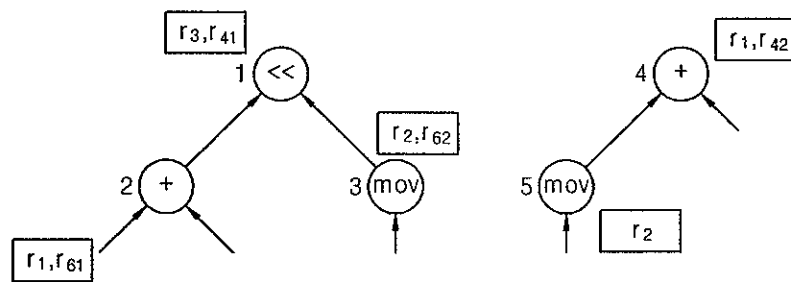


FIG. 7C

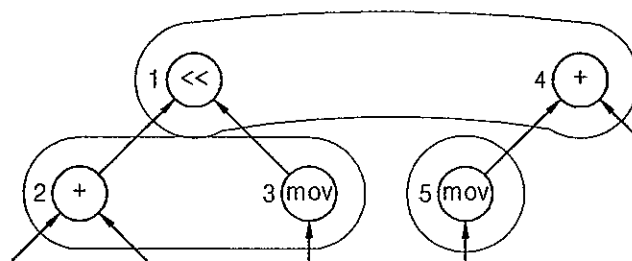


FIG. 7D

```
addmov t23, t21, t22, t25, t24  
mov t28, t27  
slladd t26, t23, t25, t30, t28, t29
```

t24 and t25 are pre-assigned
to argument register r1 and r2

FIG. 7E

```
addmov r1, r2, r3, r2, r1  
mov r4, r4  
slladd r1, r1, r2, r4, r5
```

FIG. 8

