

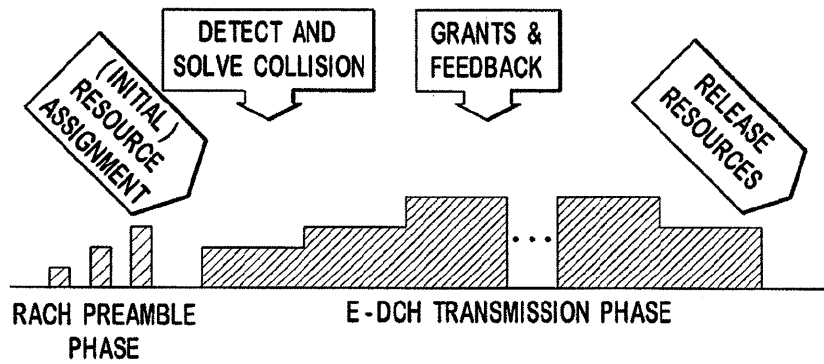


FIG. 1
(PRIOR ART)

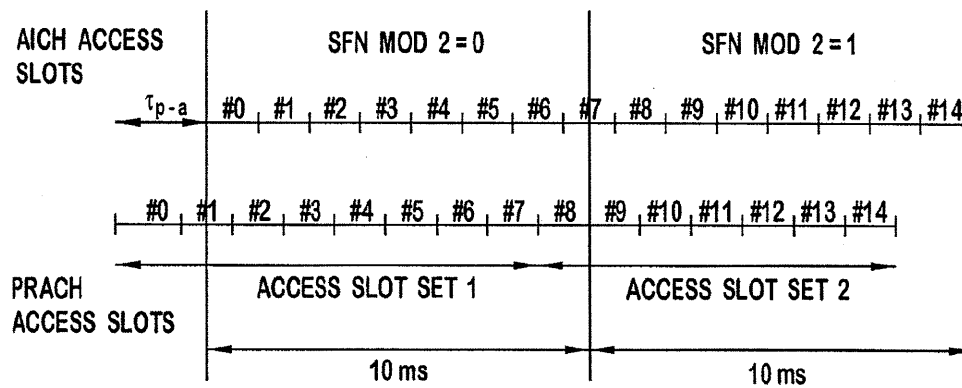


FIG. 2
(PRIOR ART)

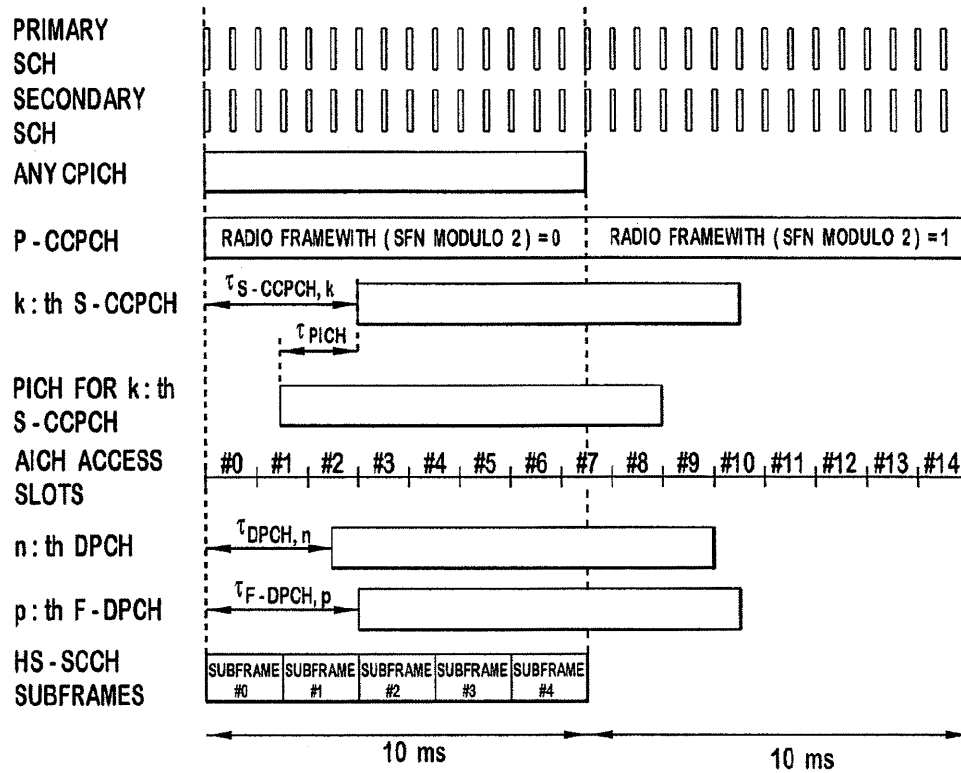


FIG. 3
(PRIOR ART)

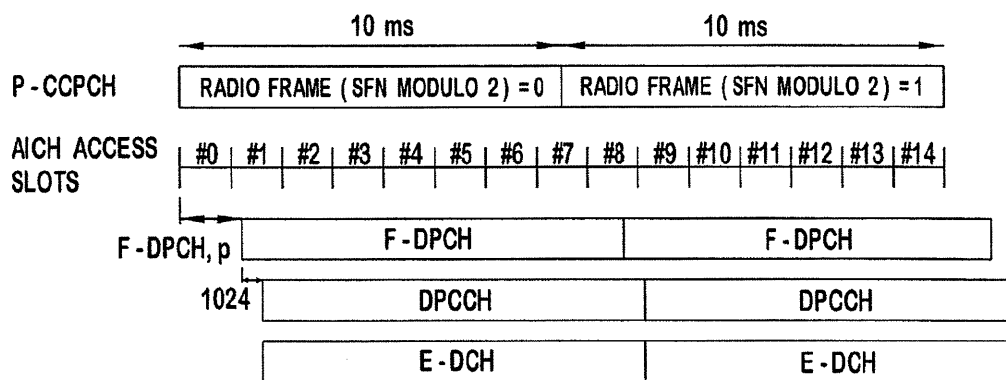


FIG. 4
(PRIOR ART)

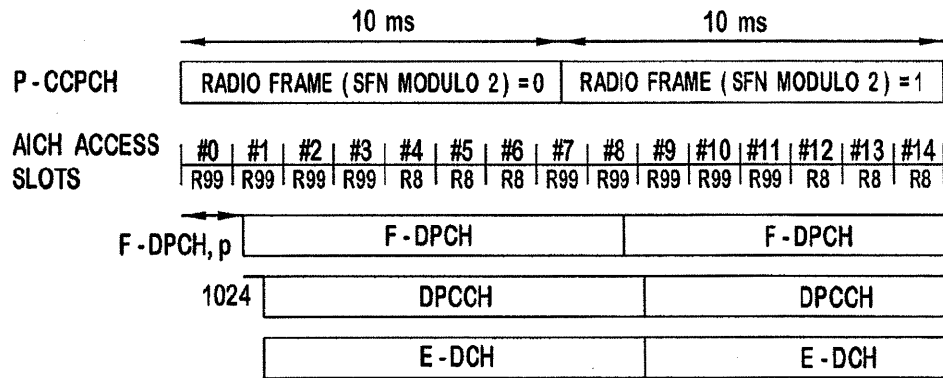
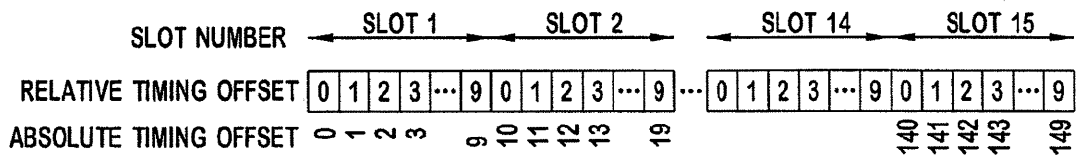


FIG. 5



NOTE : ALL OFFSETS IN MULTIPLE OF 256 CHIPS

FIG. 6

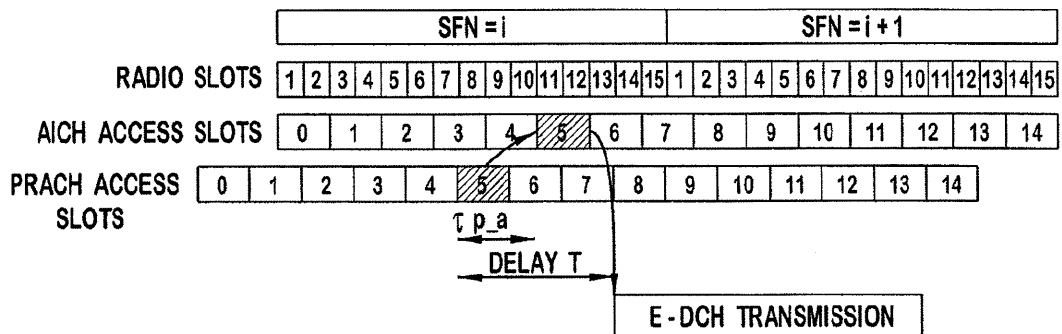


FIG. 7

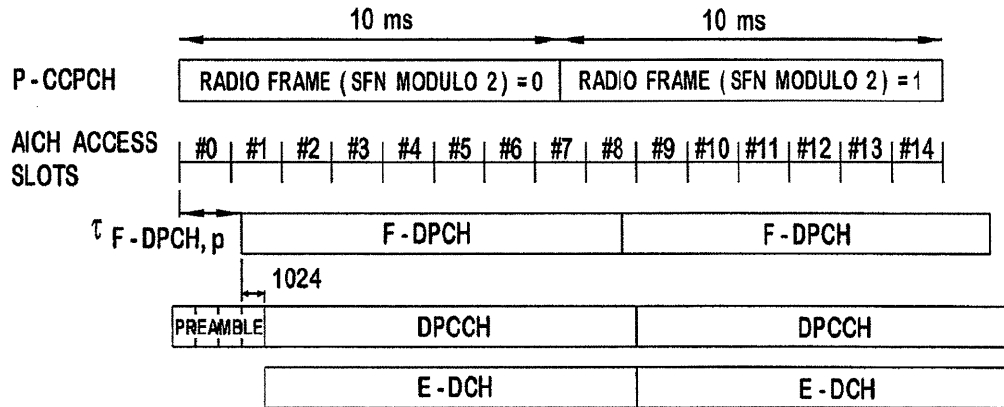


FIG. 8

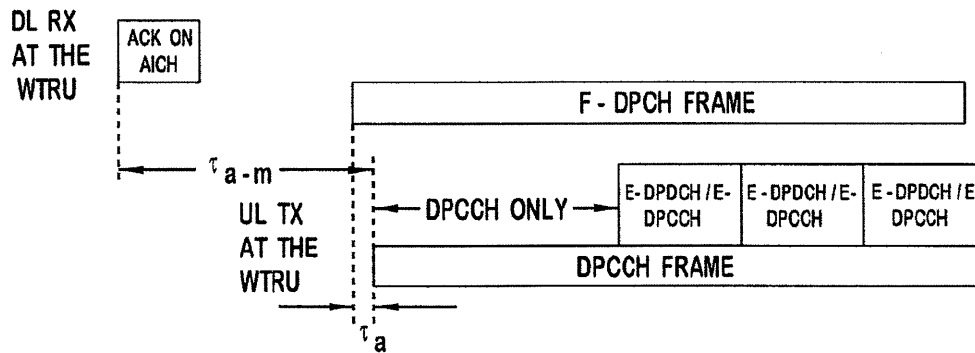


FIG. 9

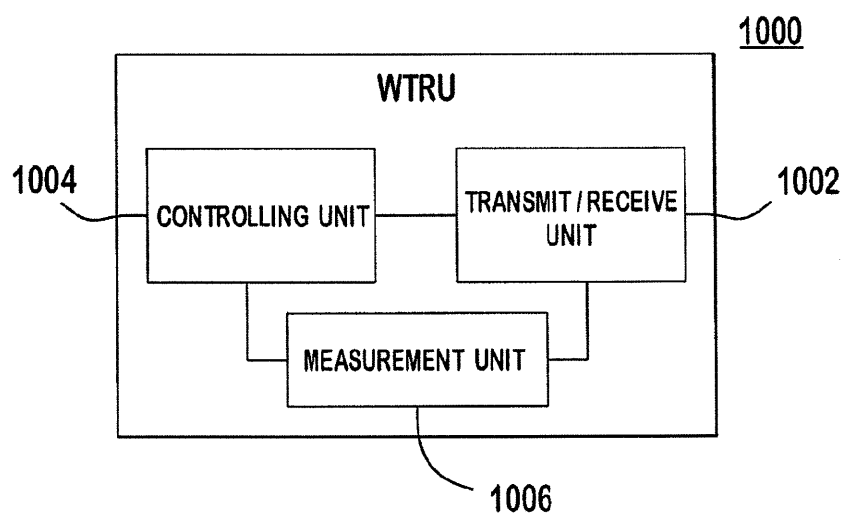


FIG. 10