

FIG. 1A

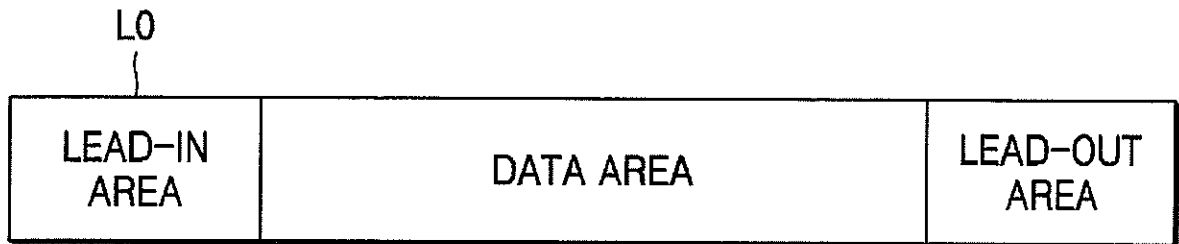


FIG. 1B

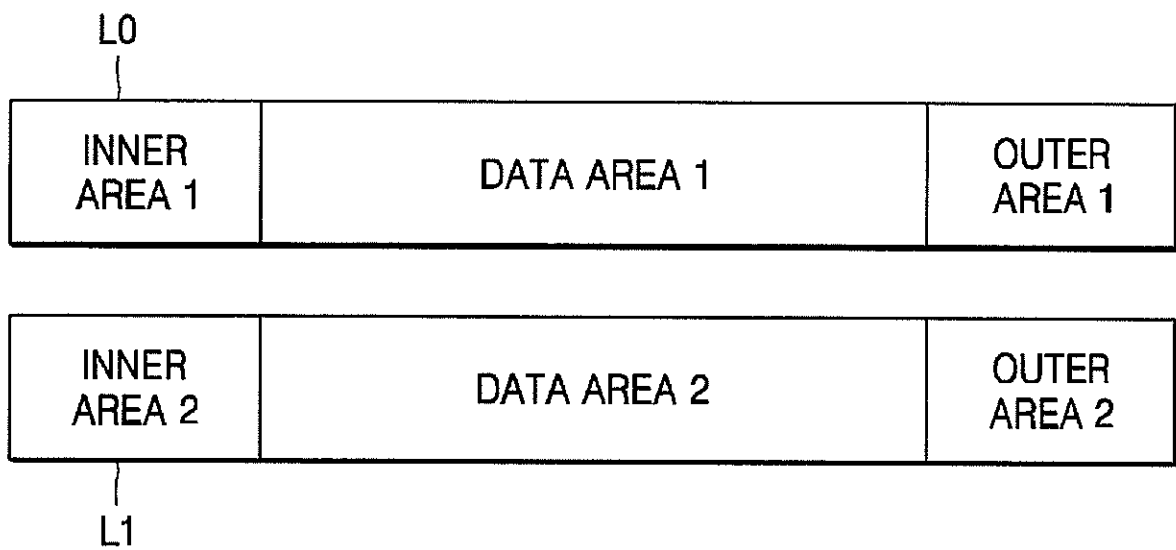


FIG. 2

LEAD-IN AREA	...
	DMA2
	RECORDING CONDITION TEST AREA
	AIA
	TDMA1
	DMA1
	...
DATA AREA	SPARE AREA1
	USER DATA AREA
	TDMA2
	SPARE AREA2
LEAD-OUT AREA	...
	DMA3
	...
	DMA4
	...

FIG. 3A

INNER AREA1	...
	DMA2
	RECORDING CONDITION TEST AREA
	AIA
	TDMA1
	DMA1
	...
DATA AREA1	SPARE AREA1
	USER DATA AREA
	TDMA3
	SPARE AREA2
OUTER AREA1	...
	DMA3
	...
	DMA4
	...

FIG. 3B

INNER AREA2	...
	RECORDING CONDITION TEST AREA
	...
	DMA8
	TDMA2
	DMA7
	...
DATA AREA2	SPARE AREA4
	TDMA5
	USER DATA AREA
	TDMA4
	SPARE AREA3
OUTER AREA2	...
	DMA6
	...
	DMA5
	...

FIG. 4A

INNER AREA1	...
	DMA2
	RECORDING CONDITION TEST AREA
	AIA
	TDMA1
	DMA1
	...
DATA AREA1	SPARE AREA1
	USER DATA AREA
	SPARE AREA2
OUTER AREA1	...
	DMA3
	...
	DMA4
	...

FIG. 4B

INNER AREA2	...
	RECORDING CONDITION TEST AREA
	...
	DMA8
	TDMA2
	DMA7
	...
DATA AREA2	SPARE AREA4
	TDMA5
	USER DATA AREA
	SPARE AREA3
OUTER AREA2	...
	DMA6
	...
	DMA5
	...

FIG. 5A

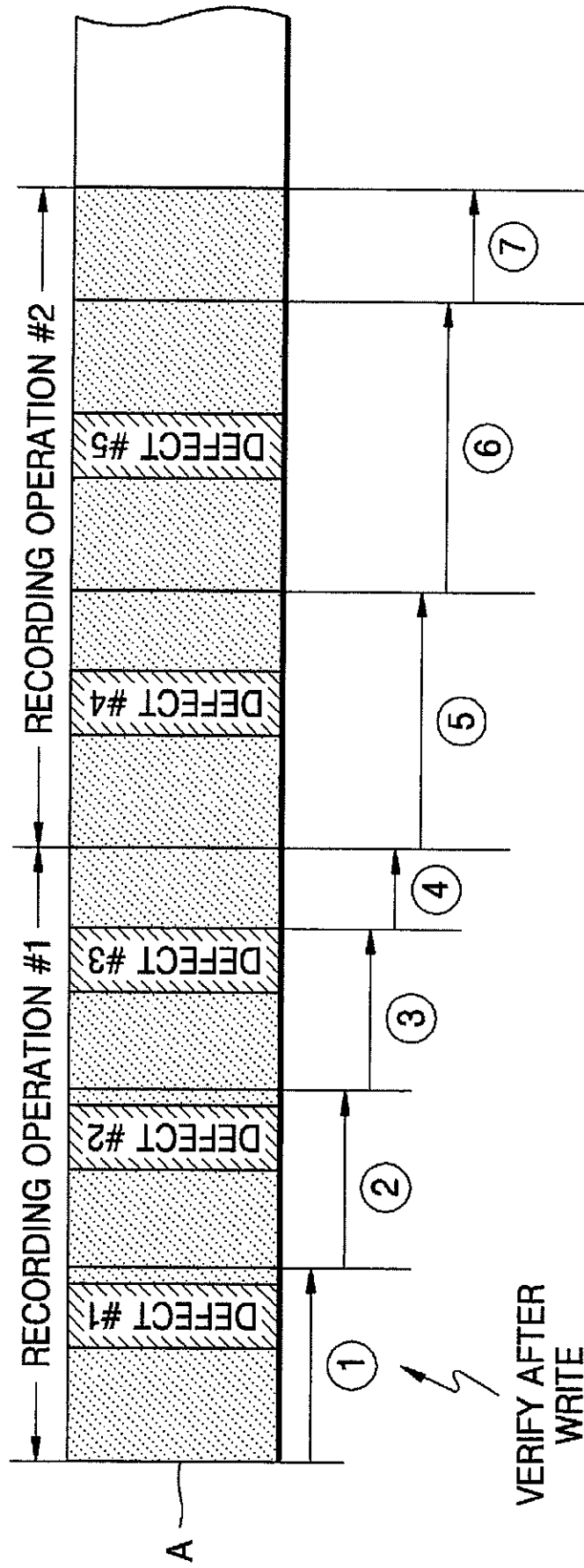


FIG. 5B

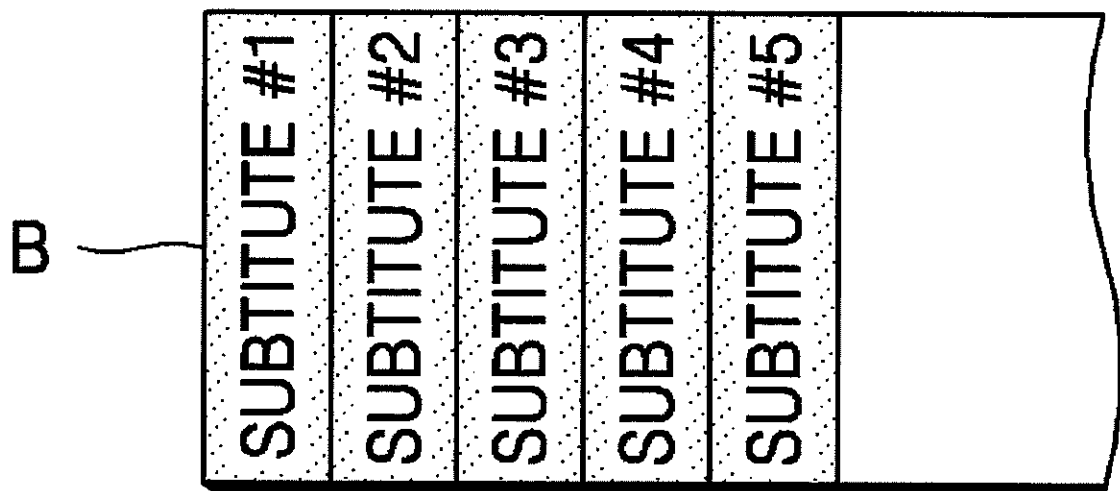


FIG. 6

TDDS

...
LOCATION INFORMATION OF TDMA2
LOCATION INFORMATION OF TDMA3
LOCATION INFORMATION OF TDMA4
...

FIG. 7

SUB AIA	SUB TDMA 1	SUB TDMA 2	...	SUB TDMA M
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FIG. 8

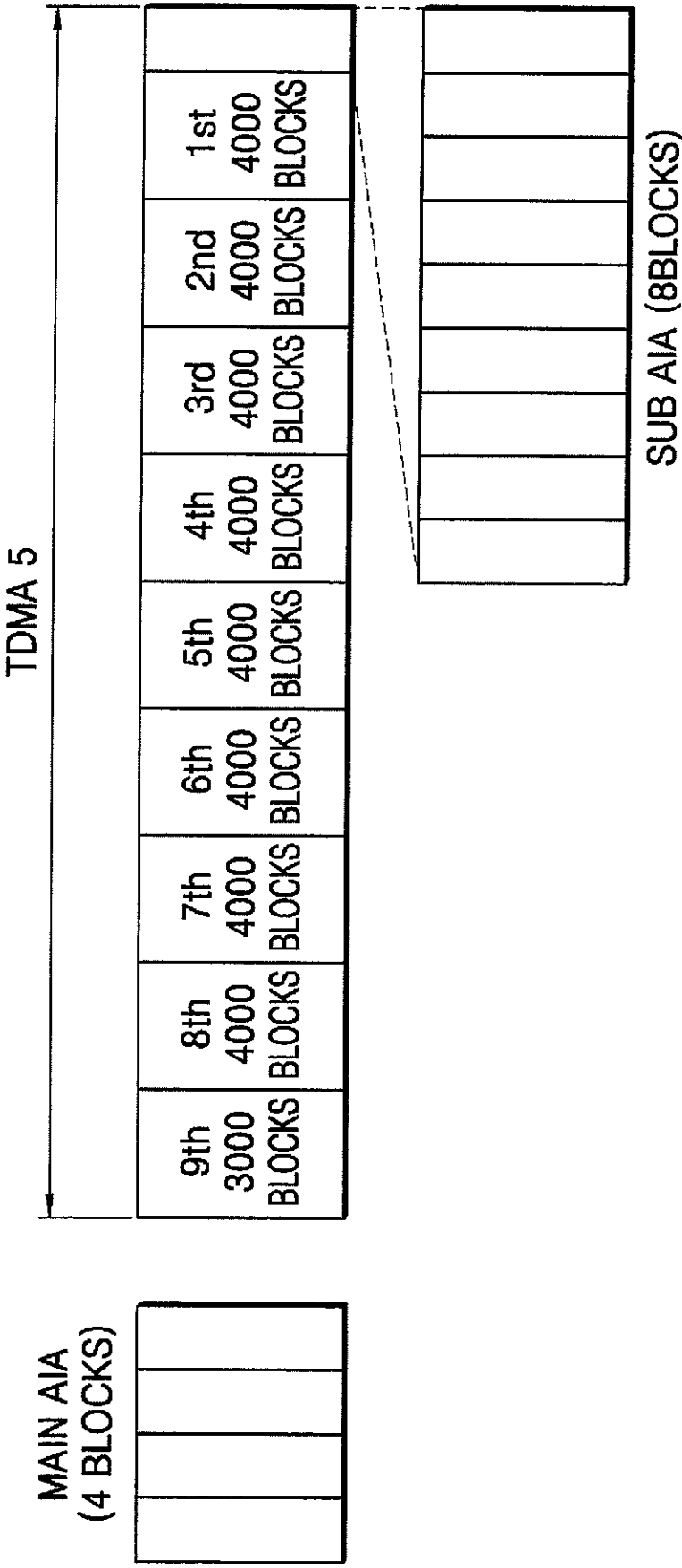


FIG. 9A

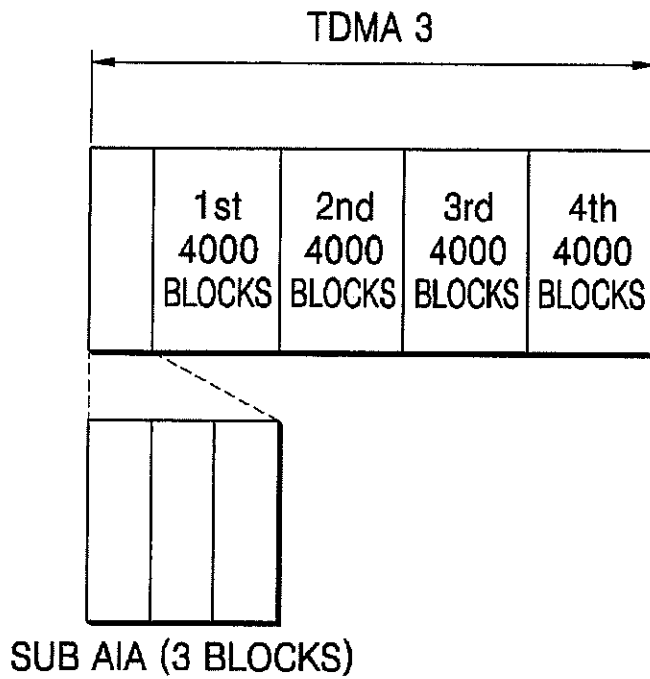


FIG. 9B

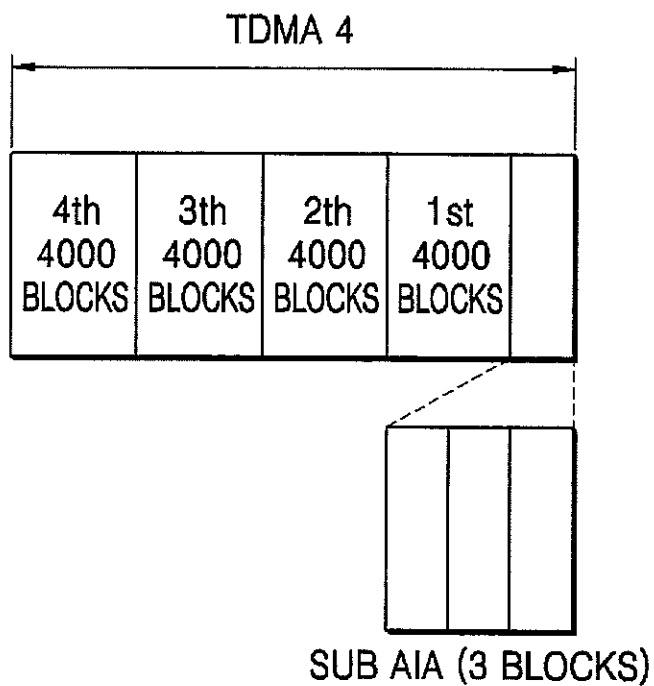


FIG. 10

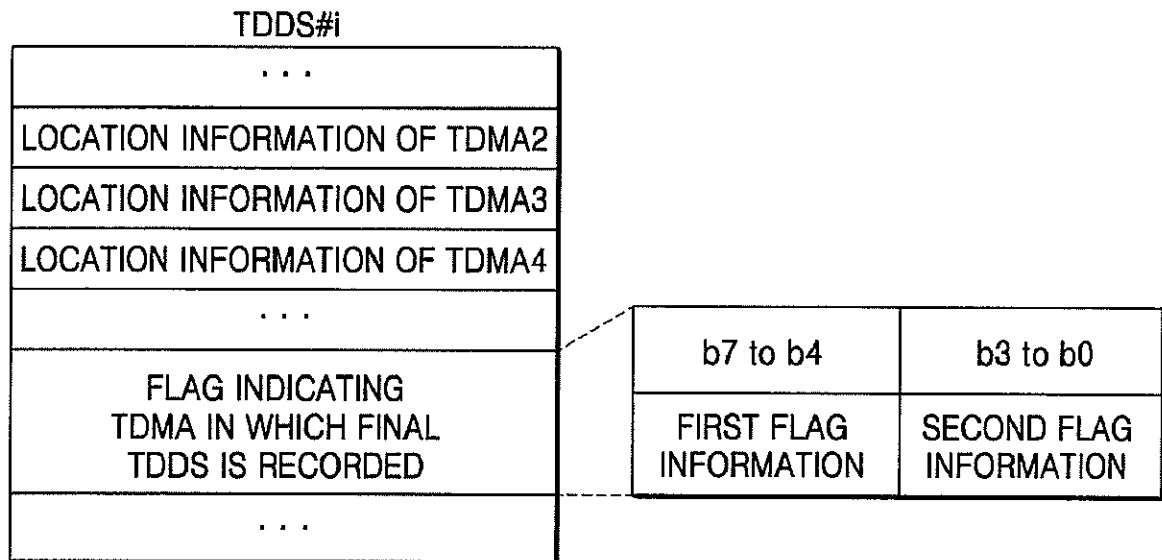


FIG. 11

