

Description

The present invention relates to a computer system according to the preamble part of claim 1.

Such a system is known from EP-A2-0 289 157, mentioning a system, which prevents powerup if an incompatible board pair is interconnected.

Many computer systems utilize what are called mother boards, system boards, or main boards which contain signal busses, microprocessors, device controllers, device interface connectors, and expansion slots. The system boards generally include some amount of base memory on the board itself. The system boards may also include predesignated locations or sockets for the installation of additional semiconductor memory chips, thereby increasing the base memory of the computer system. However, factors such as system board space allocation place a practical limit on the amount of memory which may be added to a system board.

One manner of addressing this practical limitation is to add memory through the use of circuit boards external to the main system board. These external memory boards are connected to the system board by means of connectors or slots on the system board. Generally, a memory expansion board is inserted into one of the system board's expansion slots and the computer system is updated as to the total amount of available memory to include the base memory on the system board and the memory on the expansion board. The means of updating the computer system as to the total memory available may include mechanical means, such as switches or jumpers, or electronic means, such as setting certain signal lines to certain levels.

Another manner of addressing the space limitations of the system board was to use single inline memory modules (SIMM's) on the system board. On a SIMM the memory devices use very small packages and are very densely packed, so that a large amount of memory can be installed in a small area. The SIMM is installed in a connector on the system board and are installed perpendicular to the system board, thus using little space.

Recent developments in computer technology, particularly in the area of microcomputers have lead to an increase in the amount of total memory which may be effectively managed by the computer systems. With the introduction of Intel Corporation's 80386 microprocessor, microcomputers are now capable of managing in excess of 16 megabytes (Mb) of memory. With the advancement of gate array and application specific integrated circuits (ASIC's) many functions previously performed on interchangeable circuit boards located in slots on the system board have been integrated onto the system board. Thus, system board space becomes even more precious and memory areas must be very dense. The space problem on the memory board can become so severe that even the use of SIMM's can not be tolerated and all the memory must be placed on external circuit boards.

Personal computers are generally designed to use circuit boards conforming to a selected standard for expansion. One such standard is the Industry Standard Architecture (ISA) which is based on the IBM PC/AT manufactured by International Business Machines Corporation (IBM). The use of the ISA standard limits the physical size of the circuit boards and memory boards which can be installed in the computer system. This size limitation, when coupled with the addressing capabilities of the microprocessors such as the 80386 and the demands for memory developed by many computer programs currently available, limits the amount of memory which can be installed on an external circuit board. The use of SIMM's may result in sufficient memory being available or being capable of being installed on the circuit board.

However, the use of SIMMs imposes a practical limitation on the computer system. The physical height of the modules, when mounted on the external circuit board effectively renders closely adjacent expansion slots unusable. Thus, the use of SIMMs have the effect of decreasing the number of expansion slots available in the computer system, thereby decreasing the flexibility of a computer system.

It is the object of the present invention to provide a computer system which is designed to guard against improper insertion of the memory module in the expansion board. An improper insertion of the module will result in the misalignment of connector and could do significant damage to the memory module and the expansion board. The present invention is designed to prevent this damage from occurring by effectively shorting the power supply when the module is misaligned with the expansion board.

This object is solved by the subject matter of claim 1.

Figure 1 is block diagram of a computer system motherboard.

Figures 2A and 2B illustrate the problems encountered in using a SIMM technology on an expansion board to increase system memory. Figure 2B is illustrative of a typical SIMM memory module.

Figure 3 is a drawing a preferred memory expansion board of the present invention.

Figures 4A - 4C are different views of the memory module.

Figures 5A and 5B are illustrative of an expansion board populated with two memory modules installed on the expansion board.

Figure 6 is illustrative of the advantages of the preferred memory expansion board when installed in a computer system motherboard.

Figures 7A - 7C are schematics showing the means of protecting semiconductor memory from misalignment.

Figure 8 is illustrative of the means of accomplishing memory addressing in accordance with a preferred embodiment.

Figure 9 is a diagram of the current limiting circuit utilized in the present invention.

Figure 1, which is a block representation of a micro-

computer system board 10, is included only for general physical reference and is used to illustrate the advantages of the present invention. The system board 10 includes a microprocessor CPU 12, a memory cache controller 14 and cache memory 18. A numeric coprocessor 16 may be included as part of the computer system. The system board 10 of Fig. 1 includes an integral video graphics controller 24 and video memory 26. Also included as integral parts on the system board 10 are a floppy disk controller 20 and a hard disk controller 22. It will be appreciated that not all computer systems will have device controllers, such as the hard disk controller 22, or the graphics controller 24 and graphics memory 26 as an integral part of the system. These functions are often mounted on external cards or boards and are inserted into the system board 10 by way of connectors or expansion slots 30. The system board 10 of Fig. 1 is shown as having six expansion slots 30, each of the slots 30 having two discrete connectors for insertion of external cards. Also shown is a special expansion slot 31, having three discrete connectors 31A, 31B and 31C, for use by a memory expansion board. The slots 30 and 31 have uniform spacing between the slots 30 and 31 conforming to the general suggestions and parameters of the ISA. These slots may be used to insert interface and control devices such as disk controllers, graphics controllers, additional memory or other external interfaces such as local interface networks, or modems. It will be appreciated that the availability of these expansion slots 30 significantly increases the flexibility of a computer system.

In Fig. 2A, a SIMM memory expansion board 200 has been inserted into expansion slot 31. Mounted on expansion board 200 are a series of connectors 206 and miscellaneous circuitry, including diagnostics, transceivers, and other signal control devices which are not shown. The expansion board 200 lies in a plane which is perpendicular to the system board 10. SIMM memory modules 202 are inserted into the memory expansion board connectors 206 in order to add additional memory to the computer system. In Figure 2A, a total of eight SIMM modules 202 are installed on the expansion board 200. Referring to Fig. 2B, the SIMM module 202 includes circuit board 208, memory devices 204 and a connector 210 which mates with SIMM expansion board connector 206.

Referring to Fig. 2A, it will be appreciated that the configuration of the expansion board 200 and SIMM modules 202 in memory expansion slot 31 effectively renders expansion slots 30B and 30C unusable. The SIMM modules 202 effectively prevent the insertion of any other card or board into these slots. The net result is that the computer system has been rendered less flexible as a result of the addition of the SIMM memory expansion board 200 and modules 202.

Fig. 3, is a preferred memory expansion board. A memory expansion board 300 is designed to fit into the memory expansion slot 31 as shown in Fig. 1. The expansion board 300 generally conforms to the general

size requirements of the ISA, except that the card edges used for connection to the system board are configured differently than as necessary for insertion into an ISA expansion slot, such as a slot 30. An ISA board is approximately 13.375 inches long and 4.8 inches wide, including the card edge. In the preferred embodiment the expansion board 300 is only 13 inches wide to allow for additional clearances. The expansion board 300 includes on-board memory 302 and address and control buffers 304 for the on-board memory. Also mounted on the expansion board are connectors 306 for the insertion of additional memory modules 400 (Fig. 4A). In the illustrated configuration, two connectors 306 are used to connect a memory module 400 to the expansion board 300. In order to minimize the size of the memory modules 400, much of the circuitry associated with each of the modules 400 is placed on the expansion board 300. Address and control signal buffers 308 are mounted between the connectors 306 for each of the modules. The control circuitry for the memory modules 400 is placed on the expansion board 300, thereby decreasing the necessary size of the module 400 itself. In addition, parity chips 314, module control circuitry 310 and diagnostic circuitry 312 commonly used by the on-board memory 302 and the modules 400 are placed on the expansion board 300. Also included as part of the memory expansion board 300 are connectors 316 and 318 which mate with the connectors 31A, 31B and 31C of the system board 10 as shown in Fig. 1.

Thus, the addition of the expansion board 300 by itself increases the effective memory of the computer system. In the preferred embodiment addition of the expansion board 300 would expand memory by 2 Mbytes.

Referring now to Figs. 4A - 4C, Fig. 4A is a bottom view of the memory module 400. The module is comprised of a board 401, having a memory devices area 402, parity devices area 403 and two connectors 404 which are designed to mate with the memory expansion board connectors 306. The module 400 in the preferred embodiment is approximately 4.5 inches tall and 1.625 inches wide. Fig. 4B is a top view of the memory expansion module showing the board 401 and memory device area 402 and parity device area 403. It will be appreciated when viewing Fig. 4C that the preferred expansion module 400 is designed to have memory devices 407 mounted on both useable surfaces of board 401. Further, the configuration of the connectors 404 results in a low profile expansion module. In the preferred embodiment of the invention, each module 400 has 2 Mbytes of memory by using 1 Mbit memory devices for data storage and 256 kbit memory devices for parity storage. The data memory devices are preferably organized as 256k by 4 and are located in the memory device area 402, with the 256 kbit parity memory devices being preferably organized as 256k by 1 and being located in the parity device area 403.

Referring now to Fig. 5A, which illustrates the memory expansion board 300 having two memory modules

400 installed, the memory modules 400 do not significantly increase the footprint of the memory expansion board 300. As may be seen in Figure 5B, when a memory module 400 is installed, connectors 306 and 404 are mated and the module 400 is installed over its accompanying control and address buffers 308. Even though memory devices 407 have been mounted on both sides of the memory module 400, there still exists sufficient clearance between the memory devices 407 and the buffers 308 due to the height of the connectors 306 and 404. The module circuit board 401 is approximately 0.40 inches over the surface of the expansion board 300 when the module 400 is fully seated.

Fig. 6 shows a partially populated expansion board 300 of Figs. 5A and 5B installed on the system board 10. In the preferred configuration, a special slot 31 using connectors 31A, 31B and 31C has been created for memory expansion board 300. However, it will be appreciated that a memory board having the same configuration could be used in the standard expansion slots 30. It will be appreciated that the low profile of the populated board of Figs. 5A and 5B does not require the space envelope of comparable SIMM memory expansion boards and modules as shown in Figs. 2A and 2B. Though only two memory modules 400 are shown as being installed, if the expansion board 300 were fully populated with memory modules 400, the resulting space requirements, as shown by a dotted line 600 would not interfere with the use of expansion slots 30A - 30F. Thus, the present invention significantly increases the memory capacity of a computer system in discrete steps without adversely affecting the flexibility of the computer system.

In the current preferred embodiment the system board 10 contains no main memory. The system comes equipped with a memory expansion board 300 which includes 2Mbyte of memory. As applications require additional memory, the user can purchase memory modules 400 which are configured in 2Mbyte increments. The user can install up to 7 memory modules on a single board 300, thereby increasing total system available memory to 16Mbyte. However, it is expected that as the practical storage size of memory devices increases, the memory capacities of the module 400 and the expansion board 300 may be significantly increased with no increase in the size of the expansion board 300 or memory modules 400.

The present invention encompasses a method of protecting computer system memory due to misalignment of connectors 306 and 404. Figure 7A depicts one of the two connectors 306 on expansion board 300, connector 306 having a multiplicity of address, data, control, power and ground lines. In the preferred embodiment, connector 306 is a double row connector, each row having two lines, A_n and B_n . Lines A_1 , B_1 , A_2 , and B_2 of connector 306 are connected to bus 710. Lines A_3 and A_6 of connector 306 are connected to power line 706. Lines B_3 , A_4 , B_4 , A_5 and B_5 of connector 306 are connected to ground 702. Fig. 7A also depicts one of two

connectors 404 on module 400. Connector 404 is also a double row connector having a multiplicity of address, data, control, power and ground lines. Lines A_1' , B_1' , A_2' , and B_2' of connector 404 are connected to bus 712. Lines A_3' and A_6' of connector 404 are connected to power line 708. Lines B_3' , A_4' , A_5' and B_5' of connector 404 are connected to ground 702. For the purpose of simplicity and clarity, Figs. 7A and 7B do not display all of the address, data, control, ground and power lines.

Referring still to Fig. 7A, it will be appreciated that the address, power and ground lines of connector 306 on expansion board 300 will mate with their corresponding address, power and ground lines of connector 404 on memory module 400 and the module 400 will be properly energized. It will be appreciated that connectors 306 and 404 are preferably mating pin and socket connectors.

Fig. 7B illustrates the condition that occurs when the memory module 400 is inserted into the memory expansion board 300 and is misaligned by one connector row. Because connectors 306 and 404 are double row connectors, the module 400 misalignment effectively shifts connector 404 down two line positions. Memory board 300 line A_2 is connected to module 400 line A_1' . It will be appreciated that the misalignment shown in Fig. 7B could cause severe errors. Further, various signal lines could be misaligned such that two enabled outputs are connected, possibly causing permanent damage to both the memory expansion board 300 and the memory modules 400.

As a further result of the misalignment, connector 306 power line A_6 is mated with connector 404 ground line A_5' and connector 306 ground line B_4 is mated with connector 404 ground line B_3' , thereby shorting power 706 to ground 702. This power to ground connection creates a short across the power supply of the computer. This short is detected by the computer power supply before any substantial damage can occur to the memory expansion board 300 or modules 400. The power supply itself may be protected by a fuse, circuit breaker or current detection and limiting circuitry. In the preferred embodiment, the computer system includes current limiting circuitry which detects the high current being attempted to be delivered because of the short and turns off the power supply before any permanent damage is done. Further, the current limiting circuitry waits a predetermined period and attempts to reset the power supply. If the misaligned condition persists, the current limiting circuitry again shuts down the power supply and continues to cycle until the misalignment is corrected.

Figure 9 is a block diagram showing the preferred method of protecting the power supply. Power supply circuitry 900 includes a power supply 902 having a positive line 906 and a negative line 908. In practice, power supply 902 would provide a number of voltage sources. However, for the purpose of clarity, only the lines which would be used to supply power to the memory expansion board 300 are considered. The positive lead 906 is con-

ected to current limiting and timing circuitry 904 of the type generally known in the industry. The limiting circuitry 904 detects the high current being attempted to be delivered because of the short described in Fig. 7B and turns off the power supply 902 via line 910. Under normal operating conditions, power is supplied through lines 906 and 908 to expansion connector 31A which in turn supplies power to the memory expansion board 300 and memory modules 400.

The protective means, illustrated in Figs. 7A and 7B is designed to protect the circuitry when the connectors are misaligned in only one direction. Fig. 7C illustrates how the same means may be used to protect circuitry against misalignment in either direction in a general case. Connector 720 is shown as having power 726 connected to line 2 and ground 722 to lines 1, 3, 4 and 5. Lines 1 and 3 may be connected to ground or lines on which it is intended that a low signal be placed. Connector 724 is shown as having power 728 line 2' and ground 730 lines 1' and 3'-5'. When connectors 720 and 724 are misaligned by one position up, a power to ground short is created. Power 726 line 2, is connected to ground 730 line 3' and ground 730 line 5' is connected to ground 722 line 4, creating the short. Similarly, when misaligned one position down a power to ground short is created. Power 726 line 2 is connected to ground 730 line 1' and ground 730 line 4' is connected to line 5, creating the short. It will be appreciated that this general method of creating power to ground shorts may be adapted to various connector configurations.

Referring again to Figs. 4A-4C, the present invention minimizes the space requirements for the additional memory modules. In doing so, memory devices 407 are mounted on both sides of the memory module 400. The memory device area 402 is shown as a monolithic block but is made up of a plurality of memory devices. The devices are mounted such that all the memory devices are physically aligned in the same direction, i.e., pin 1 on the memory devices pointed in the same direction. However when mounting the memory devices on both sides of the board, the effect is to flip the pin assignments relative to each other. For example, the memory devices are mounted back to back and pin 1 on one memory device is physically located over pin 26 of the opposing memory device.

Referring now to Fig. 8, a memory device 800 contained in the memory device area 402. The device 800 includes data and control signals, B0, B1, WE* and RAS* on pins 1-4; ground on pin 13; control and signals, OE*, CAS*, B2 and B3, data and on pins 22 through 25; and power on pin 26. A second memory device 802 of the same type as memory device 800 is also shown in Fig. 8 as a dashed figure. Device 802 is shown dashed to represent that it appears on the opposite side of the board 401 and is shown smaller to make this illustration easier. It should be understood that there is no physical difference in the size of devices 800 and 802, and indeed that the devices 800 and 802 are identical devices. De-

vice 802 includes data and control signals, B0, B1, WE* and RAS* on pins 1-4; ground on pin 13; data and control signals, OE*, CAS*, B2, and B3 on pins 22 through 25; and power on pin 26. Because it appears as a mirror image to device 800, the physical position of the pins between devices 800 and 802 are misaligned. In Fig. 8, pin 1 appears at the top right of device 800 and at the bottom right of device 802. One manner of addressing this problem is to create a series of runs between the pins to address the mismatch. This is shown in Fig. 8, by way of example as a run between device 800, pin 22 (OE*) to device 802, pin 22 (OE*). The control lines RAS*, CAS*, WE*, OE* and the power line in Fig. 8 are generally shown as having these runs to correct for the misalignment condition. However, there is a disadvantage to this technique when used for address lines. Address lines are preferably as short as possible to limit capacitances and thus allow more memory devices to be driven from a single driver in acceptable times. Further, the shorter lines reduce radio frequency interference, which is always a problem at the speeds of current computer systems.

It was recognized that while certain memory address pins on devices 800 and 802 bore designations of $A_0 - A_8$, the designations are arbitrary and an address bit is simply an address bit, not an assigned bit in the vast majority of memory devices. Accordingly, it was determined that the length of any address line runs between the memory devices 800 and 802 could be drastically reduced by flipping the address pin assignments for device 802. Address bits $A_0 - A_3$ were assigned to device 800, pins 9 - 12 and device 802, pins 18 - 15. Similarly, address bits $A_5 - A_8$ were assigned to device 800, pins 15 - 18 and device 802, pins 12 - 9. This eliminated the need for most extensive address line runs between the devices 800 and 802. However, one run were required for the memory address lines. Pin 13 (ground) is opposite pin 14, address bit A_4 . Therefore, two runs were required to properly connect the ground for devices 800 and 802 and address bit A_4 , which remained assigned to pin 13 on both devices 800 and 802.

It will be appreciated that this means of addressing memory is one which is relatively independent of the arbitrary chip assignment. Further, as a result of this means of addressing memory, the complexity of the memory module 400 design was greatly reduced. The same technique may generally be used where two semiconductor devices having address lines 0-n are mounted on opposite sides of a board. The first device having address $A_0 - A_n$ is mounted opposite the second device having address lines $B_0 - B_n$. As shown in Fig. 8, control, ground and power lines would be connected via board runs. However, the address lines would be connected as follows:

$$A_0 - B_n$$

$$A_1 - B_{n-1}$$

$$A_{n-1} - B_1$$

$$A_n - B_0$$

Where this direct connection would result in connecting a power ground or control line to an address line, board runs would be created as shown in fig. 8 for address line A_4 .

Claims

1. A computer system having means for protecting electronic circuit boards in the computer system during the addition of circuit boards comprising:

a computer power supply (902) having at least a first and a second output terminal (906,908); characterized by:

a means (904) for limiting current flow in said power supply;

a first circuit board (300) having first means (306) for locating and connecting additional second circuit boards, said first means (306) having a connection (702) for said first power supply output terminal (908) and at least two connections (706) being located adjacent to and on either side of the connection (702) for said first power supply output terminal, both of said connections (706) being connected to said second power supply output terminal (906);

a second circuit board having second means (404) for locating and connecting said second circuit board to said first circuit board, said second means having at least one connection (704) to receive said first power supply output terminal (908) and corresponding to said first power supply output terminal connection (702) on said first circuit board, said second means having at least two connections (708) to receive said second power supply output terminal (906) and being located adjacent to and on either side of the connection (704) of said first power supply output terminal and corresponding to said second power supply output terminal connections (706) on said first circuit board;

said connections of said first and second circuit boards being arranged so that the mislocation

of said second circuit board in said first circuit board resulting in the connection of system power to system ground, thereby causing the current flow from said power supply (902) to exceed acceptable limits, initiating said limiting means (604) to shut down the supply of power.

2. The computer system of claim 1, wherein said current limiting means (904) comprises an electrical fuse.
3. The computer system of claim 1, wherein said current limiting means (904) comprises an electrical circuit breaker.
4. The computer system of claim 1, wherein said current limiting means (904) includes current limiting circuitry which, upon sensing an excessive current flow, turns off said power supply.
5. The computer system of claim 1, wherein said current sensing circuitry includes means for resetting said power supply.
6. The computer system of claim 1 wherein said locating and connecting means comprise a pin and a mating socket.

Patentansprüche

1. Computersystem mit einer Einrichtung zum Schützen elektronischer Leiterplatten in dem Computersystem während des Zufügens von Leiterplatten, mit:

einer Computerleistungsversorgung (902) mit zumindest ersten und zweiten Ausgangsanschlüssen (906, 908), gekennzeichnet durch:

eine Einrichtung (904) zum Begrenzen des Stromflusses in der Leistungsversorgung;

eine erste Leiterplatte mit einer ersten Einrichtung (306) zum Erkennen und Verbinden zusätzlicher zweiter Leiterplatten, wobei die erste Einrichtung eine Verbindung (702) für den ersten Leistungsversorgungsanschluß (908) aufweist und zumindest zwei Verbindungen (706) aufweist, die nahe bei und zu jeder Seite von der Verbindung (702) für den ersten Leistungsversorgungsanschluß angeordnet sind, wobei beide Verbindungen (706) mit dem zweiten Leistungsversorgungsausgangsanschluß (906) verbunden sind;

eine zweite Leiterplatte mit einer zweiten Einrichtung (404) zum Erkennen und Verbinden

$$A_{n-1} - B_1$$

$$A_n - B_0$$

Where this direct connection would result in connecting a power ground or control line to an address line, board runs would be created as shown in fig. 8 for address line A_4 .

Claims

1. A computer system having means for protecting electronic circuit boards in the computer system during the addition of circuit boards comprising:

a computer power supply (902) having at least a first and a second output terminal (906,908); characterized by:

a means (904) for limiting current flow in said power supply;

a first circuit board (300) having first means (306) for locating and connecting additional second circuit boards, said first means (306) having a connection (702) for said first power supply output terminal (908) and at least two connections (706) being located adjacent to and on either side of the connection (702) for said first power supply output terminal, both of said connections (706) being connected to said second power supply output terminal (906);

a second circuit board having second means (404) for locating and connecting said second circuit board to said first circuit board, said second means having at least one connection (704) to receive said first power supply output terminal (908) and corresponding to said first power supply output terminal connection (702) on said first circuit board, said second means having at least two connections (708) to receive said second power supply output terminal (906) and being located adjacent to and on either side of the connection (704) of said first power supply output terminal and corresponding to said second power supply output terminal connections (706) on said first circuit board;

said connections of said first and second circuit boards being arranged so that the mislocation

of said second circuit board in said first circuit board resulting in the connection of system power to system ground, thereby causing the current flow from said power supply (902) to exceed acceptable limits, initiating said limiting means (604) to shut down the supply of power.

2. The computer system of claim 1, wherein said current limiting means (904) comprises an electrical fuse.
3. The computer system of claim 1, wherein said current limiting means (904) comprises an electrical circuit breaker.
4. The computer system of claim 1, wherein said current limiting means (904) includes current limiting circuitry which, upon sensing an excessive current flow, turns off said power supply.
5. The computer system of claim 1, wherein said current sensing circuitry includes means for resetting said power supply.
6. The computer system of claim 1 wherein said locating and connecting means comprise a pin and a mating socket.

Patentansprüche

1. Computersystem mit einer Einrichtung zum Schützen elektronischer Leiterplatten in dem Computersystem während des Zufügens von Leiterplatten, mit:

einer Computerleistungsversorgung (902) mit zumindest ersten und zweiten Ausgangsanschlüssen (906, 908), gekennzeichnet durch:

eine Einrichtung (904) zum Begrenzen des Stromflusses in der Leistungsversorgung;

eine erste Leiterplatte mit einer ersten Einrichtung (306) zum Erkennen und Verbinden zusätzlicher zweiter Leiterplatten, wobei die erste Einrichtung eine Verbindung (702) für den ersten Leistungsversorgungsanschluß (908) aufweist und zumindest zwei Verbindungen (706) aufweist, die nahe bei und zu jeder Seite von der Verbindung (702) für den ersten Leistungsversorgungsanschluß angeordnet sind, wobei beide Verbindungen (706) mit dem zweiten Leistungsversorgungsausgangsanschluß (906) verbunden sind;

eine zweite Leiterplatte mit einer zweiten Einrichtung (404) zum Erkennen und Verbinden



der zweiten Leiterplatte mit der ersten Leiterplatte, wobei die zweite Einrichtung zumindest eine Verbindung (704) zum Empfangen des ersten Leistungsverorgungsausgangsanschlusses (908) aufweist und dem ersten Leistungsverorgungsausgangsanschluß (702) auf der ersten Leiterplatte entspricht, wobei die zweite Einrichtung zumindest zwei Verbindungen (708) zum Empfangen des zweiten Leistungsverorgungsausgangsanschlusses (906) aufweist, die nahe bei und zu jeder Seite der Verbindung (704) des ersten Leistungsverorgungsausgangsanschlusses angeordnet sind und den zweiten Leistungsverorgungsausgangsanschlußverbindungen (706) auf der ersten Leiterplatte entsprechen;

wobei die Verbindungen der ersten und zweiten Leiterplatten so angeordnet sind, daß eine Fehl-anordnung der zweiten Leiterplatte in der ersten Leiterplatte in einer Verbindung der Systemversorgung mit der Systemmasse resultiert, wodurch ein Stromfluß von der Leistungsver-sorgung (902) akzeptierbare Grenzen übersteigt, wodurch die Begrenzungseinrichtung (604) aktiviert wird, die Leistungsver-sorgung auszuschalten.

2. Computersystem nach Anspruch 1, wobei die Strombegrenzungseinrichtung (904) eine elektrische Sicherung aufweist. 30
3. Computersystem nach Anspruch 1, wobei die Strombegrenzungseinrichtung (904) einen elektrischen Schutzschalter aufweist. 35
4. Computersystem nach Anspruch 1, wobei die Strombegrenzungseinrichtung (904) einen Strombegrenzungsschaltkreis enthält, welcher, wenn er einen übermäßigen Stromfluß feststellt, die Versorgungsleistung ausschaltet. 40
5. Computersystem nach Anspruch 1, wobei der Stromerkennungsschaltkreis eine Einrichtung zum Rücksetzen der Leistungsver-sorgung aufweist. 45
6. Computersystem nach Anspruch 1, wobei die Einrichtung zum Erkennen und Verbinden eine Buchse mit Stecker und Gegenstecker aufweist. 50

Revendications

1. Système d'ordinateur possédant des moyens destinés à protéger des cartes de circuits électroniques du système d'ordinateur au cours de l'ajout de cartes de circuits, comprenant : 55

une alimentation électrique (902) d'ordinateur possédant au moins une première et seconde bornes de sortie (906, 908) ; caractérisé par : des moyens (904) destinés à limiter le débit de courant dans ladite alimentation électrique ; une première carte de circuits possédant des premiers moyens (306) destinés à positionner et à connecter des secondes cartes de circuits supplémentaires, lesdits premiers moyens possédant une connexion (702) destinée à ladite première borne de sortie d'alimentation électrique (908) et au moins deux connexions (706) étant positionnées à proximité immédiate et sur un côté ou l'autre de la connexion (702) destinée à ladite première borne de sortie d'alimentation électrique, les deux desdites connexions (706) étant connectées à ladite seconde borne de sortie d'alimentation électrique (906) ; une seconde carte de circuits possédant des seconds moyens (404) destinés à positionner et à connecter ladite seconde carte de circuits à ladite première carte de circuits, lesdits seconds moyens possédant au moins une connexion (704) destinée à recevoir ladite première borne de sortie d'alimentation électrique (908) et correspondant à ladite connexion de la première borne de sortie d'alimentation électrique (702) sur ladite première carte de circuits, lesdits seconds moyens possédant au moins deux connexions (708) destinées à recevoir ladite seconde borne de sortie d'alimentation électrique (906) et étant positionnées à proximité immédiate et sur un côté ou l'autre de la connexion (704) de ladite première borne de sortie d'alimentation électrique et correspondant auxdites connexions de la seconde borne de sortie d'alimentation électrique (706) sur ladite première carte de circuits ; lesdites connexions desdites première et seconde cartes de circuits étant disposées de manière telle que le mauvais positionnement de ladite seconde carte de circuits dans ladite première carte de circuits se traduit par la connexion de l'alimentation électrique du système à la masse du système, amenant ainsi le débit de courant provenant de ladite alimentation électrique (902) à dépasser des limites acceptables, initiant lesdits moyens de limitation (604) pour mettre hors service l'alimentation électrique.

2. Système d'ordinateur selon la revendication 1, dans lequel lesdits moyens de limitation de courant (904) comportent un fusible électrique.
3. Système d'ordinateur selon la revendication 1, dans lequel lesdits moyens de limitation de courant (904) comportent un disjoncteur électrique.



4. Système d'ordinateur selon la revendication 1, dans lequel lesdits moyens de limitation de courant (904) comportent des circuits de limitation de courant qui, lors de la détection d'un débit de courant excessif, mettent hors service ladite alimentation électrique. 5
5. Système d'ordinateur selon la revendication 1, dans lequel lesdits circuits de détection de courant comportent des moyens destinés à réenclencher ladite alimentation électrique. 10
6. Système d'ordinateur selon la revendication 1, dans lequel lesdits moyens de positionnement et de connexion comportent une broche et une douille de raccordement. 15

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