

CIRCUIT AND METHOD OF INTERCONNECTING CONTENT
ADDRESSABLE MEMORY

Abstract of the Disclosure

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A data compressor (14) generates codewords representative of the location and length of a string match between an input data stream and a CAM array vocabulary table (22). A data decompressor (16) looks up the codewords for a string match in
10 its vocabulary table. The CAM array (50,52,54,56) is arranged in a serpentine configuration to reduce track layout. A column priority encoder (74) reverses the priority of alternate rows to maintain the logical flow through the CAM array. The CAM array uses a flipflop (122) with a common control circuit to transfer
15 and refresh data through the flipflop.

FIG. 2 accompanies this abstract.