NET BLOCK ASSEMBLY

ABSTRACT

The present invention relates to a net block assembly in a system for positioning singulated integrated circuits, having a net block surface partially covered by a two dimensional array of first and second elements, each first element having an aperture provided with vacuum adapted to receive an integrated circuit and each second element projecting from the surface said net block assembly including a plurality of rows of first and second element arranged in a chequer pattern, wherein the number of elements in the row is an odd number and a buffer row in the array having two first elements arranged adjacent to each other, wherein the number of elements in the buffer row is an odd number.

Figure 8a and 8b