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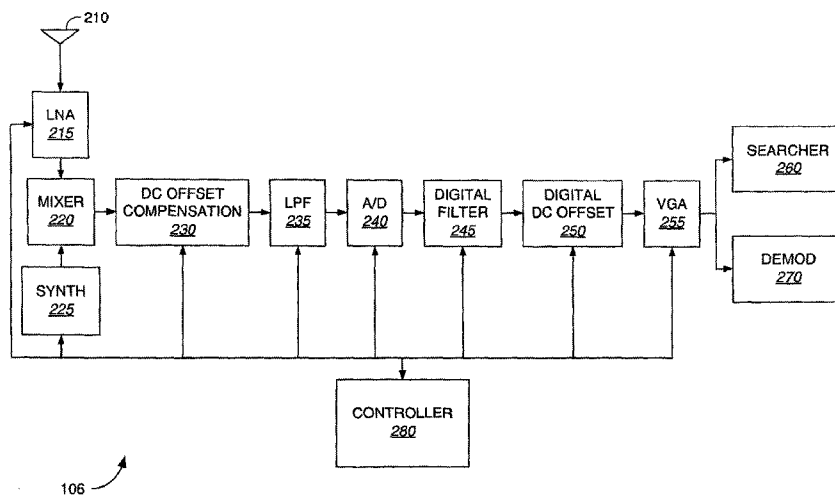
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(54) Title: LOW LATENCY FREQUENCY SWITCHING



(57) Abstract: Techniques for improved low latency frequency switching are disclosed. In one embodiment, a controller (510) receives a frequency switch command and generates a frequency switch signal at a time determined in accordance with a system timer (520). In another embodiment, gain calibration is initiated subsequent to the frequency switch signal delayed by the expected frequency synthesizer settling time. In yet another embodiment, DC cancellation control (540) and gain control (530) are iterated to perform gain calibration, with signaling to control the iterations without need for processor (550) intervention. Various other embodiments are also presented. Aspects of the embodiments disclosed may yield the benefit of reducing latency during frequency switching, allowing for increased measurements at alternate frequencies, reduced time spent on alternate frequencies, and the capacity and throughput improvements that follow from minimization of disruption of an active communication session and improved neighbor selection.



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