

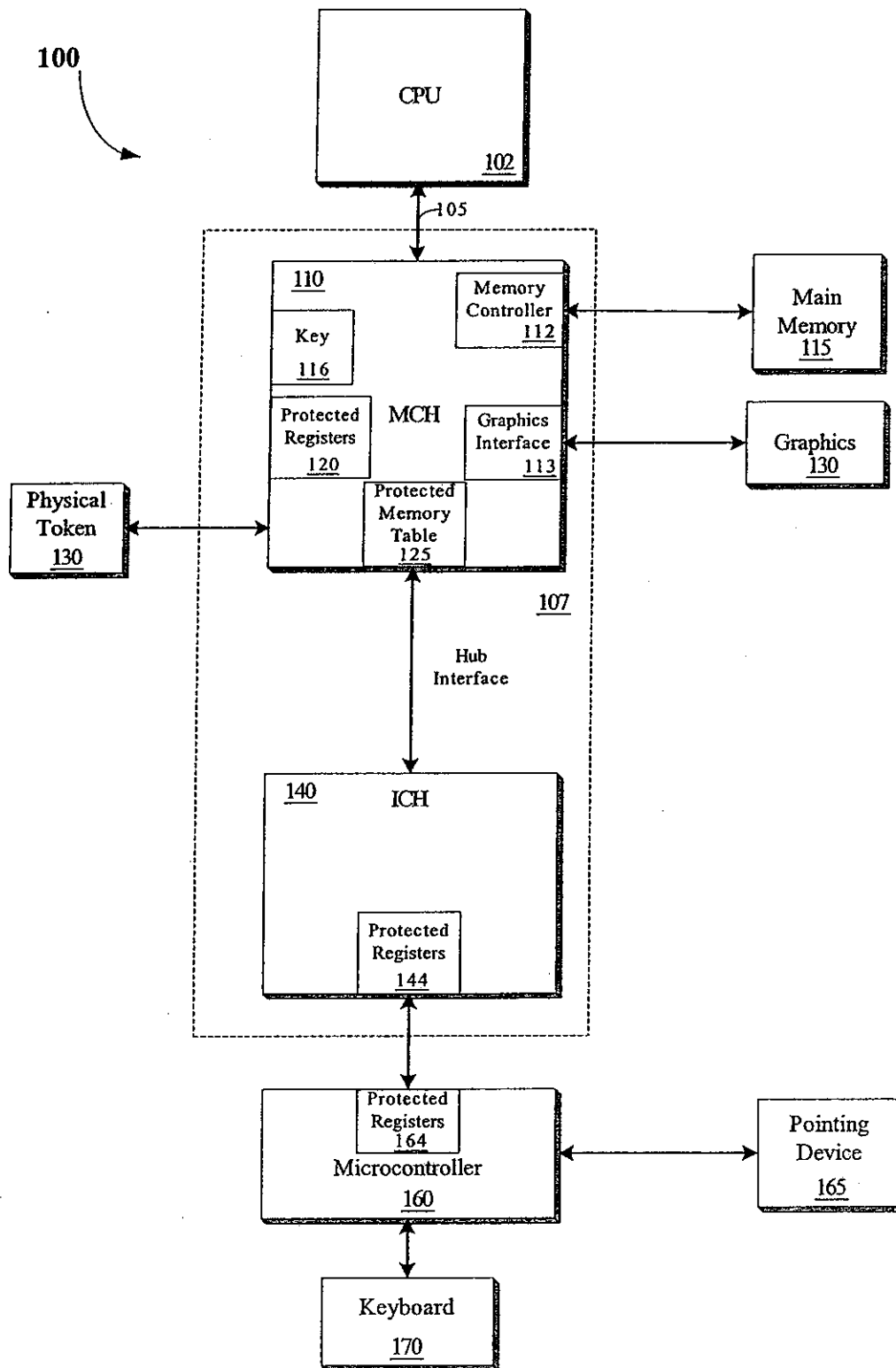


FIG. 1

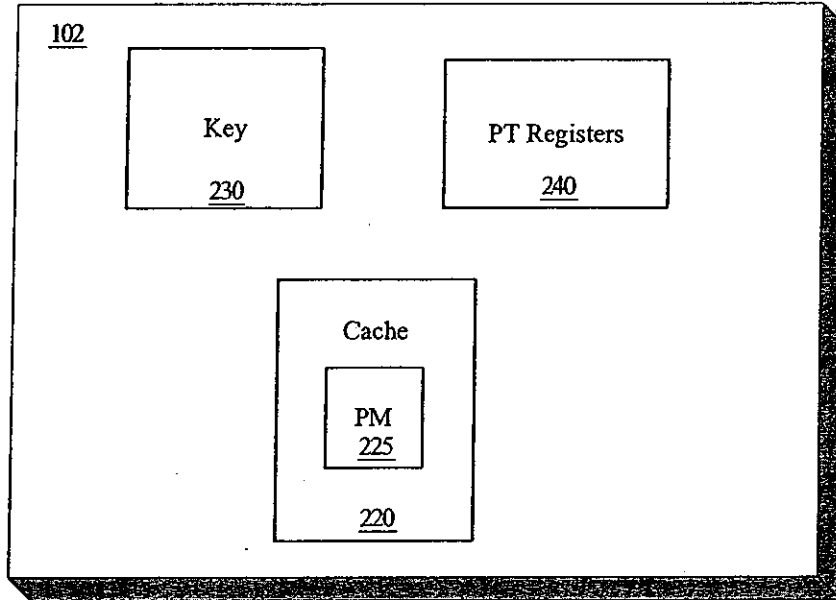


FIG. 2

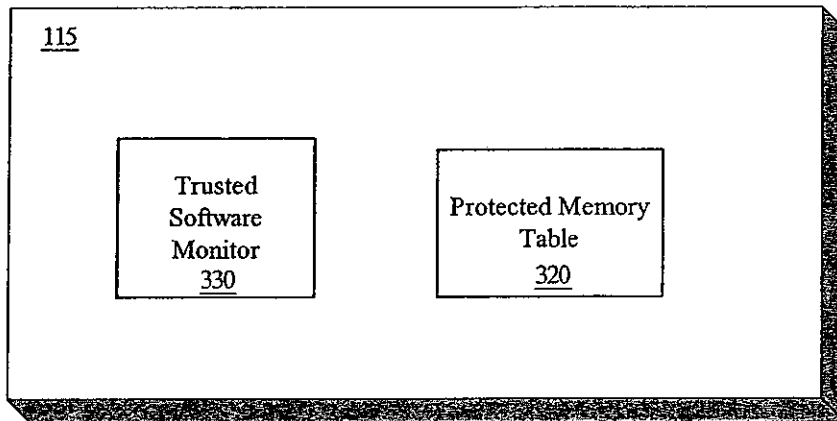


FIG. 3

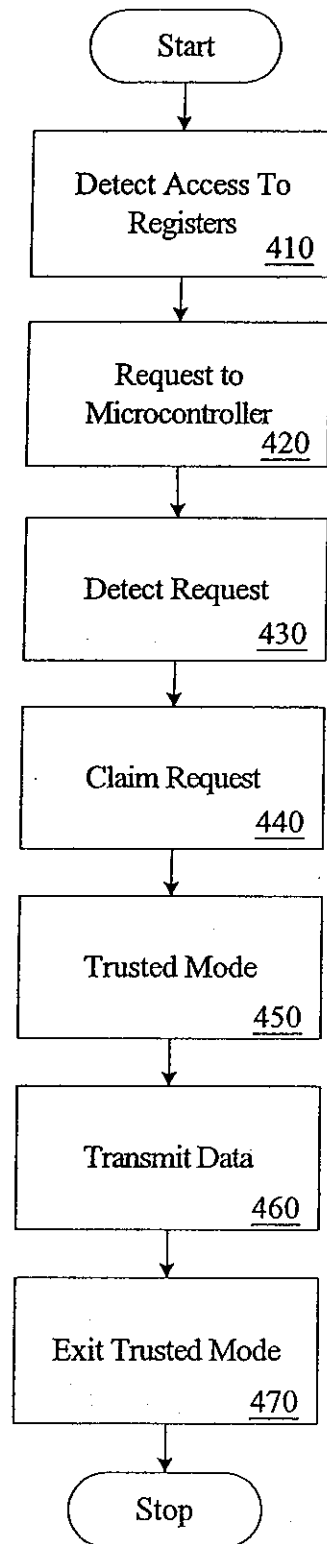
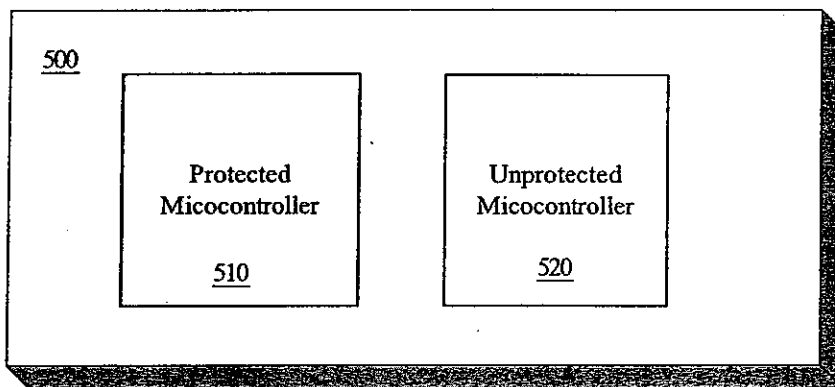
**FIG. 4**

FIG. 5 is a block diagram of a system 500 including a Protected Micocontroller 510 and an Unprotected Micocontroller 520.

**FIG. 5**