SIGNAL DISTURBANCE REDUCTION ARRANGEMENT FOR A LIQUID CRYSTAL DISPLAY

ABSTRACT

In an array of a liquid crystal display device having data lines and row select lines, a pair of conductors are provided in a direction transverse to that of the row select lines at opposite sides of the display device. The conductors are capacitively coupled to the row select lines. An amplifier is responsive to a signal generated in one of the conductors from capacitively coupled disturbance signals developed in the row select lines. The amplifier develops an output signal at the other conductor that is also capacitively coupled to the row select lines in a manner that tends to reduce the disturbance signals.

FIGRUE 6

This Sheet Has Been Replaced

