This Sheet Has Been Replaced



ABSTRACT OF THE DISCLOSURE

ACTIVE MATRIX DISPLAY DEVICE AND ITS DRIVING METHOD

To restrict a potential oscillation in a video line caused by a high speed sampling rate, the active matrix display device is comprised of gate lines X in row, signal lines Y in column and liquid crystal pixels LC of matrix arranged at each of the crossing points of both lines. The V driver 1 scans in line sequence each of the gate lines X and selects the liquid crystal pixels LC in one line for every one horizontal period. The H driver 2 performs in sequence samplings of the video signal VSIG within one horizontal scanning period to each of the signal lines Y and performs a writing of the video signal VSIG by dot sequential scanning to the liquid crystal pixels LC in one selected line. The precharging means 4 supplies in sequence the predetermined precharging signal VPS prior to the sequential sampling of the video signal VSIG for each of the signal lines Y. This precharging means 4 is comprised of a plurality of switching elements PSW connected to an end part of each of the signal lines Y, and of the P driver 5 for supplying the precharge signal VPS to each of the signal lines Y through sequential controlling of ON or OFF of each of the switching elements PSW.