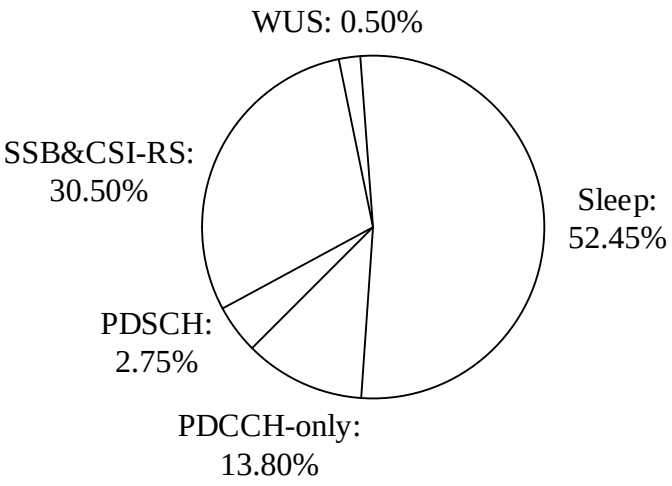

WUS: 0.50%
SSB&CSI-RS:
30.50%
Sleep:
52.45%
PDSCH:
2.75%
PDCCH-only:
13.80%

FIG. 1

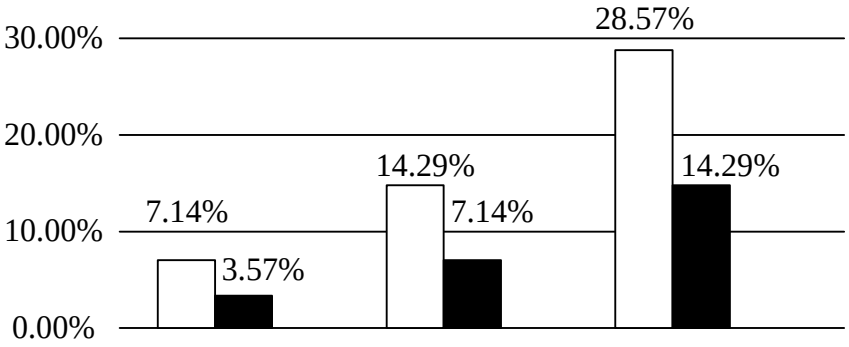

30.00%
20.00%
10.00%
0.00%
7.14%
3.57%
14.29%
7.14%
28.57%
14.29%

FIG. 2

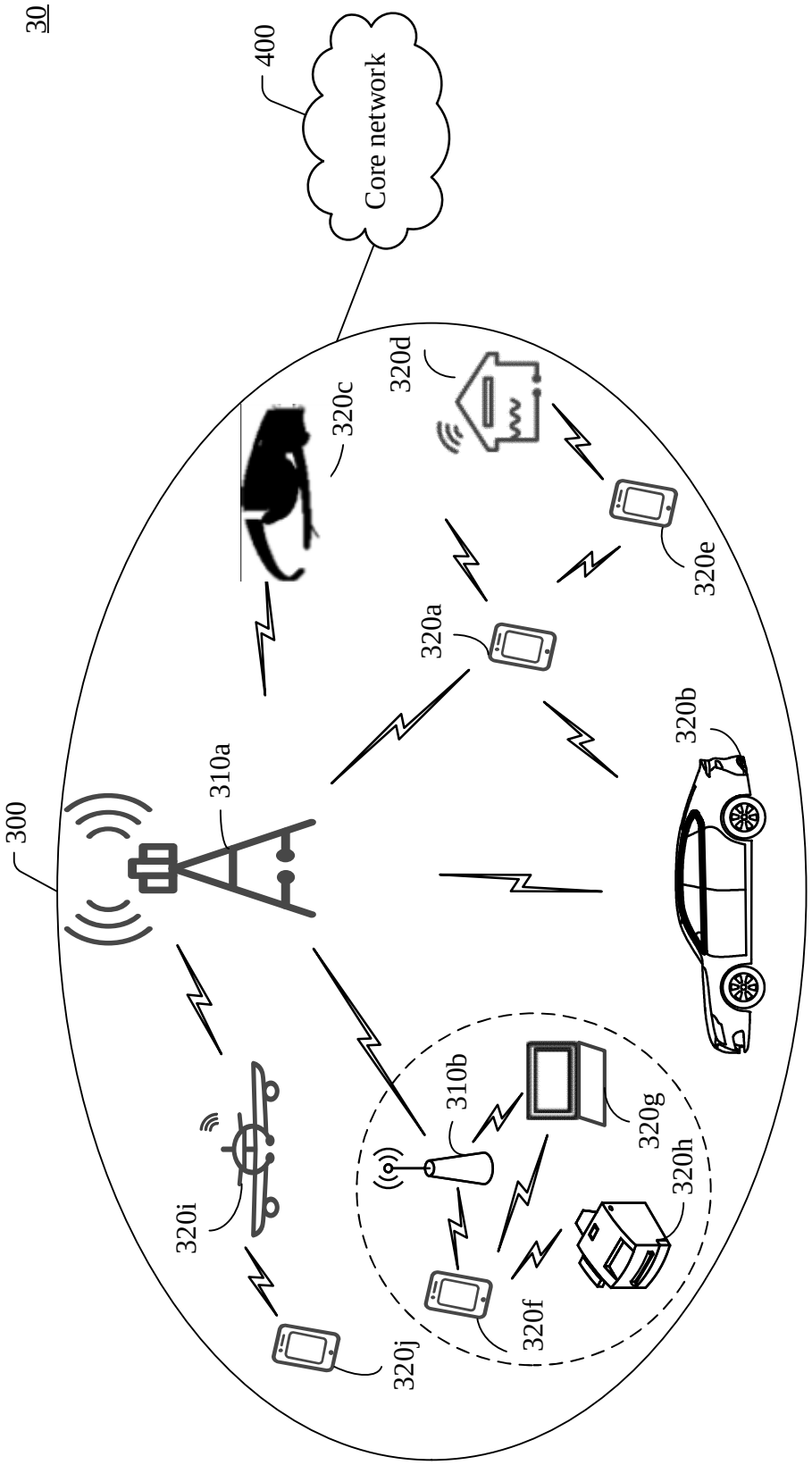

30
400
Core network
320d
320c
320e
320a
320b
310a
300
320i
310b
320g
320h
320f
320j

FIG. 3



FIG. 4

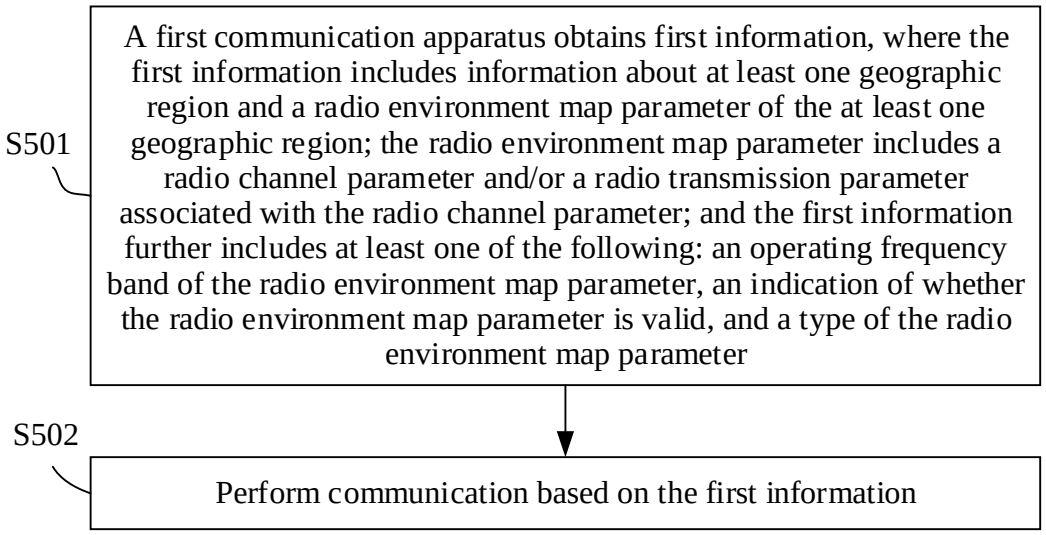


FIG. 5A

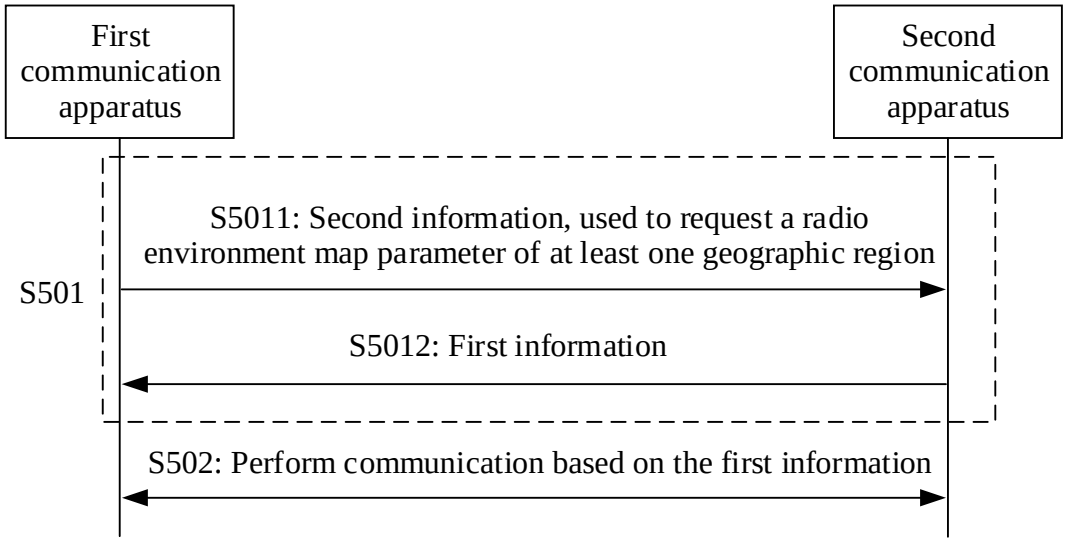


FIG. 5B

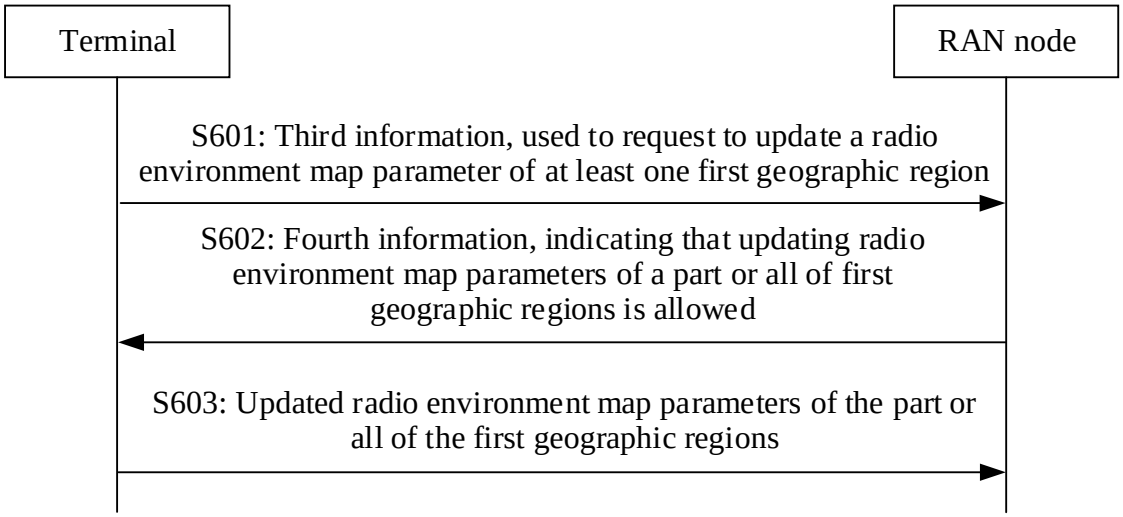


FIG. 6

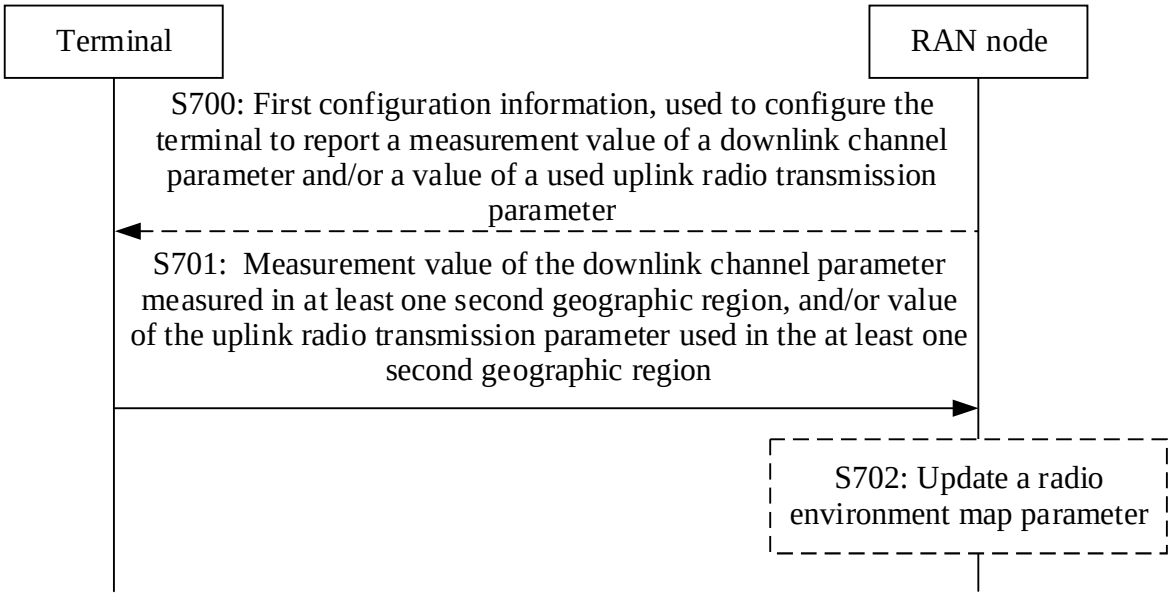


FIG. 7

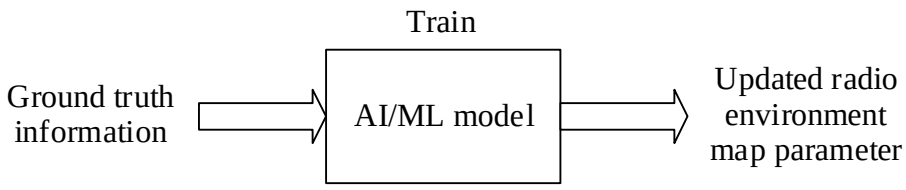


FIG. 8

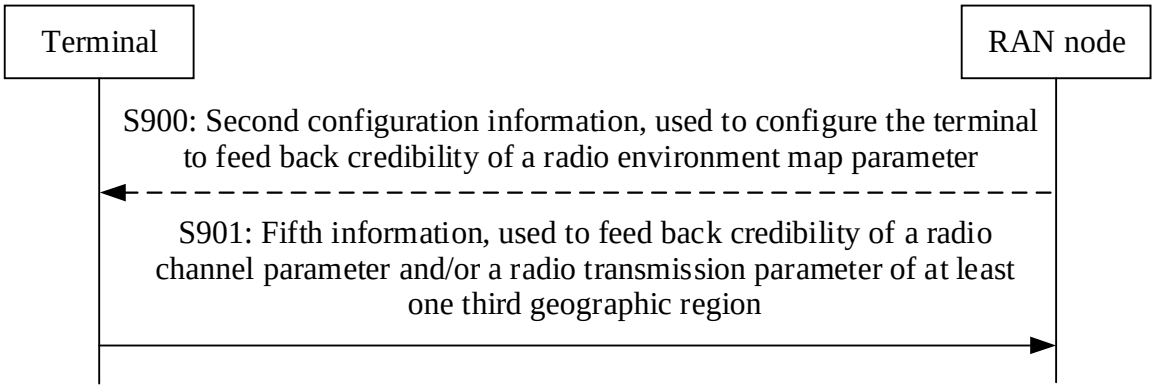


FIG. 9

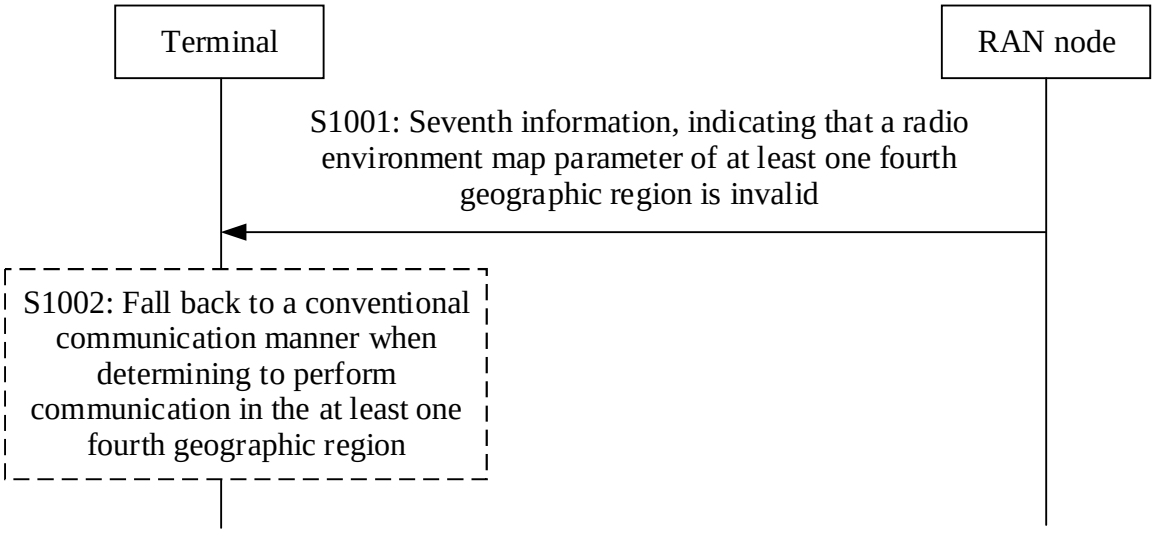


FIG. 10

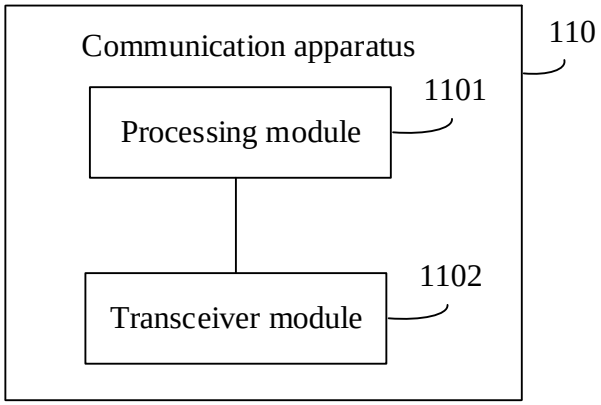


FIG. 11

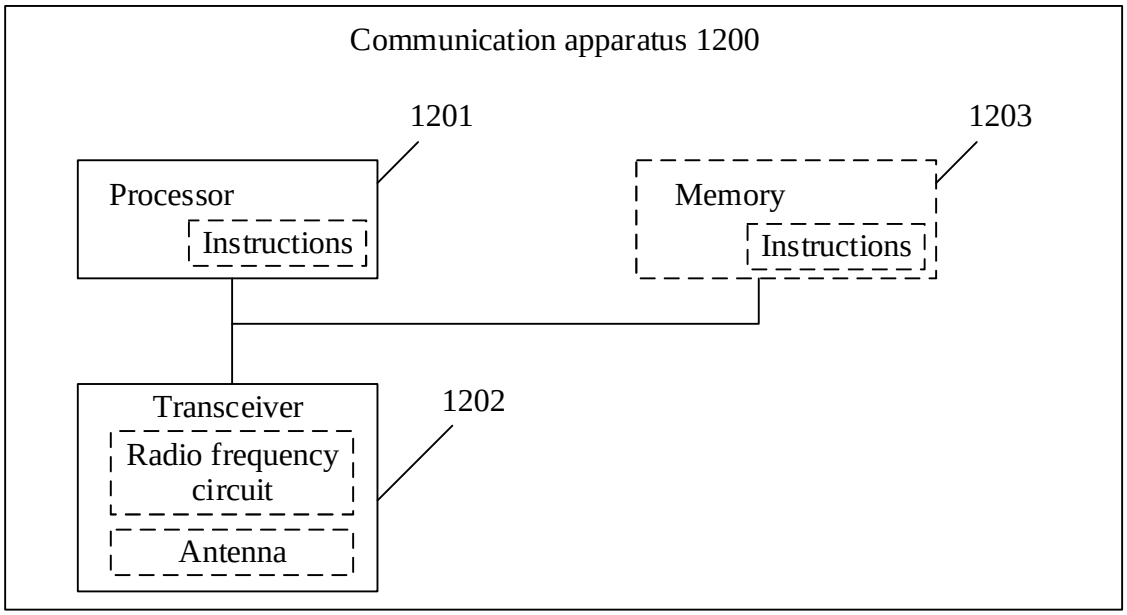

Communication apparatus 1200
1201
Processor
Instructions
1203
Memory
Instructions
Transceiver
1202
Radio frequency circuit
Antenna

FIG. 12

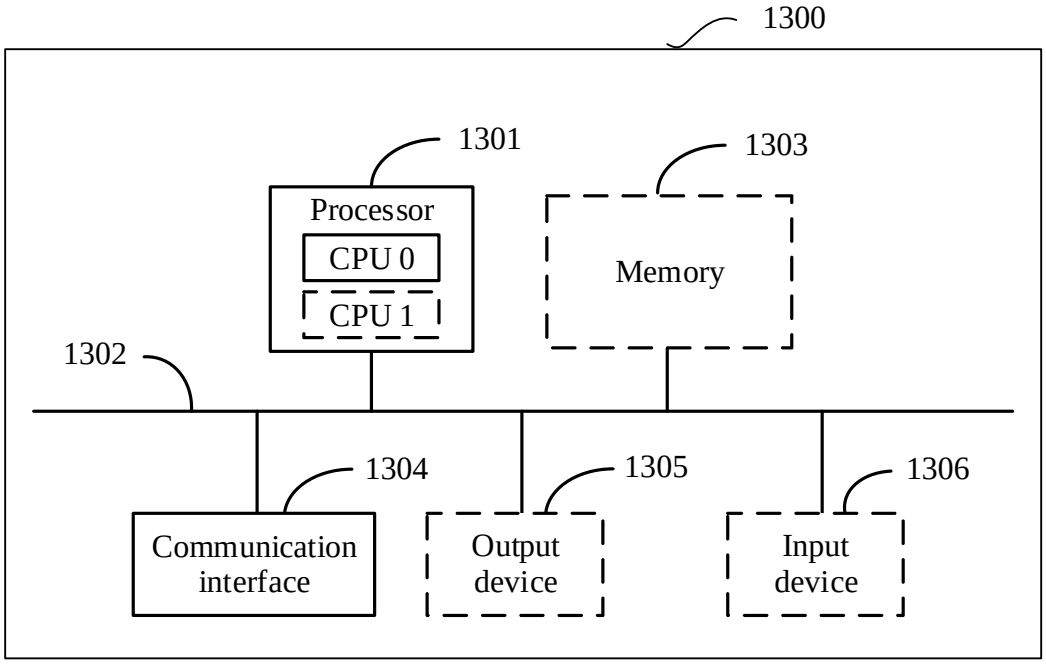

1300
1301
Processor
CPU 0
CPU 1
1303
Memory
1302
1304
Communication interface
1305
Output device
1306
Input device

FIG. 13