

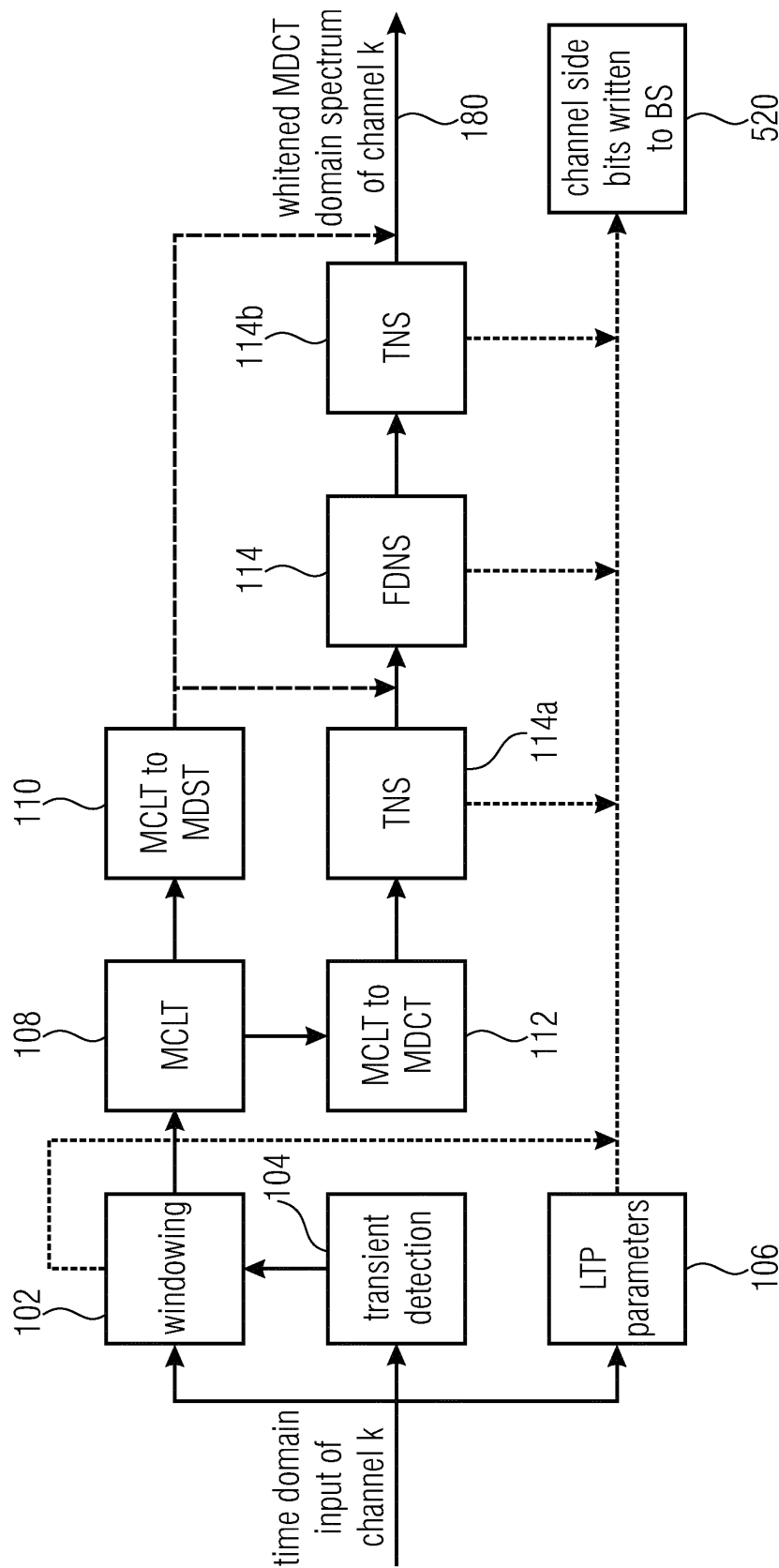


Fig. 1

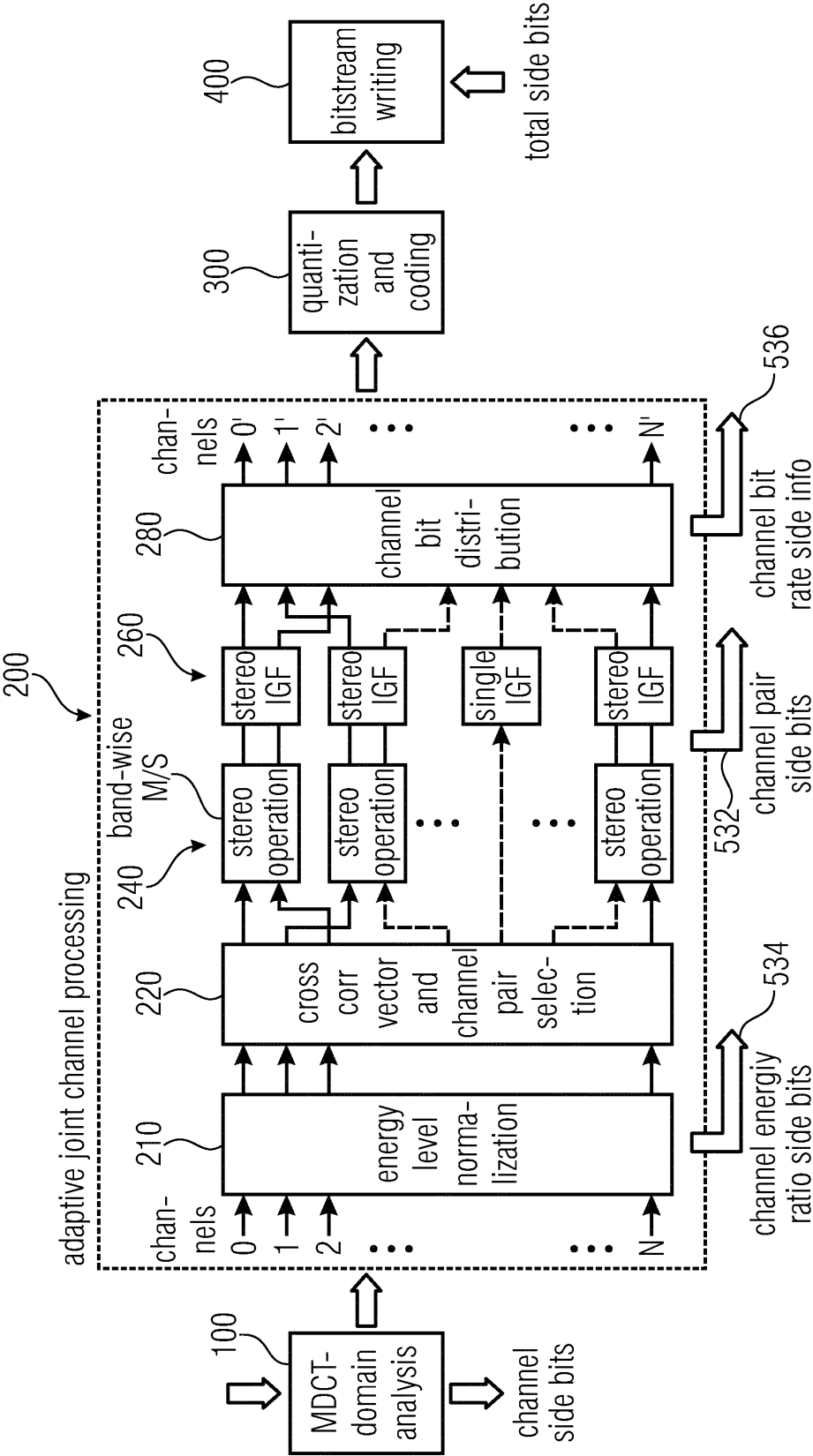


Fig. 2

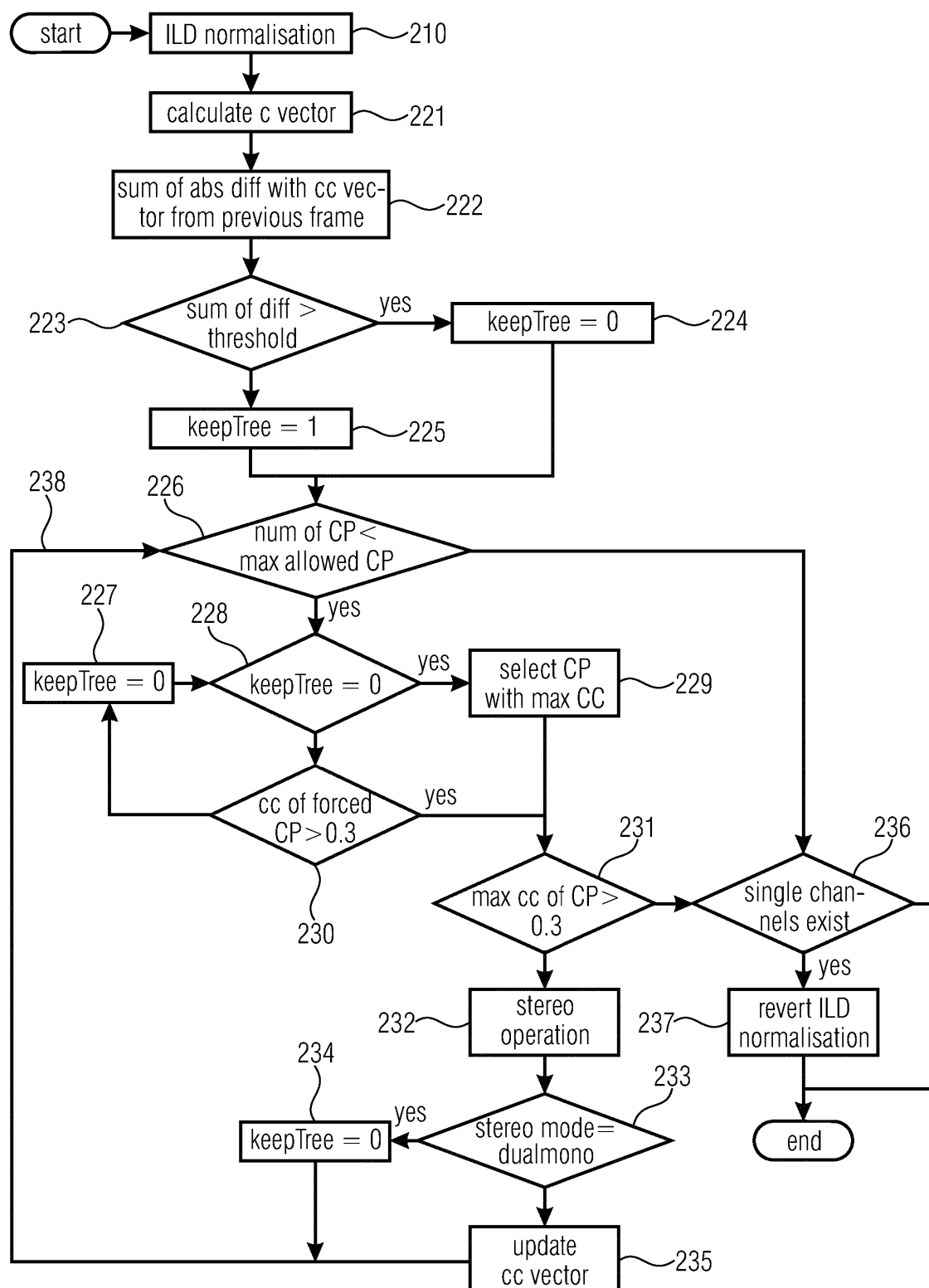


Fig. 3

definition for channel pair side bits

	0	1	2	3	4	5
0		0	1	2	3	4
1			5	6	7	8
2				9	10	11
3					12	13
4						14
5						

e.g. 6 channels
– 15 possibilities (from 0 to 14)
– 4 bits

Fig. 4

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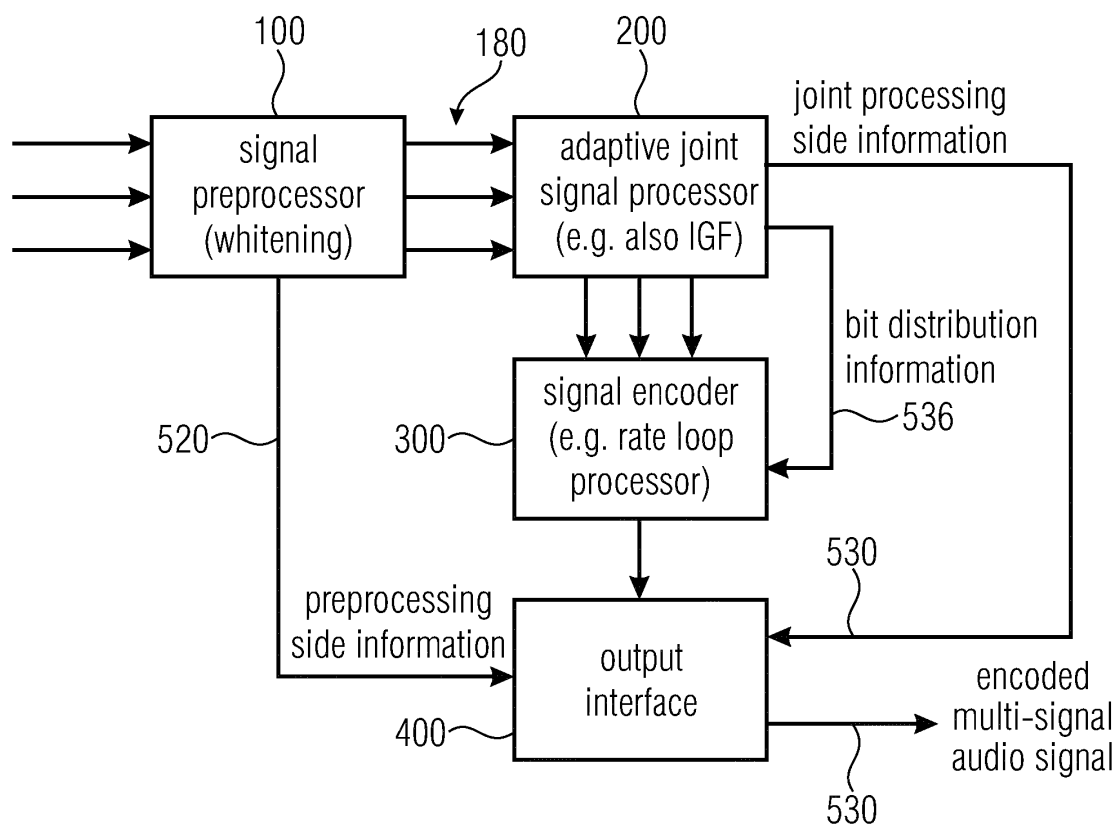


Fig. 5a

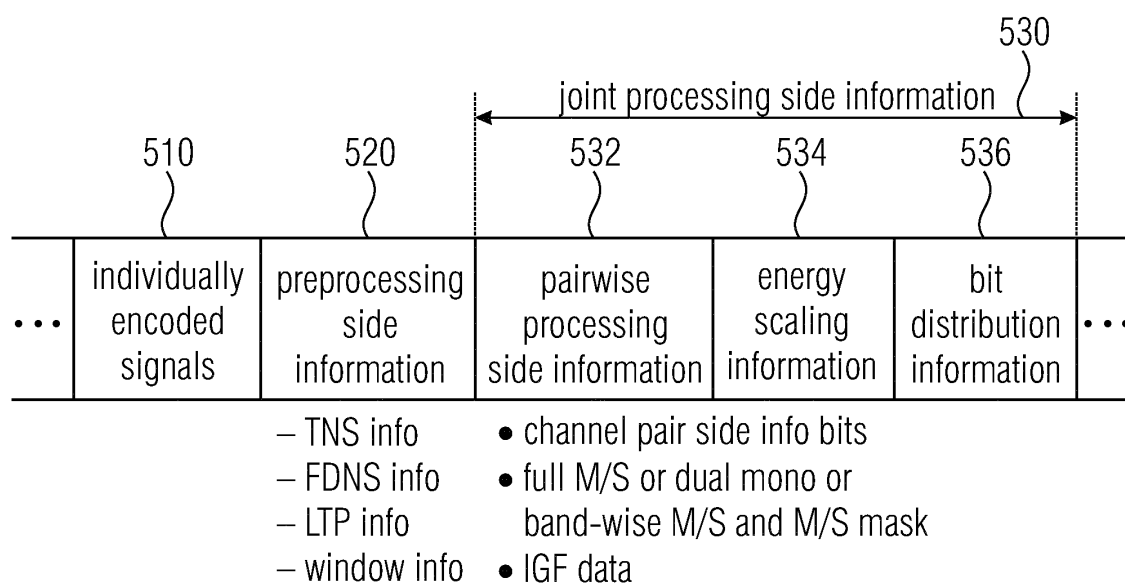


Fig. 5b

SUBSTITUTE SHEET (RULE 26)

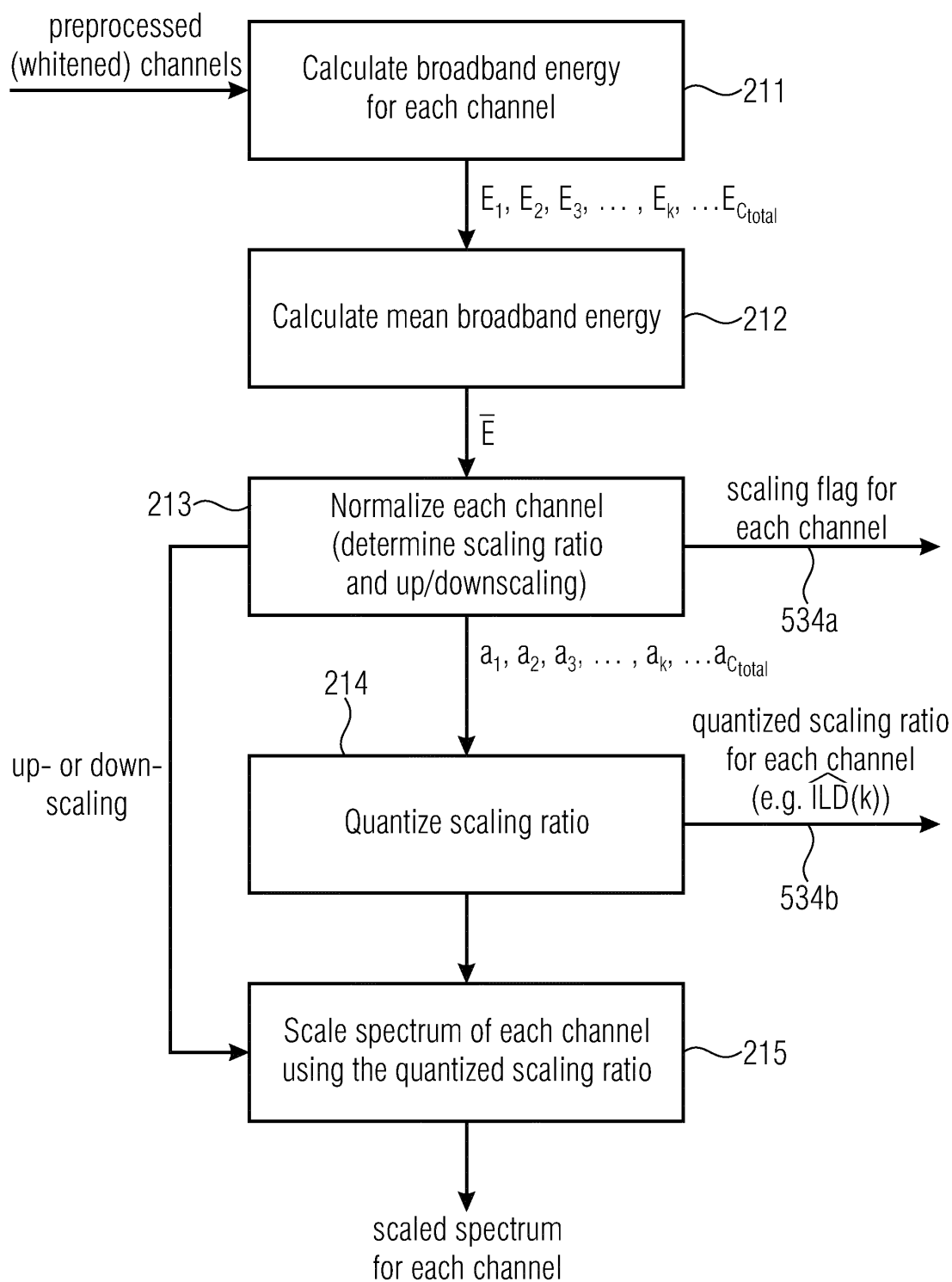


Fig. 6

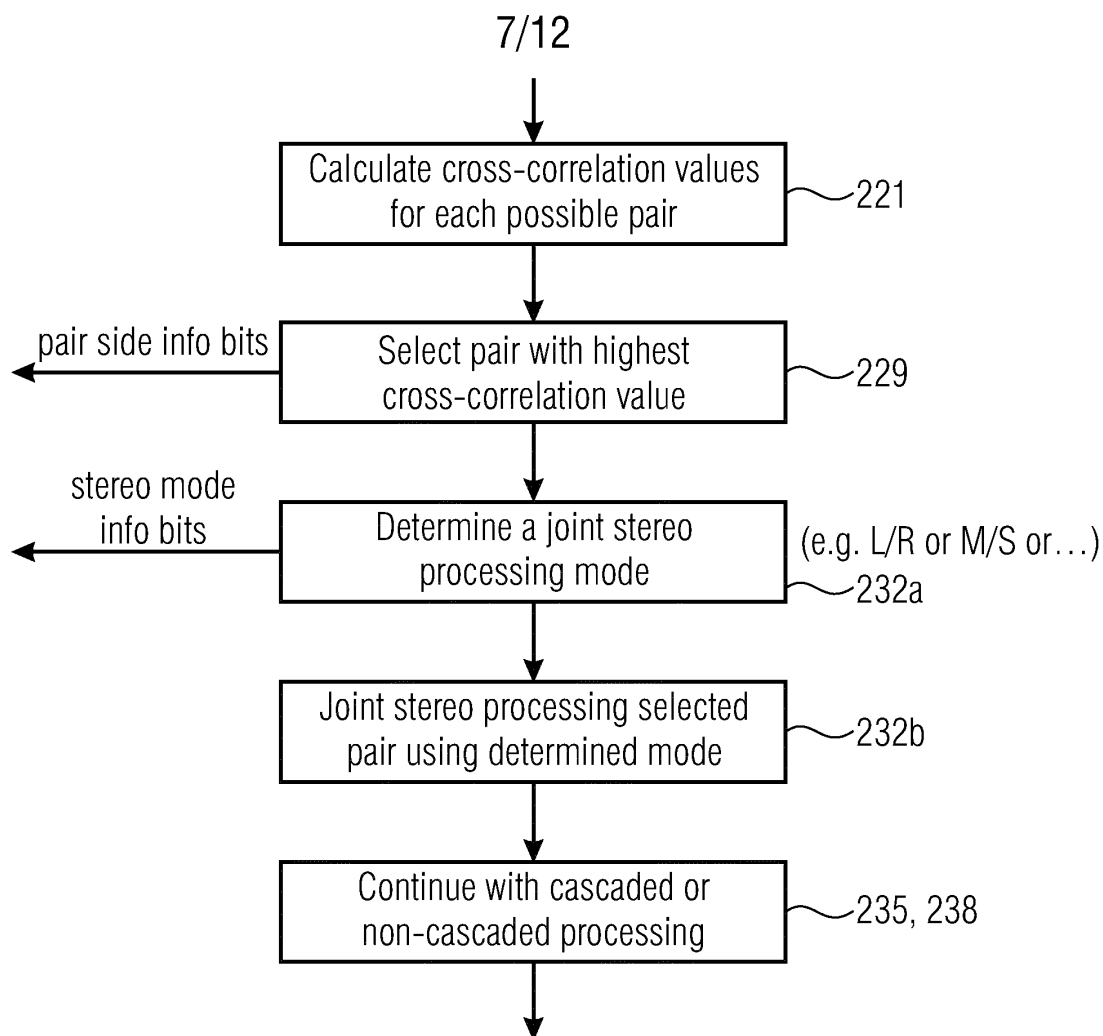


Fig. 7

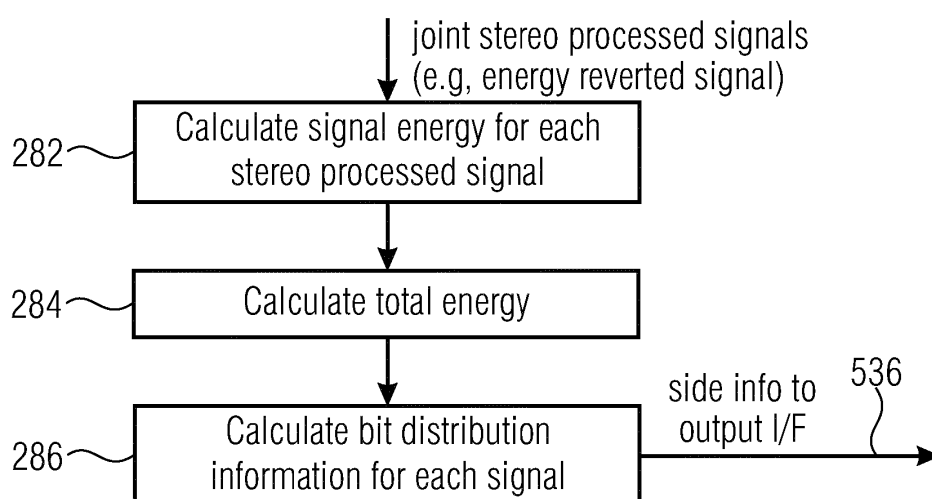


Fig. 8

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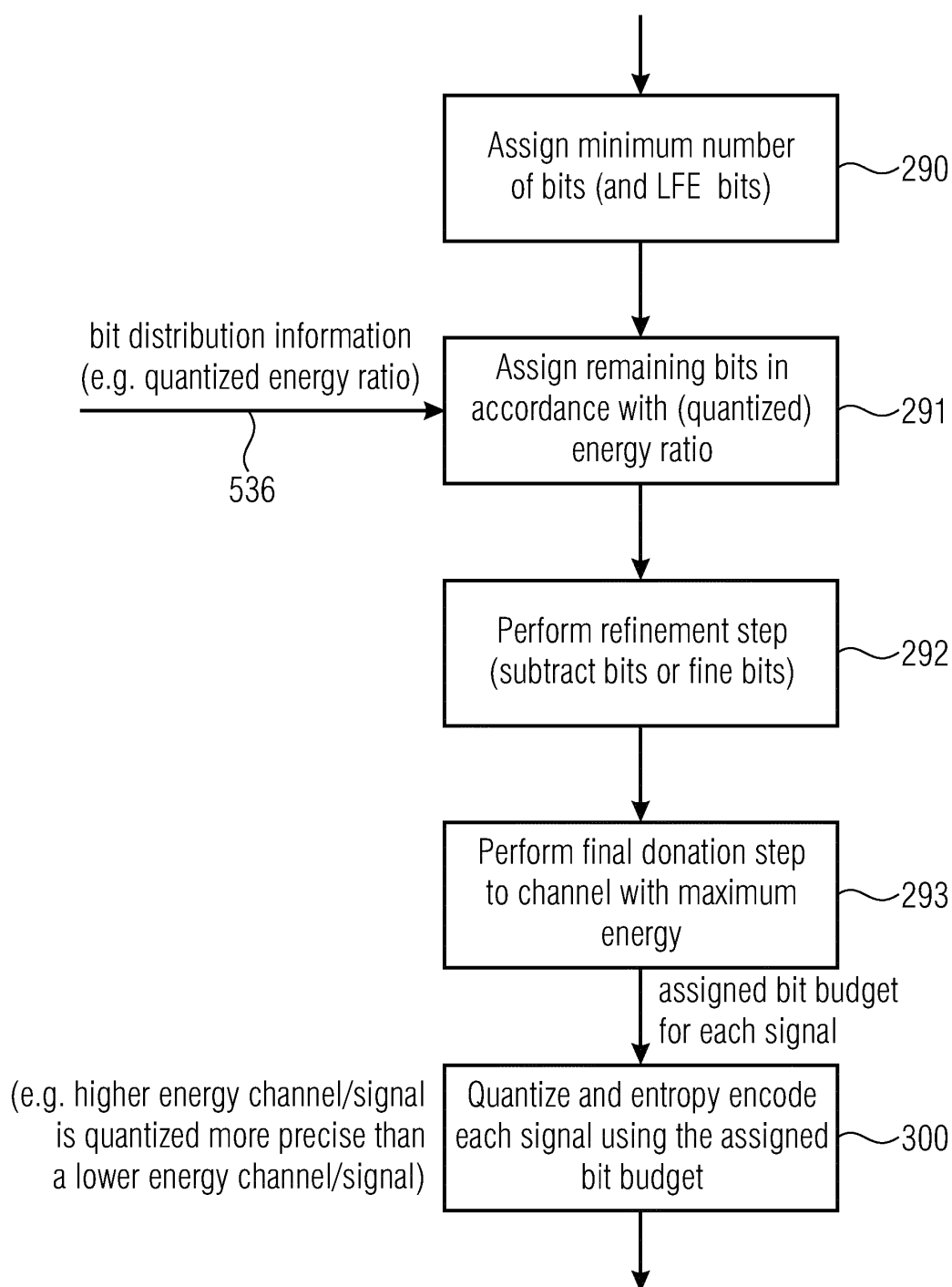


Fig. 9

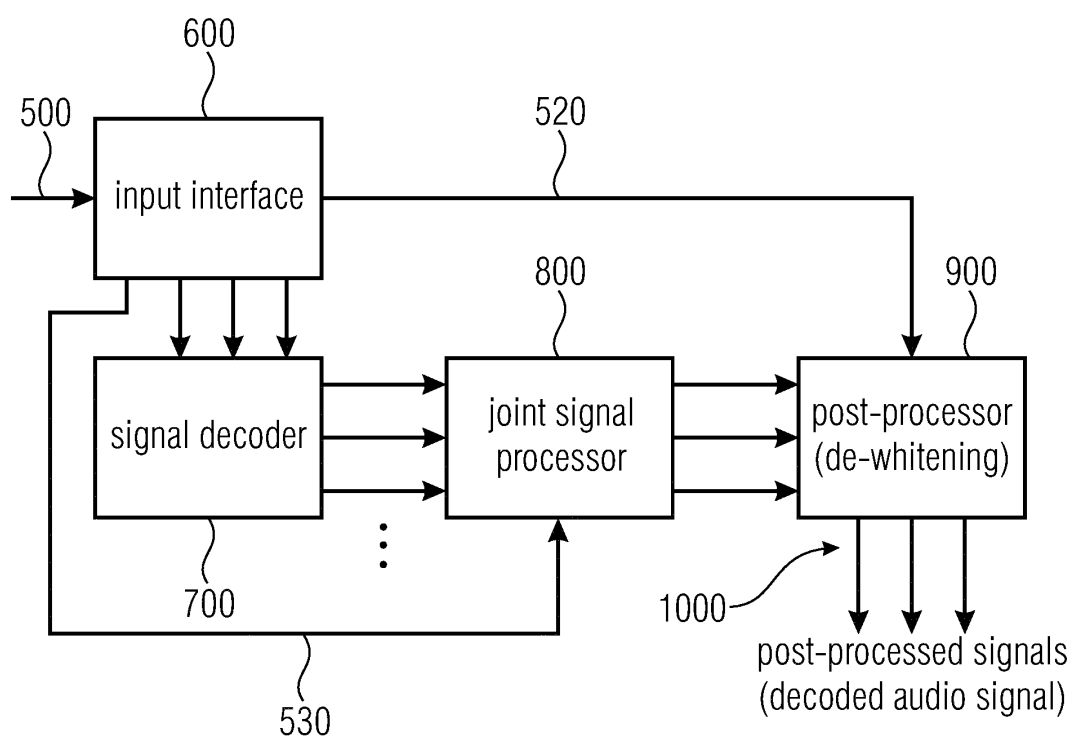


Fig. 10

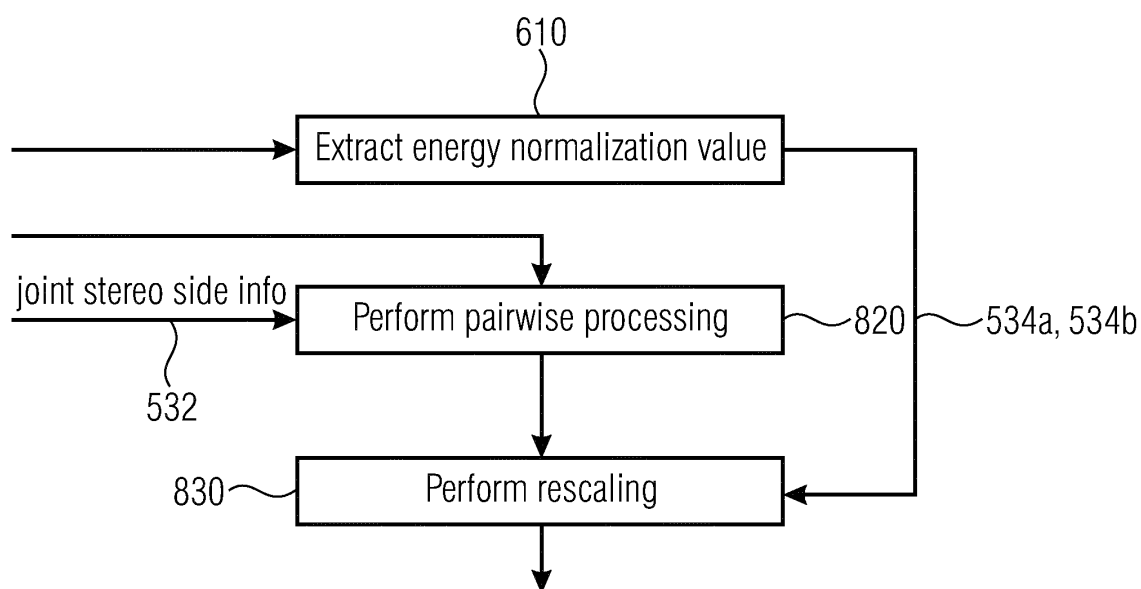


Fig. 11

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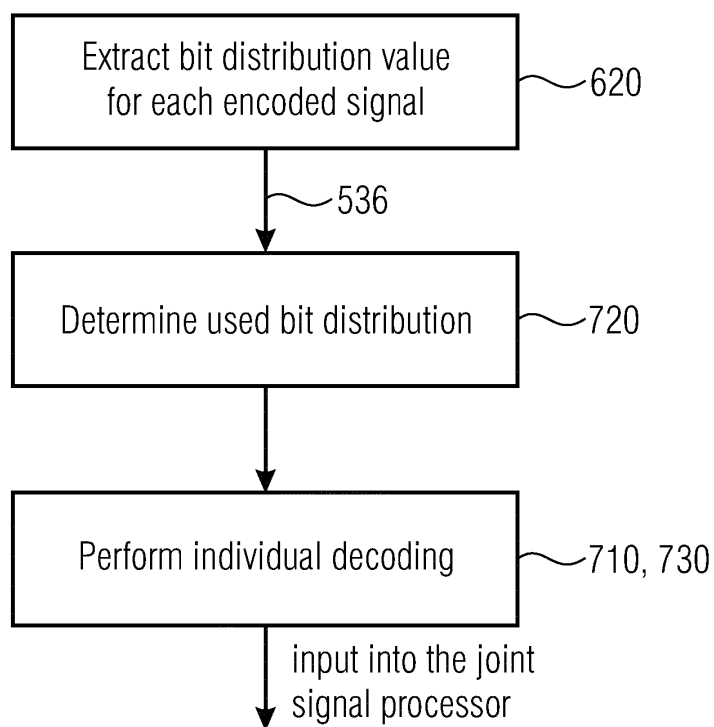


Fig. 12

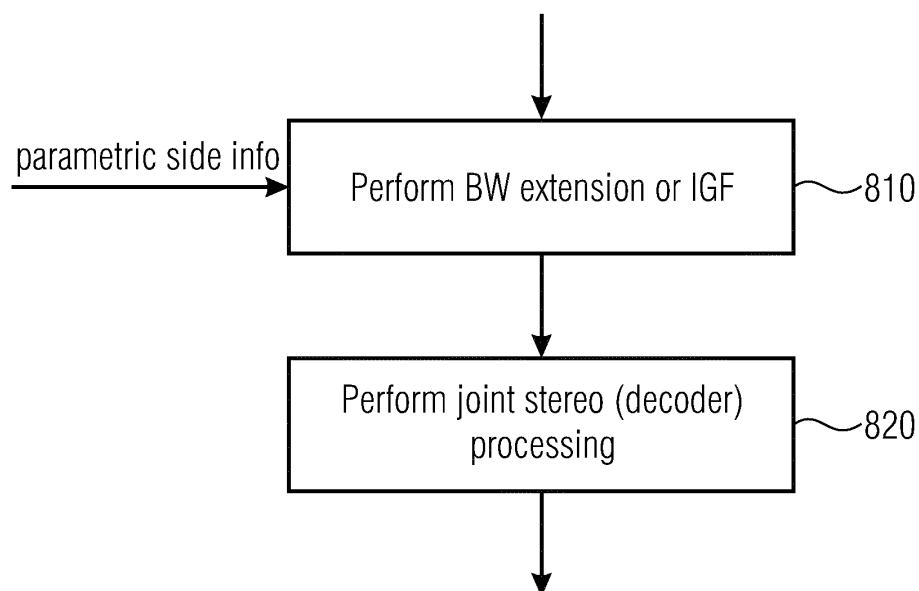


Fig. 13

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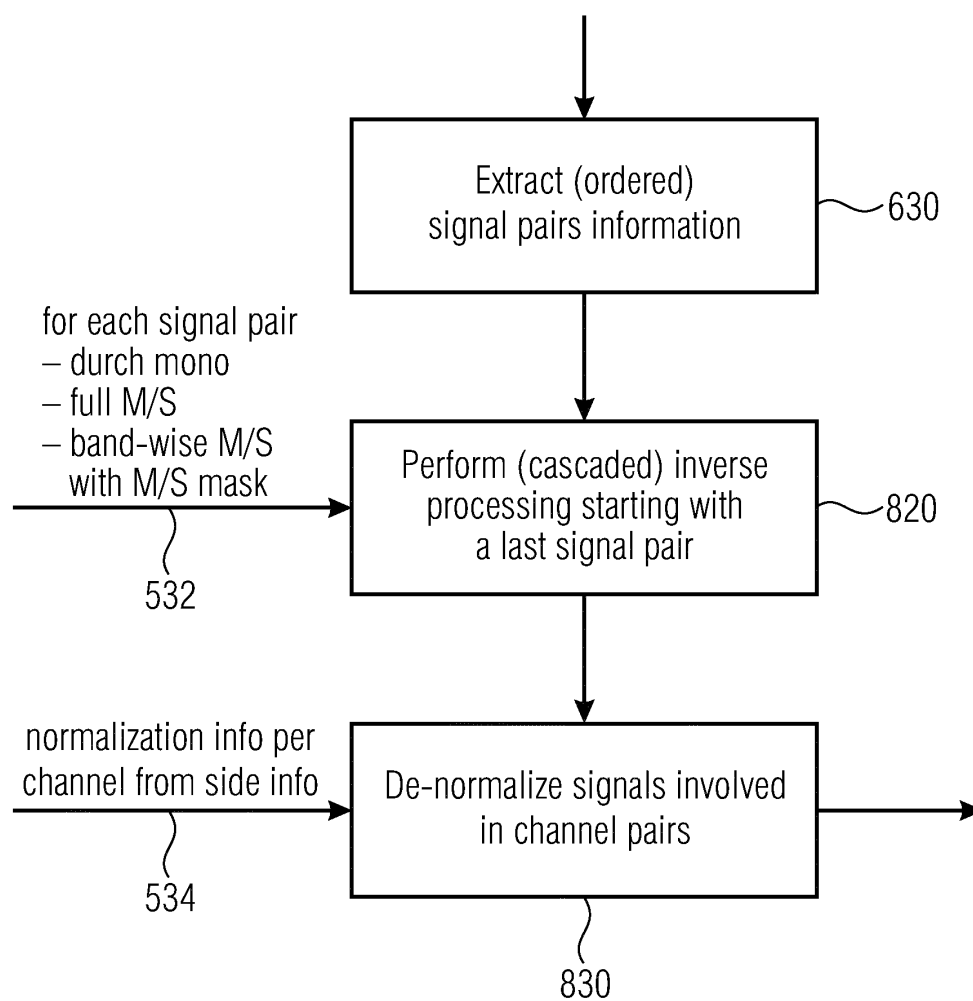


Fig. 14

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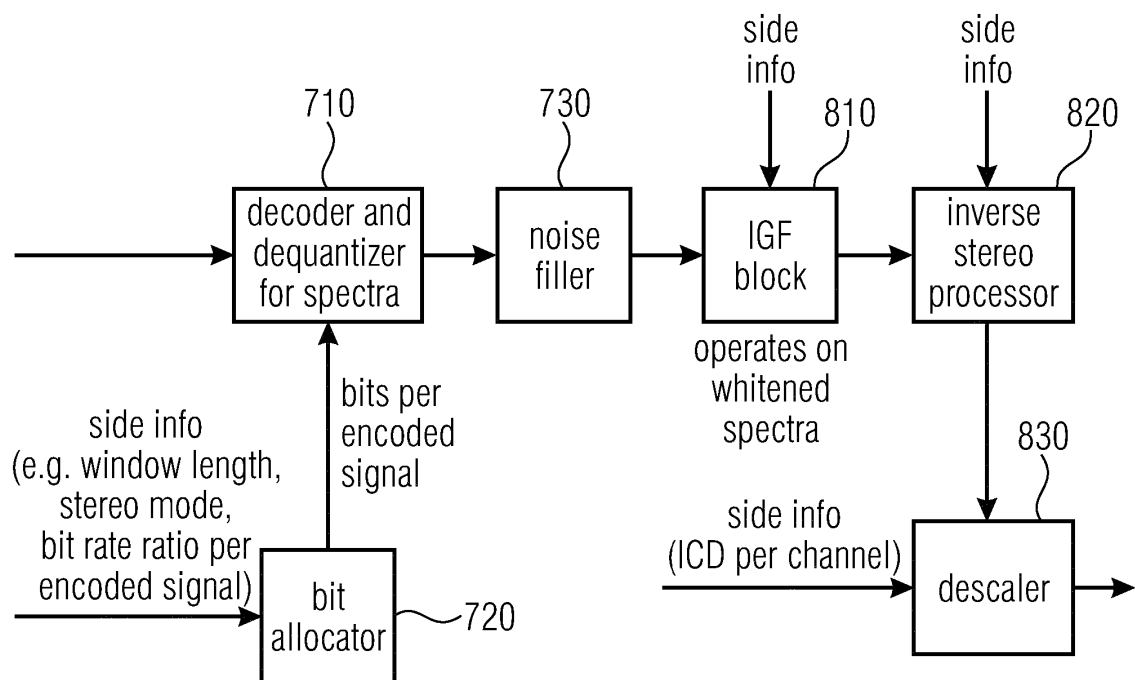


Fig. 15a

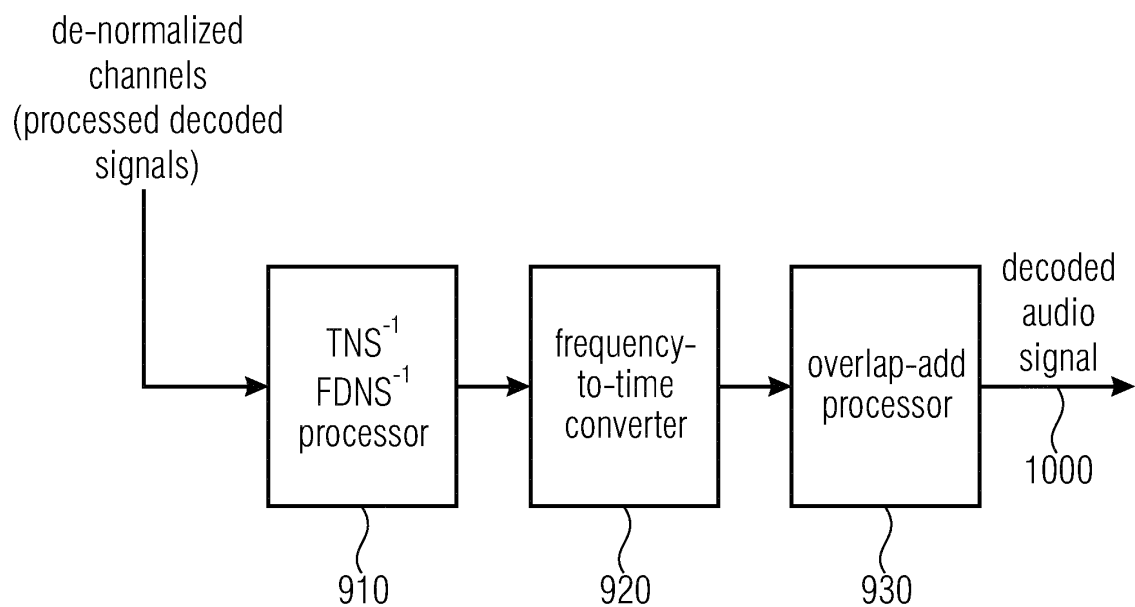


Fig. 15b