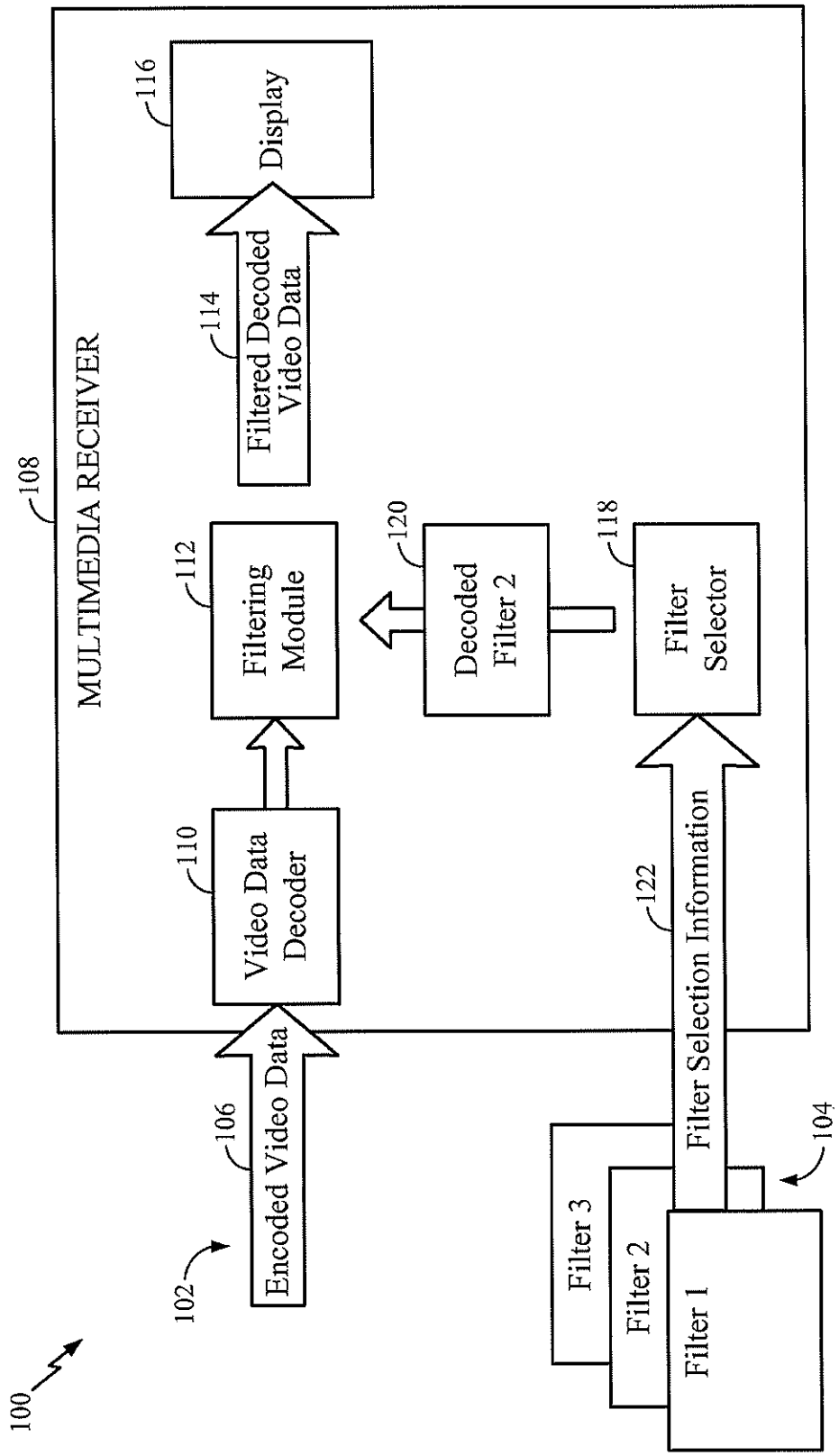

100
102
Encoded Video Data
106
MULTIMEDIA RECEIVER
108
Video Data Decoder
110
Filtering Module
112
Filtered Decoded Video Data
114
Display
116
Filter 1
Filter 2
Filter 3
104
Filter Selection Information
122
Filter Selector
118
Decoded Filter 2
120

FIG. 1

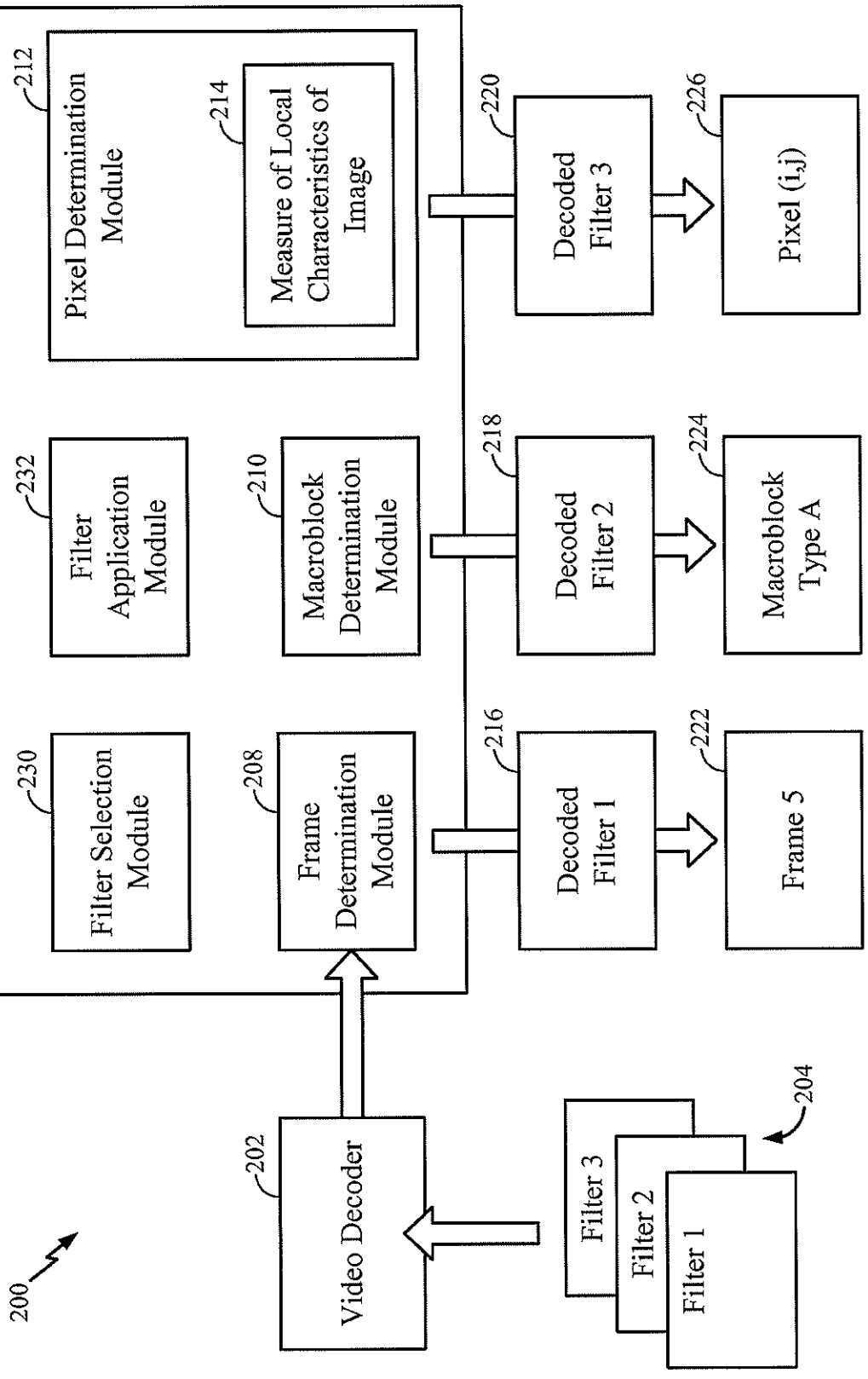

200
202
Video Decoder
204
Filter 3
Filter 2
Filter 1
206
PROCESSOR
208
Frame Determination Module
210
Macroblock Determination Module
212
Pixel Determination Module
214
Measure of Local Characteristics of Image
216
Decoded Filter 1
218
Decoded Filter 2
220
Decoded Filter 3
222
Frame 5
224
Macroblock Type A
226
Pixel (i,j)
230
Filter Selection Module
232
Filter Application Module

FIG. 2

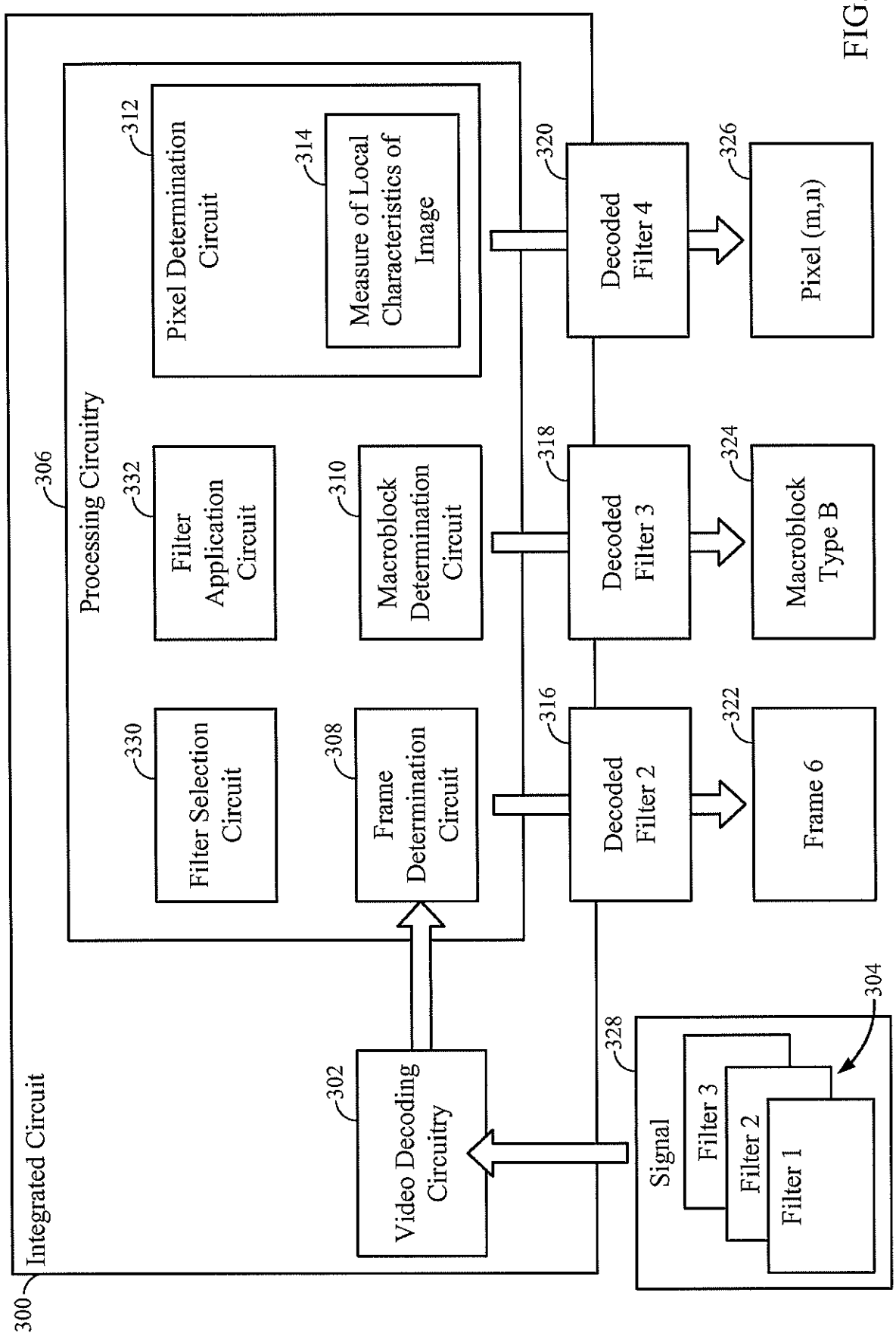

Integrated Circuit
300
Processing Circuitry
306
Video Decoding Circuitry
302
Frame Determination Circuit
308
Macroblock Determination Circuit
310
Pixel Determination Circuit
312
Measure of Local Characteristics of Image
314
Decoded Filter 2
316
Decoded Filter 3
318
Decoded Filter 4
320
Frame 6
322
Macroblock Type B
324
Pixel (m,n)
326
Signal
328
Filter 1
Filter 2
Filter 3
304
Filter Application Circuit
332
Filter Selection Circuit
330

FIG. 3

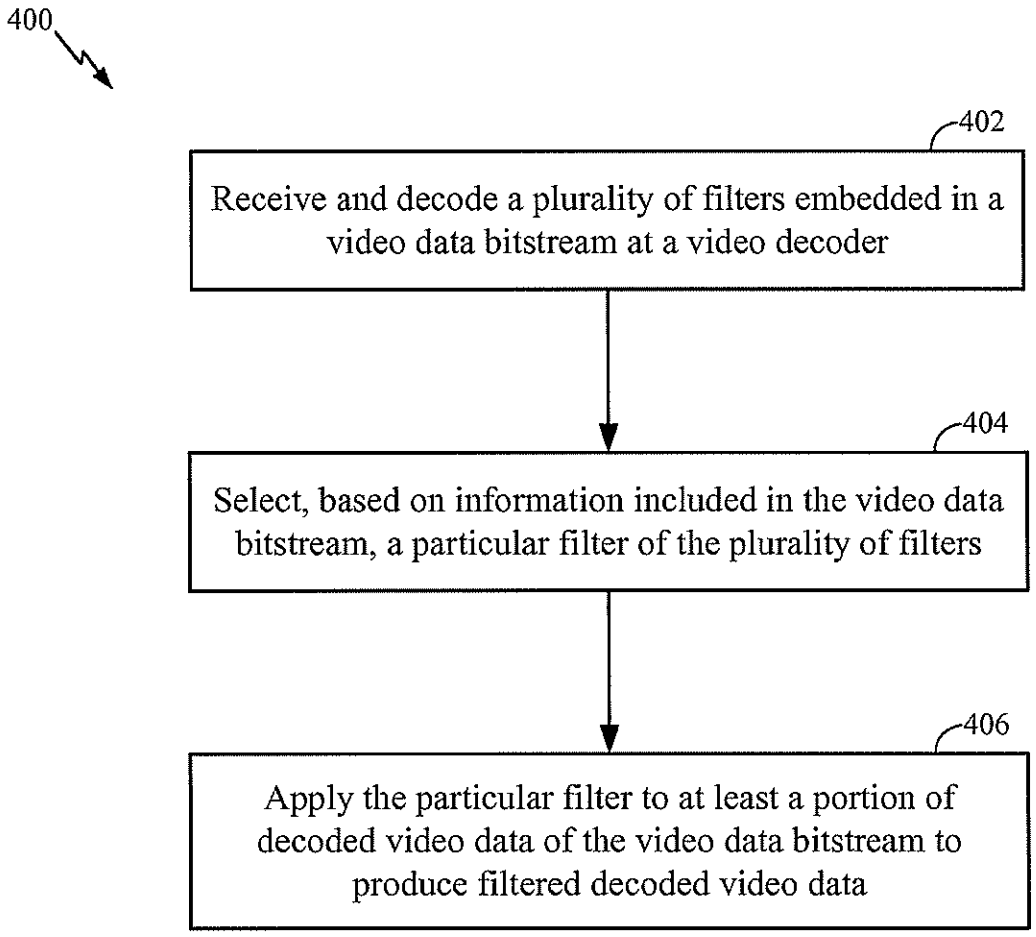

400
402
Receive and decode a plurality of filters embedded in a video data bitstream at a video decoder
404
Select, based on information included in the video data bitstream, a particular filter of the plurality of filters
406
Apply the particular filter to at least a portion of decoded video data of the video data bitstream to produce filtered decoded video data

FIG. 4

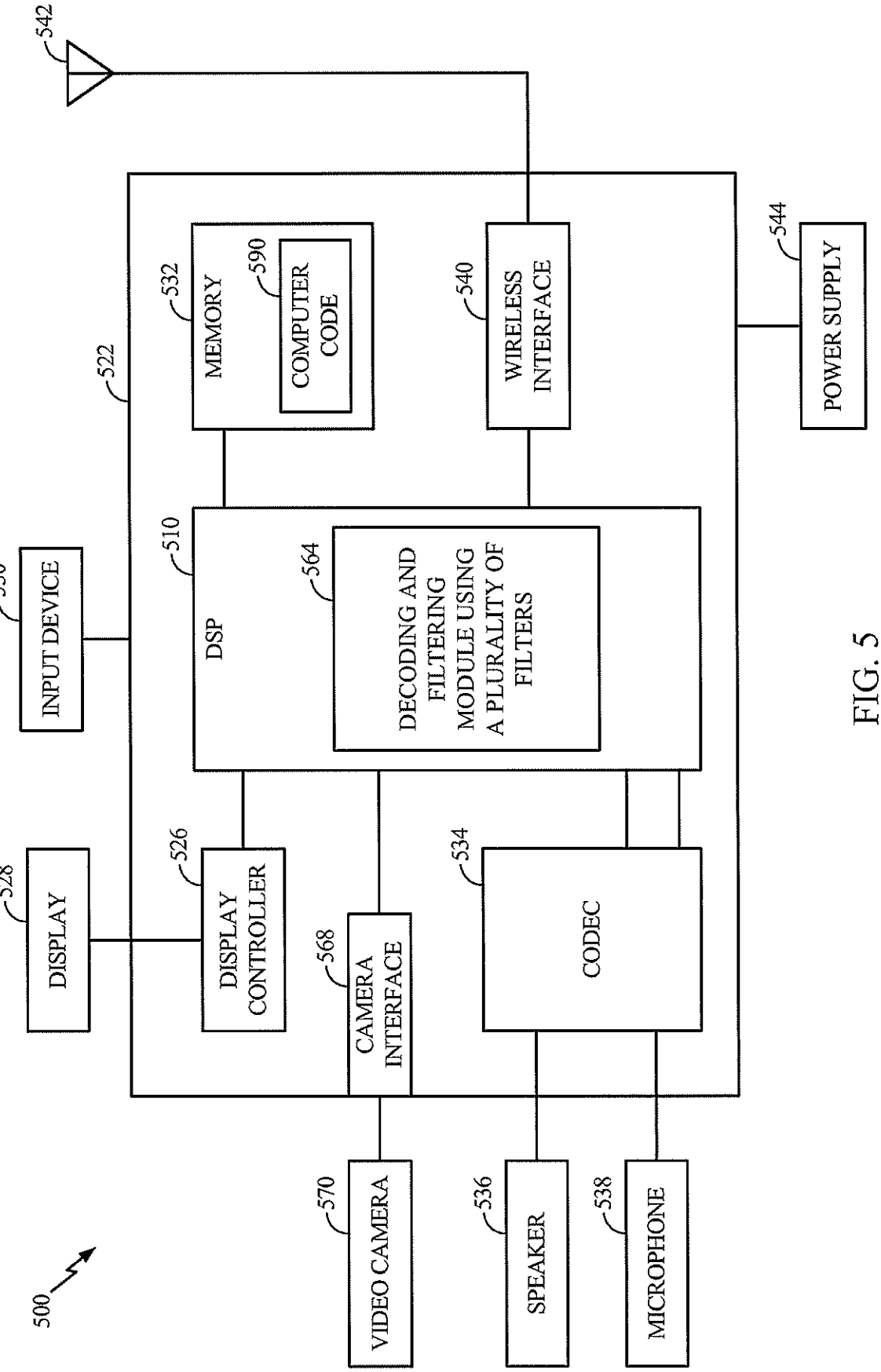

542
522
532
MEMORY
590
COMPUTER CODE
540
WIRELESS INTERFACE
544
POWER SUPPLY
530
INPUT DEVICE
510
DSP
564
DECODING AND FILTERING MODULE USING A PLURALITY OF FILTERS
528
DISPLAY
526
DISPLAY CONTROLLER
568
CAMERA INTERFACE
534
CODEC
570
VIDEO CAMERA
536
SPEAKER
538
MICROPHONE
500

FIG. 5