

ABSTRACT

In a vertical sawtooth generator (10,14) of a vertical deflection circuit, a first comparator (19) generates an output signal when a retrace portion (RETRACE) of a sawtooth signal (VRAMP) is at a first magnitude (VLOW) to initiate a vertical trace portion of the sawtooth signal. An amplifier (23) responsive to the sawtooth signal generates a feedback current (IOUT) at a time (H. LINE #210, Fig. 2c) that occurs more than one-half of the length of vertical trace portion after vertical trace is initiated. The feedback current is coupled to a capacitor (22) to develop a capacitor voltage (VAGC). The capacitor voltage is applied via a voltage-to-current converter (21) to an integrating capacitor (14) to develop in the integrating capacitor the trace portion of the sawtooth signal.

FIGURE 1a

