

**AUDIO DIFFERENTIAL BUS RECEIVER
FOR AUDIO/VIDEO INTERCONNECTION**

Abstract

Audio bus receiver includes a pair (Q1,Q2) of bipolar transistors each having a base coupled via a respective resistor 5 (R1;R2) to a respective bus receiver input terminal (1;2), each having an emitter that is diode coupled to a respective current source (16;20) and each having a collector coupled via a respective load resistor (R7;R8) to a source of reference potential. The emitters are coupled together via a gain control resistor (R5) 10 and the collectors are coupled to an output terminal (28) via a differential amplifier (30). Advantageously, the receiver avoids loading the bus under power-down conditions without requiring the use of stand-by power supplies. Additionally, high common mode rejection is achieved without need for precision matching of 15 components and only a single supply voltage is required for operation.

FIGURE 1

This Sheet Has Been Replaced

