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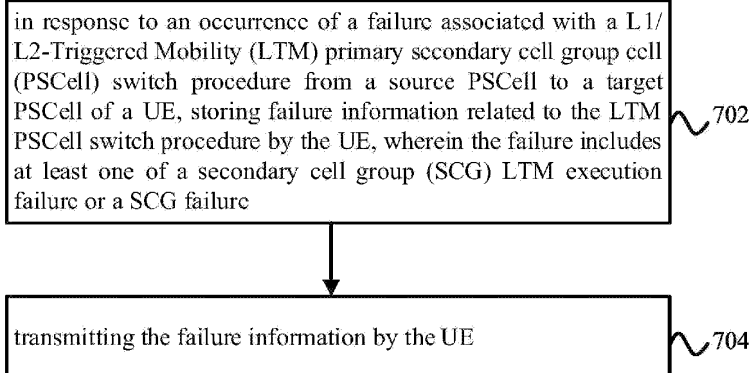
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(54) Title: METHODS AND APPARATUSES OF MRO IN AN LTM PROCEDURE

[Fig. 7]



(57) Abstract: Various aspects of the present disclosure relate to methods and apparatuses of a mobility robustness optimization (MRO) mechanism for a L1/L2-Triggered Mobility (LTM) cell switch procedure. According to an embodiment of the present disclosure, secondary node (SN) includes at least one memory and at least one processor coupled to the at least one memory and configured to cause the SN to: in response to an occurrence of a failure associated with a L1/L2-Triggered Mobility (LTM) primary secondary cell group cell (PSCell) switch procedure from a source PSCell to a target PSCell of a user equipment (UE), obtain failure information related to the LTM PSCell switch procedure, wherein the failure includes at least one of a secondary cell group (SCG) LTM execution failure or an SCG failure.

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