

FIG.1A

DRX CONTROL IN $\overline{\text{PRRC_CONNECTED STATE}}$

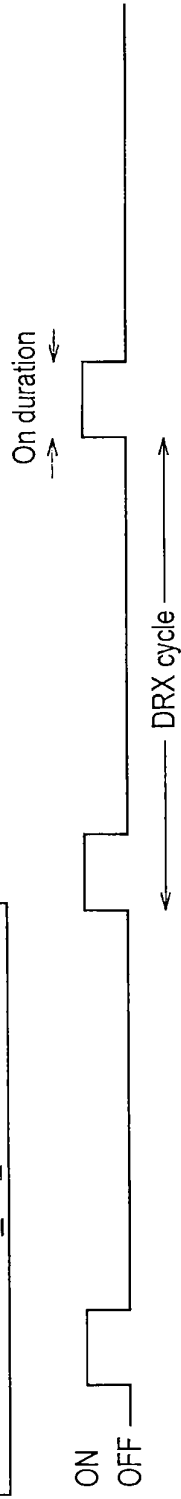


FIG.1B

TRANSITION FROM DRX STATE TO CONTINUOUS RECEPTION STATE

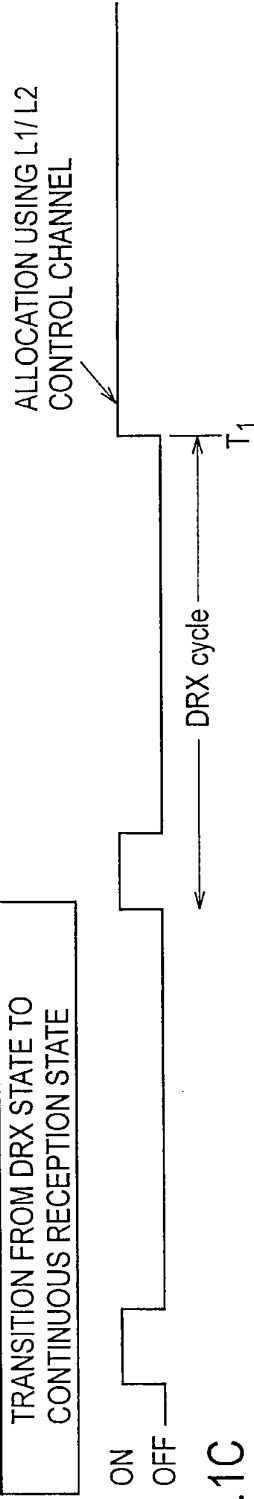


FIG.1C

TRANSITION FROM CONTINUOUS RECEPTION STATE TO DRX STATE (implicit)

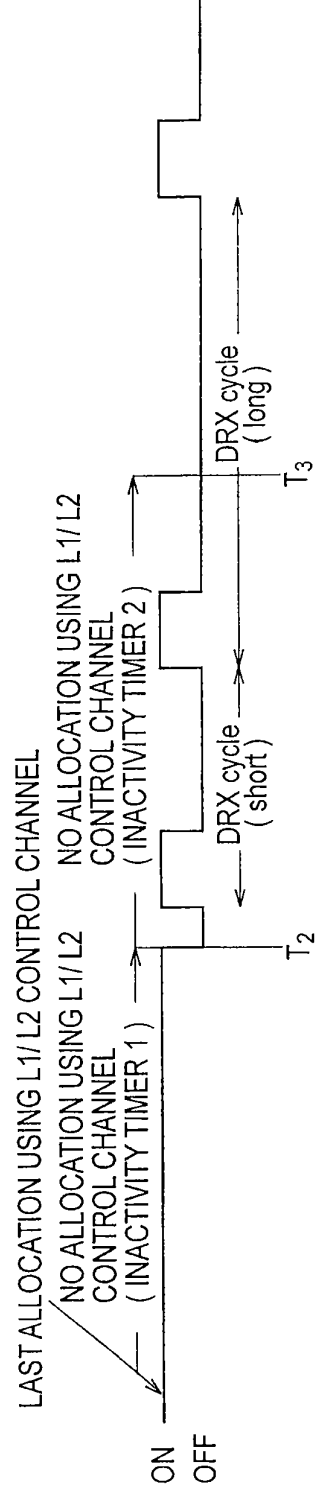


FIG.2

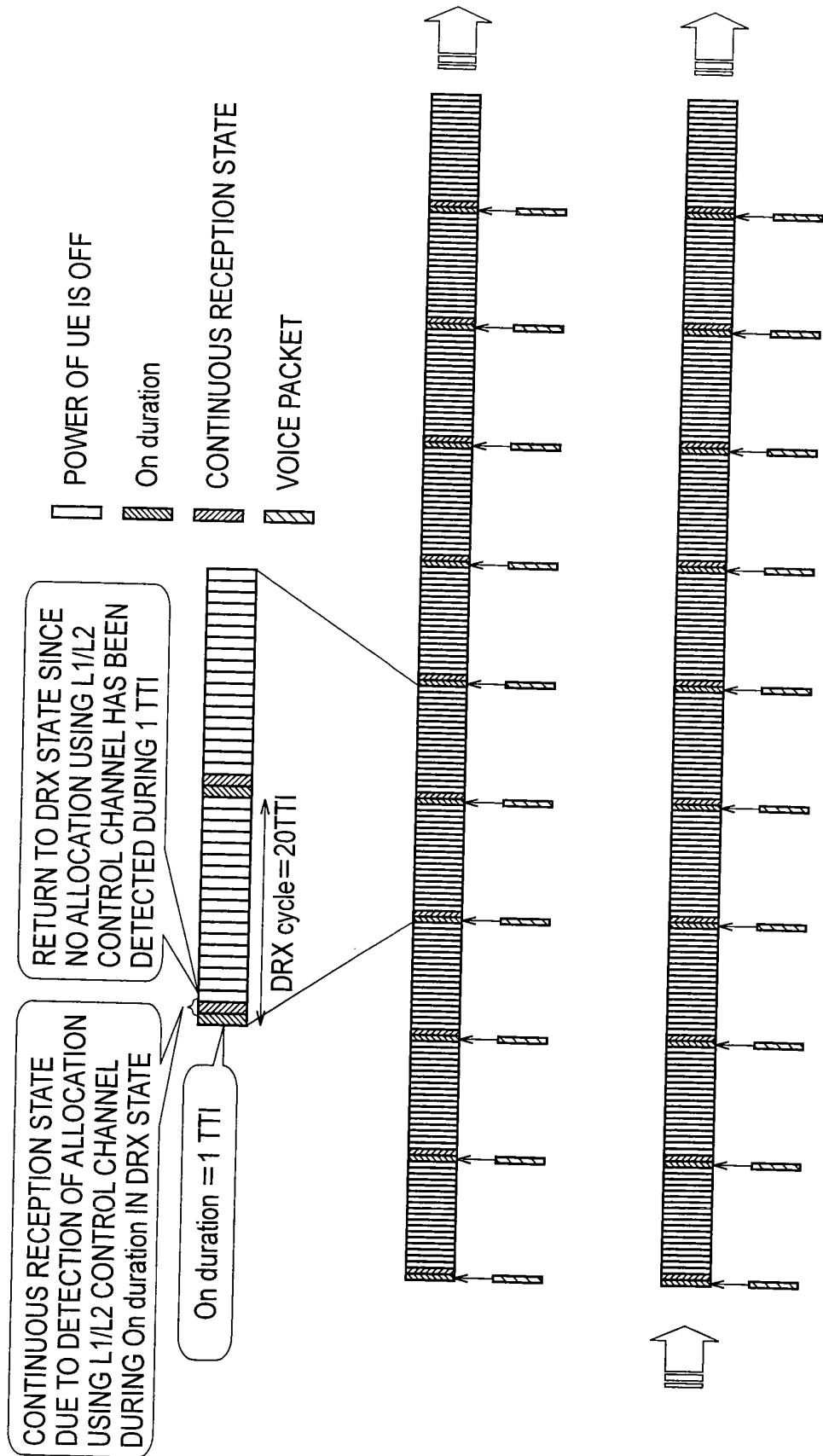


FIG.3



FIG.4

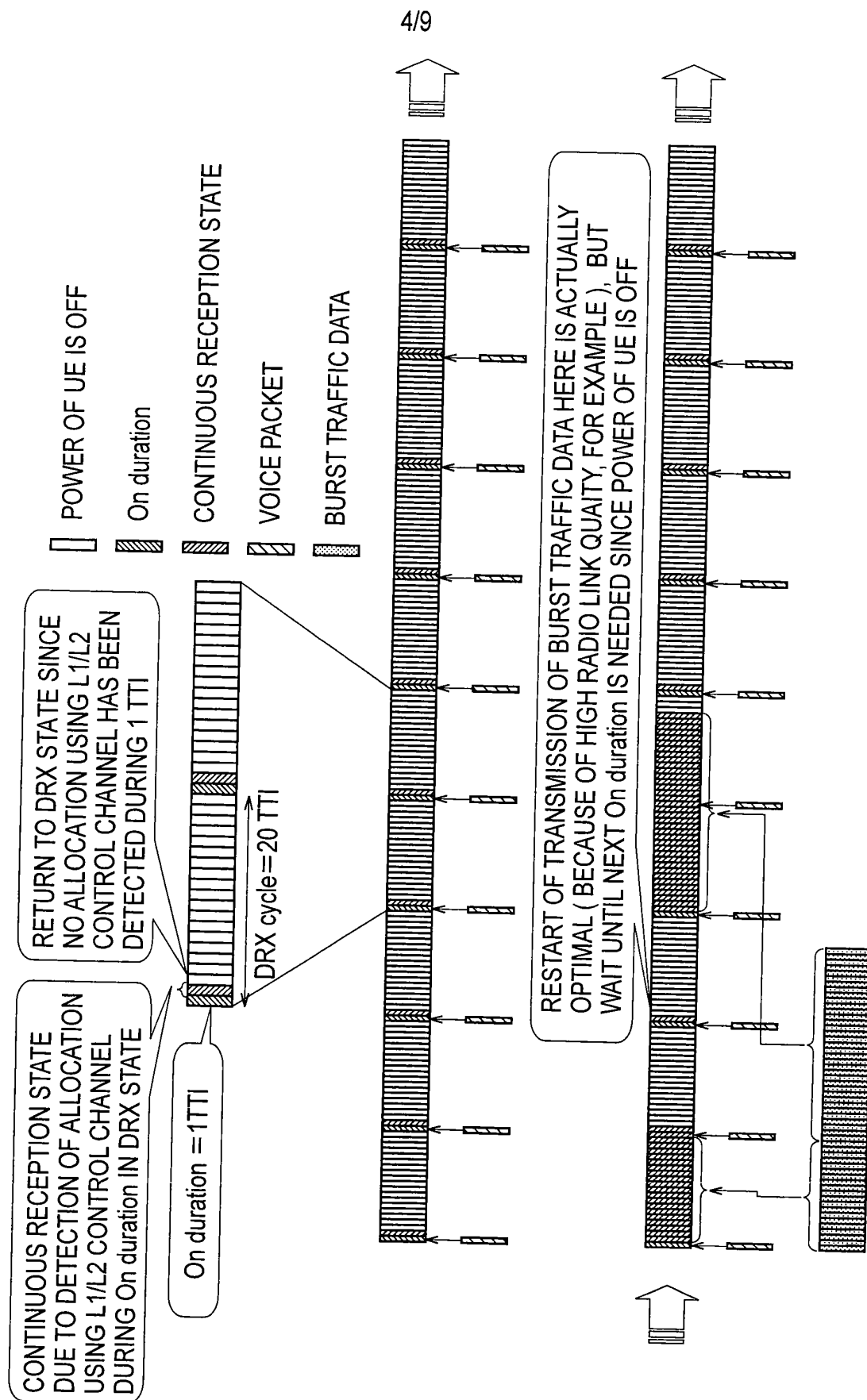


FIG. 5

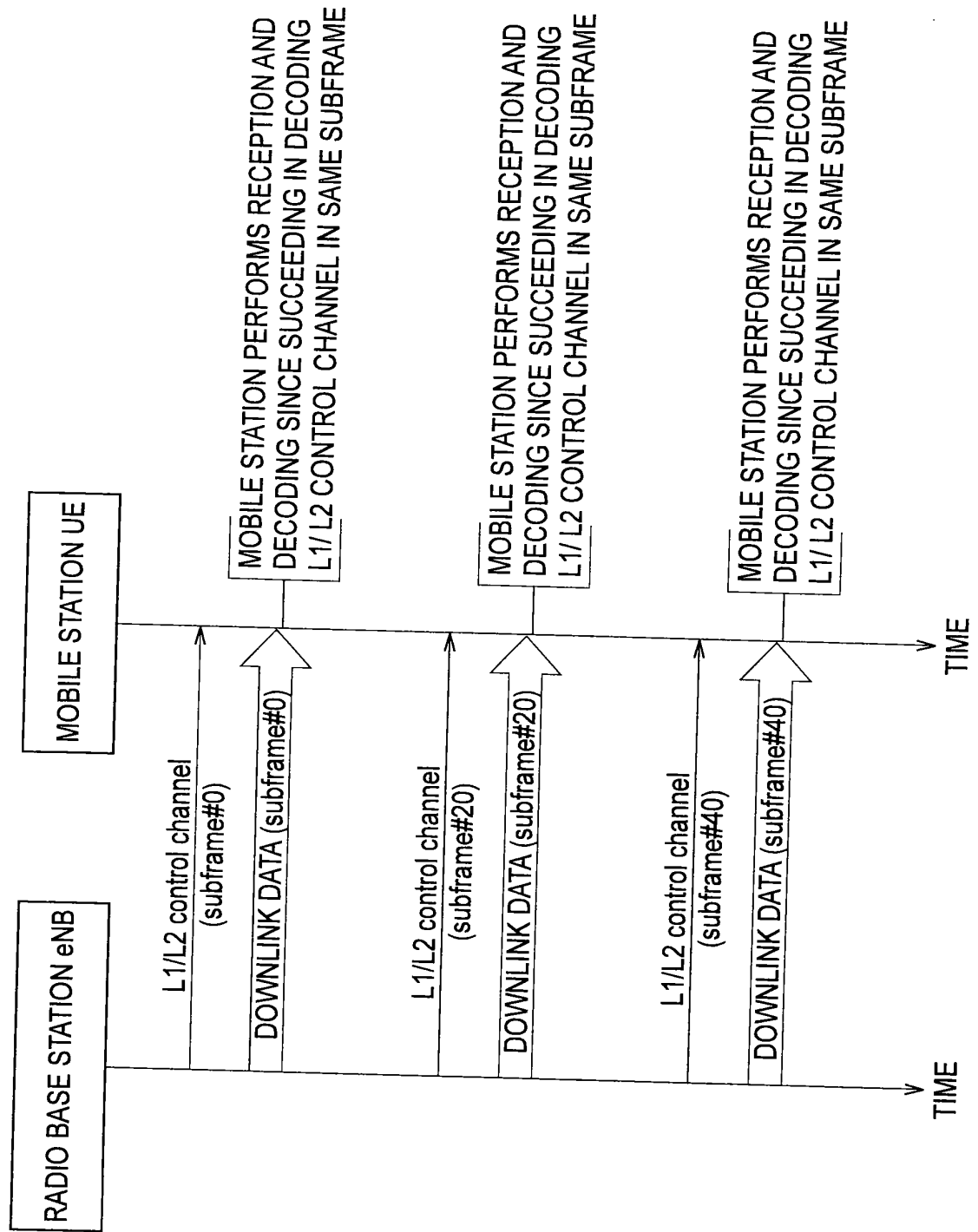


FIG.6

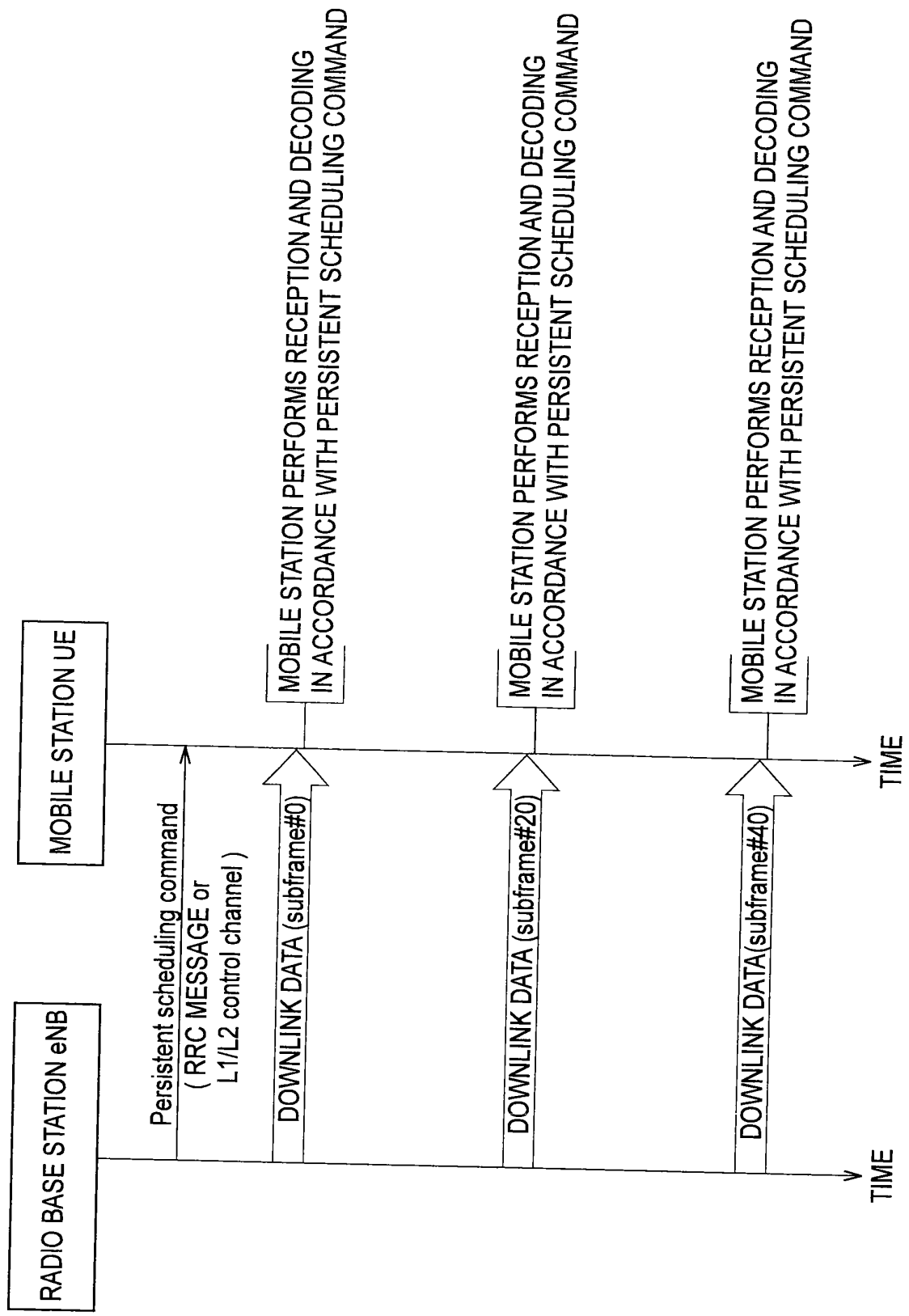


FIG.7

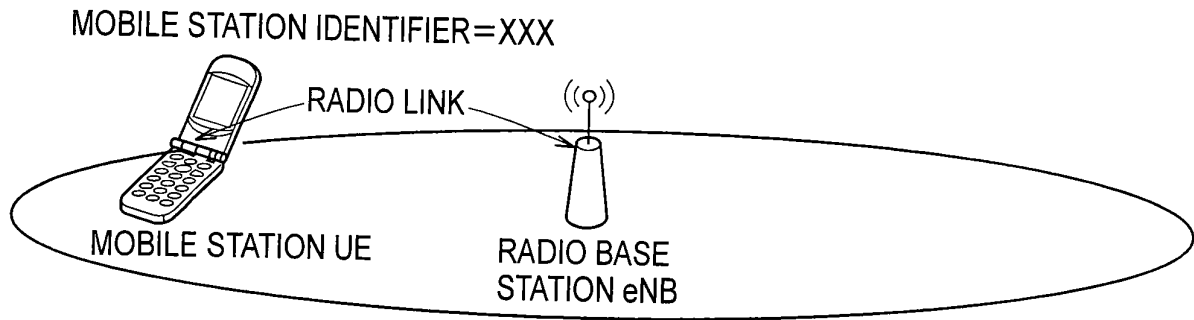


FIG.8

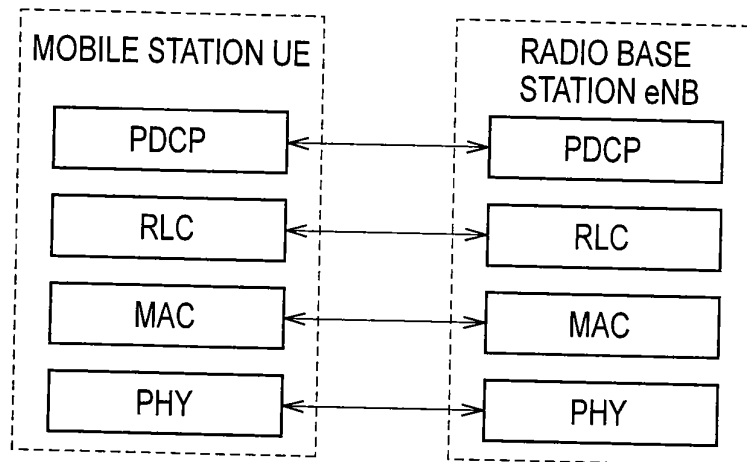


FIG.9

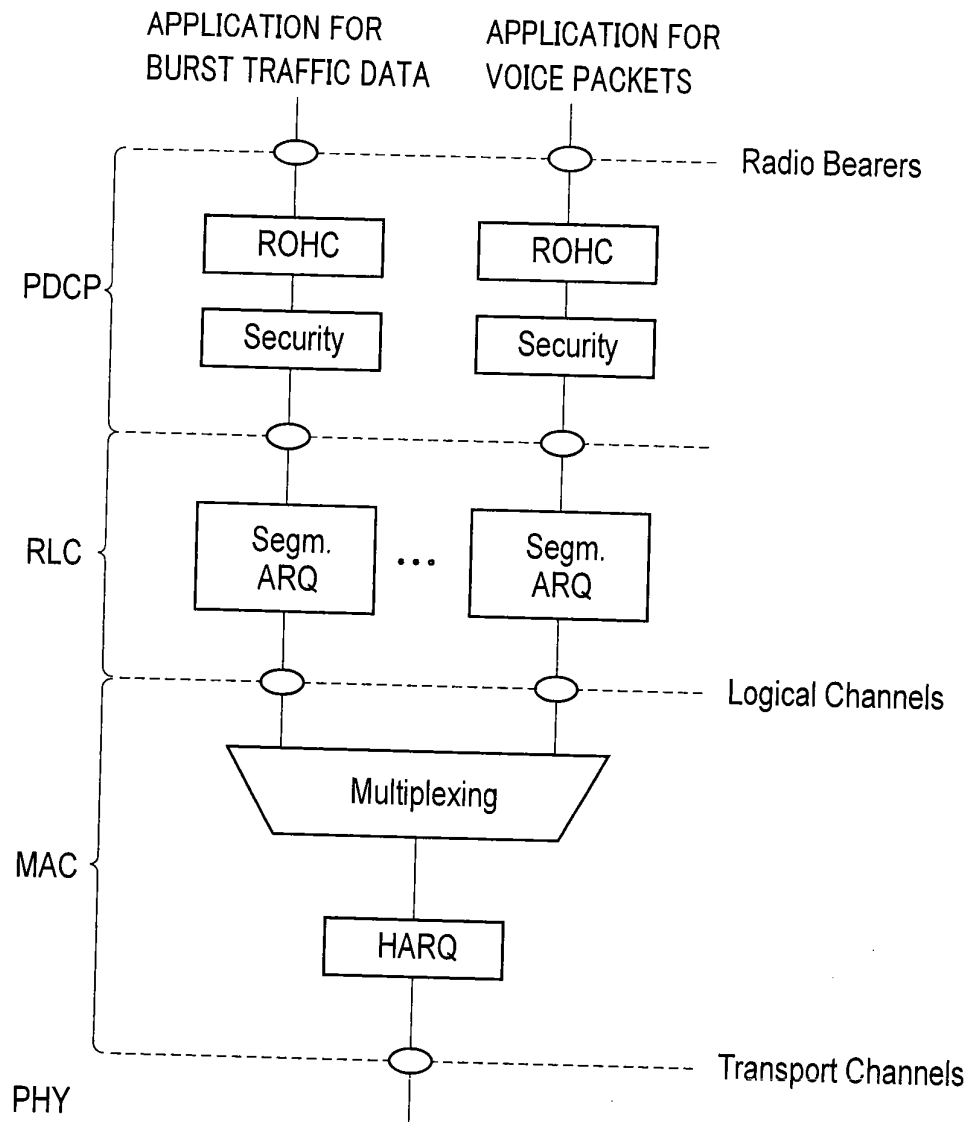


FIG.10

