Claims

1. A video decoding apparatus comprising:

a processor which is configured for obtaining, from a bitstream, left offset merging information indicating whether an offset parameter of a current block is determined according to an offset parameter of a left block, and compensating for the samples of the current block by using offset values,

wherein:

when the left offset merging information indicates that the offset parameter of the current block is not determined according to the offset parameter of the left block, the processor is configured for determining the offset parameter of the current block to using the offset parameter of the left block,

when the left offset merging information indicates that the offset parameter of the current block is not determined according to the offset parameter of the left block, the processor is configured for obtaining, from the bitstream, upper offset merging information indicating whether the offset parameter of the current block is determined according to an offset parameter of an upper block,

when the upper offset merging information indicates that the offset parameter of the current block is determined according to the offset parameter of the upper block, the processor is configured for determining an offset parameter of the current block using the offset ~~parameter~~type of the upper block,

when the upper offset merging information indicates that the offset parameter of the current block is not determined according to the offset parameter of the upper block, the processor is configured for obtaining, from the bitstream, offset parameter of the current block,

the offset parameter includes at least one of the offset type information and the offset values,

the offset type information indicates the offset type or whether to apply the offset values to the current block, and

the offset type is one of a band offset type and an edge offset type.

Claims

1. A video decoding apparatus comprising:

a processor which is configured for obtaining, from a bitstream, left offset merging information indicating whether an offset parameter of a current block is determined according to an offset parameter of a left block, and compensating for the samples of the current block by using offset values,

wherein:

when the left offset merging information indicates that the offset parameter of the current block is not determined according to the offset parameter of the left block, the processor is configured for determining the offset parameter of the current block to using the offset parameter of the left block,

when the left offset merging information indicates that the offset parameter of the current block is not determined according to the offset parameter of the left block, the processor is configured for obtaining, from the bitstream, upper offset merging information indicating whether the offset parameter of the current block is determined according to an offset parameter of an upper block,

when the upper offset merging information indicates that the offset parameter of the current block is determined according to the offset parameter of the upper block, the processor is configured for determining an offset parameter of the current block using the offset type of the upper block,

when the upper offset merging information indicates that the offset parameter of the current block is not determined according to the offset parameter of the upper block, the processor is configured for obtaining, from the bitstream, offset parameter of the current block,

the offset parameter includes at least one of the offset type information and the offset values,

the offset type information indicates the offset type or whether to apply the offset values to the current block, and

the offset type is one of a band offset type and an edge offset type.