

TRAY FOR SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE**ABSTRACT OF THE DISCLOSURE**

A tray for storing a semiconductor device such as a BGA device. This tray comprises a storage portion for receiving and storing the BGA device therein. The storage portion has a wall surface which is arranged around the semiconductor device upon storing the semiconductor device. This wall surface has an area which is inclined with an angle so as to support an edge of a package of the semiconductor device but not to come into contact with wiring terminals thereof. With this arrangement, the inclined area comes into contact with only the edge of the package so as to be able to support the package.

Figure 6

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