

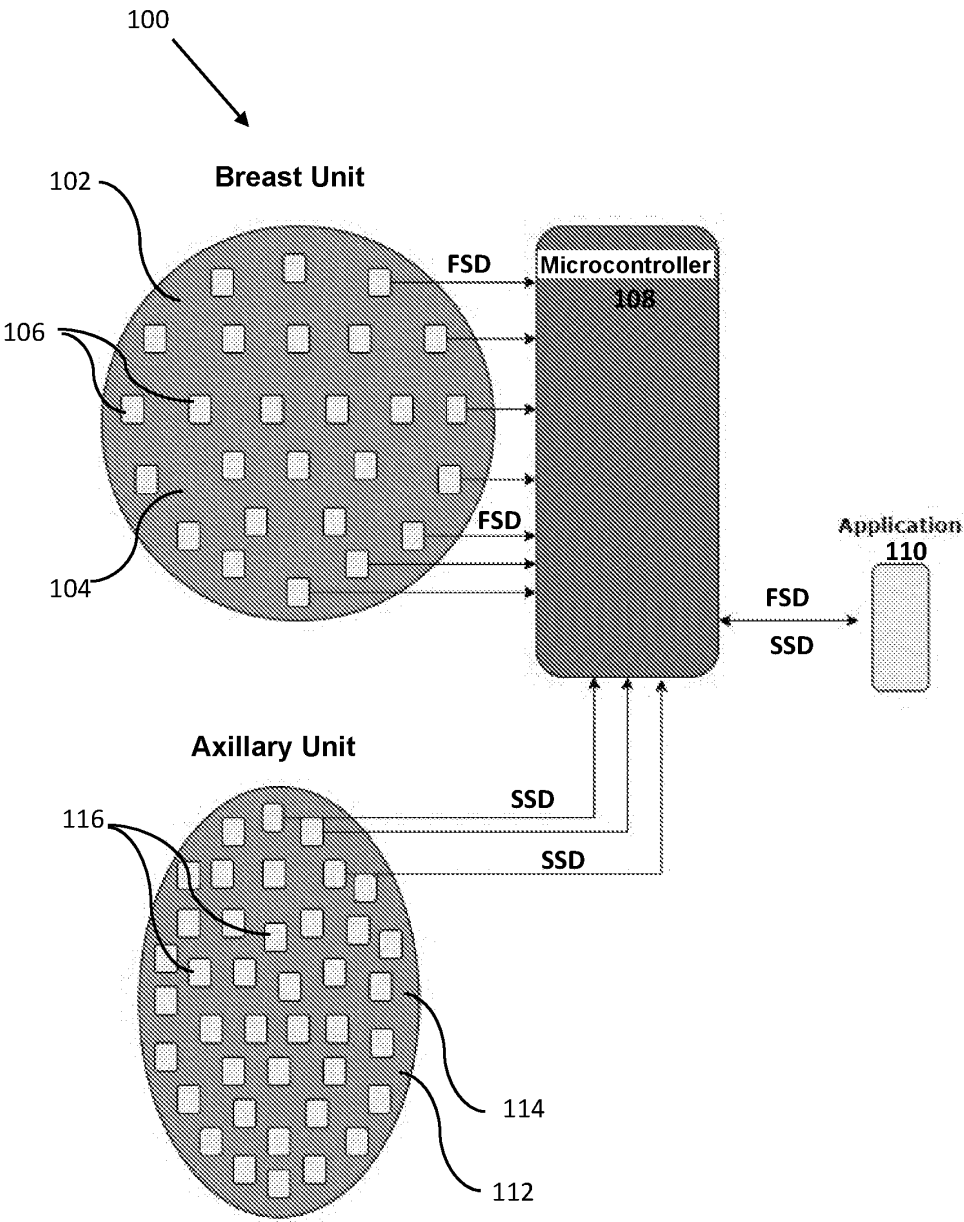


Fig. 1

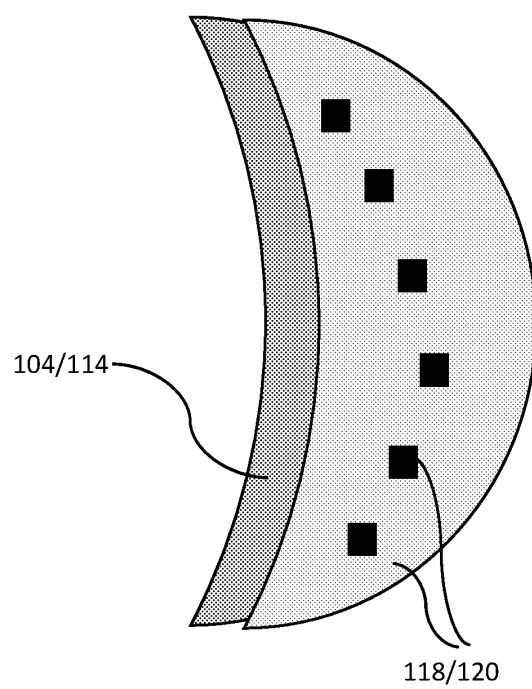


Fig. 2A

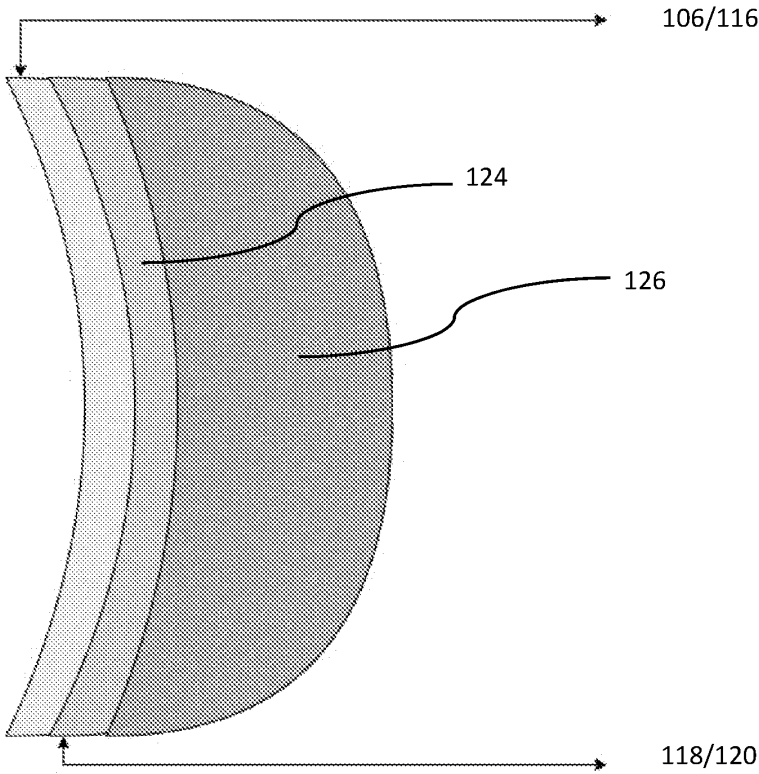


Fig. 2B

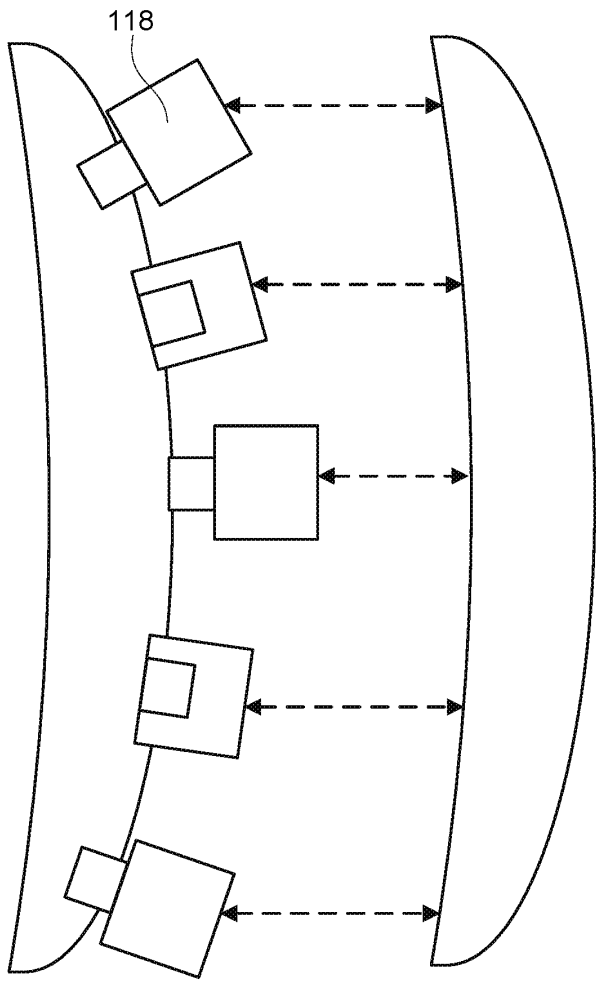


FIG. 3

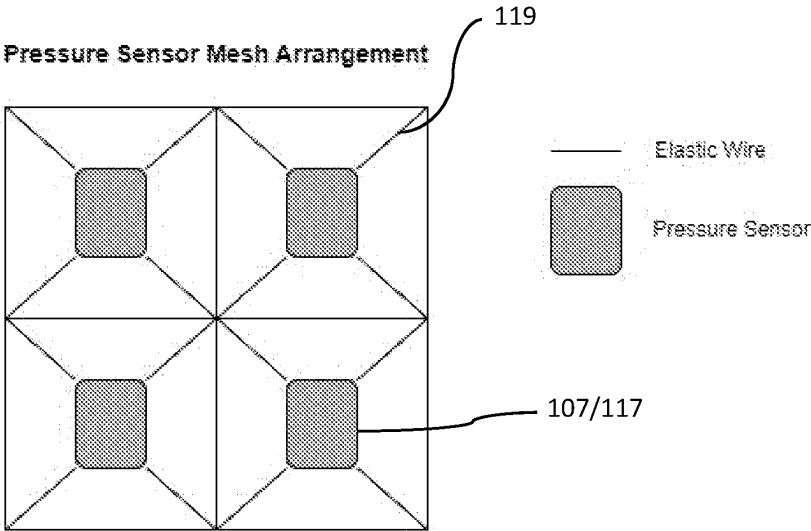


Fig. 4

Device Design and Sensors Location

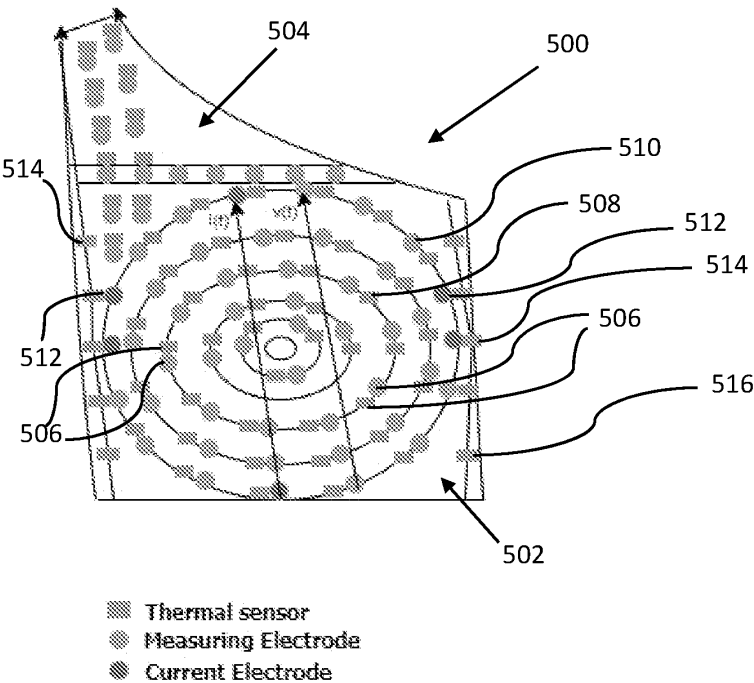


Fig. 5

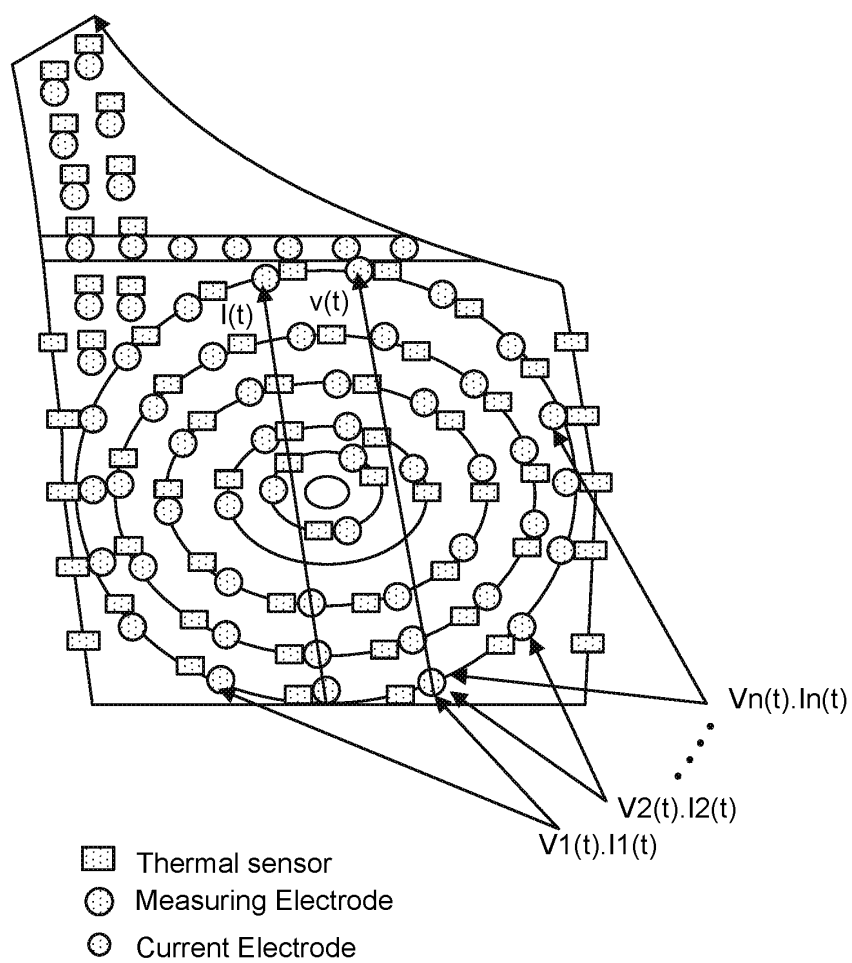


FIG. 6

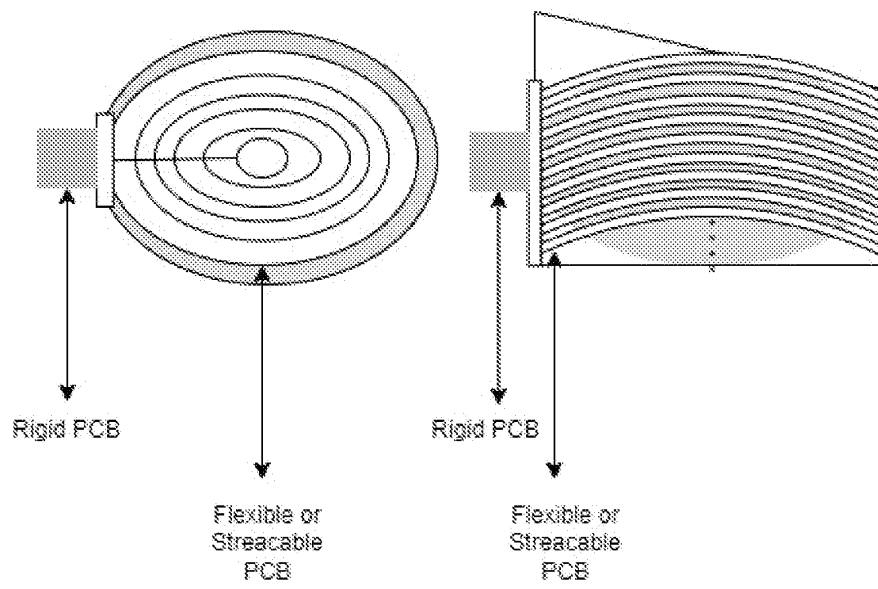


Fig. 7

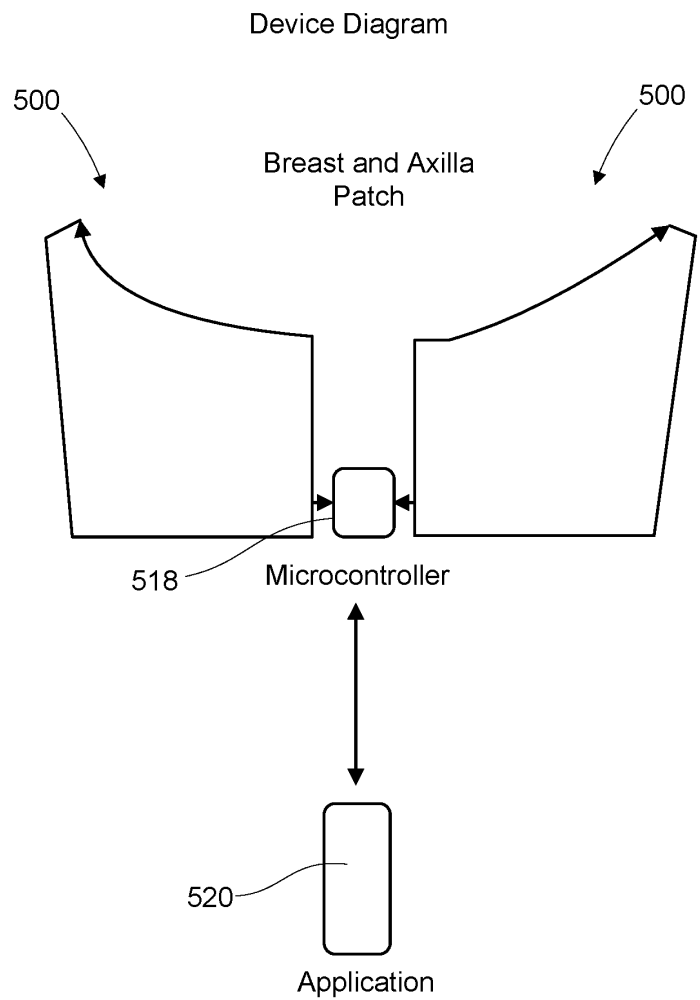


FIG. 8

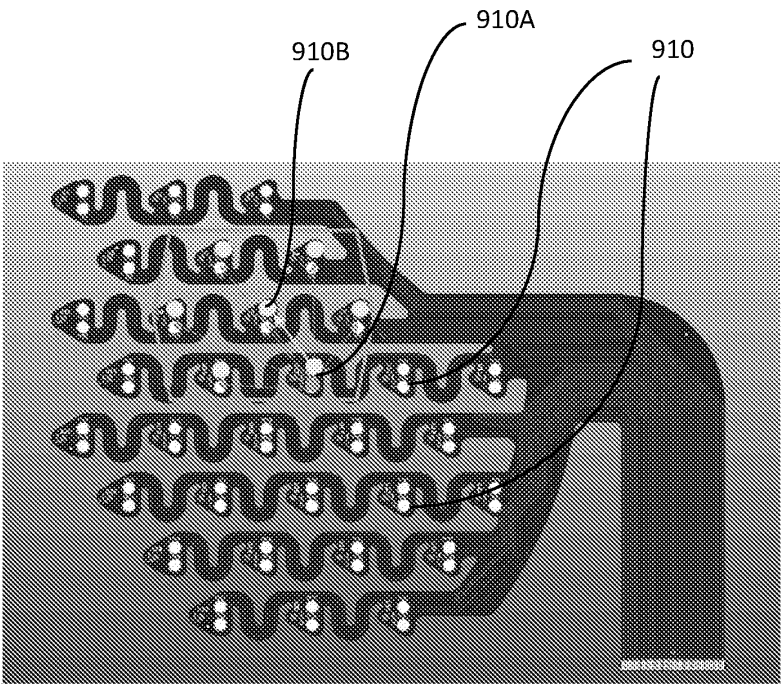


Fig. 9A

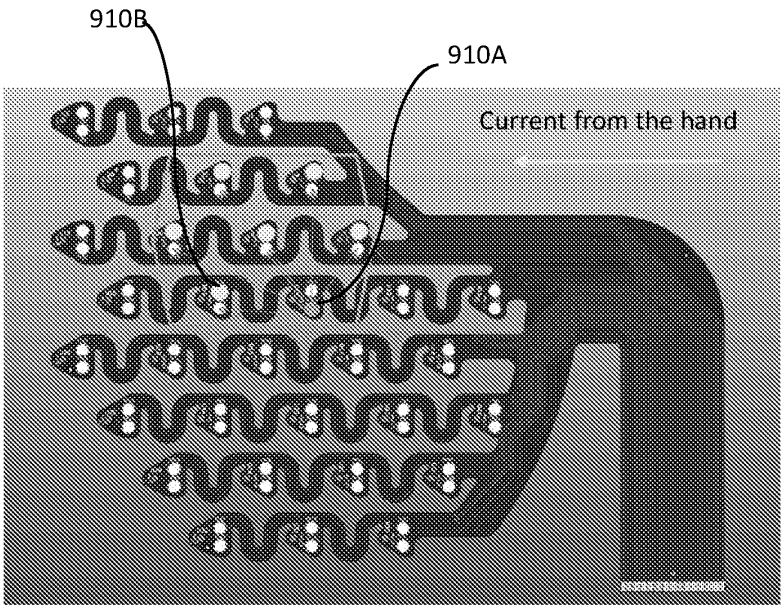


Fig. 9B

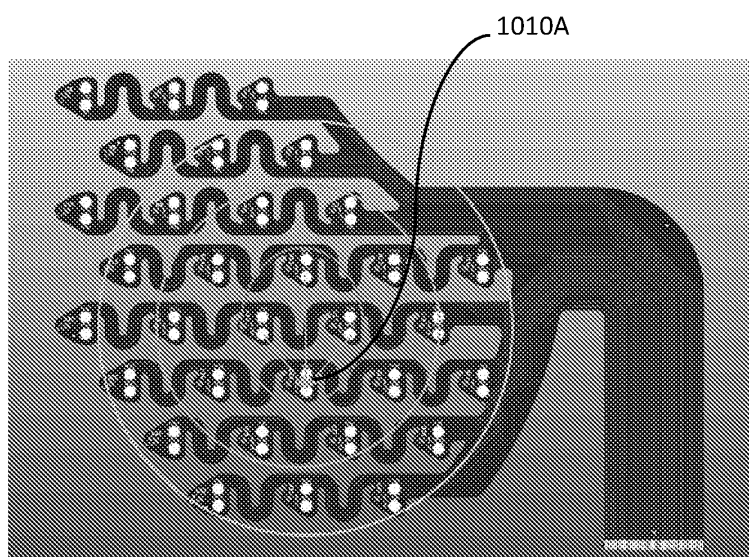


Fig. 10

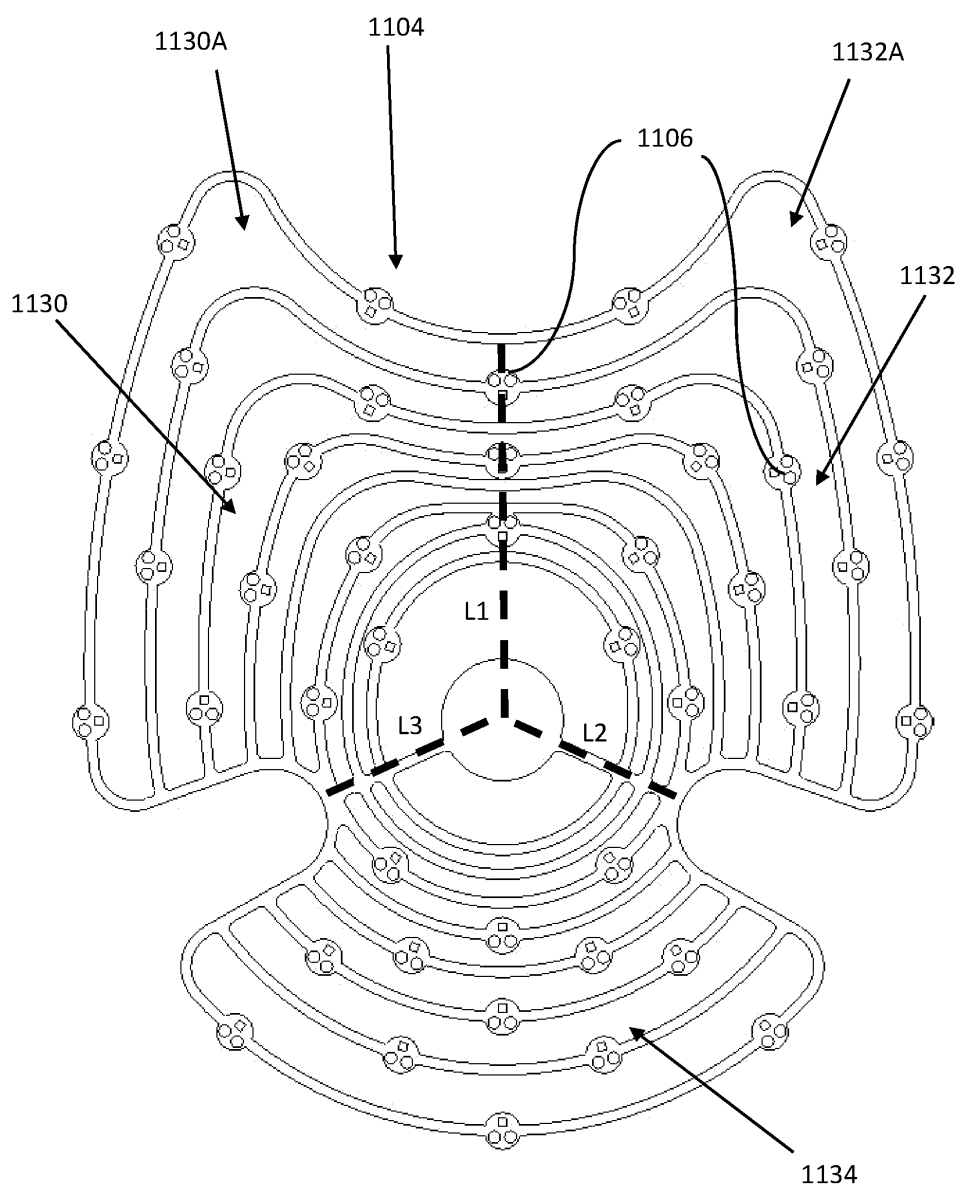


Fig. 11