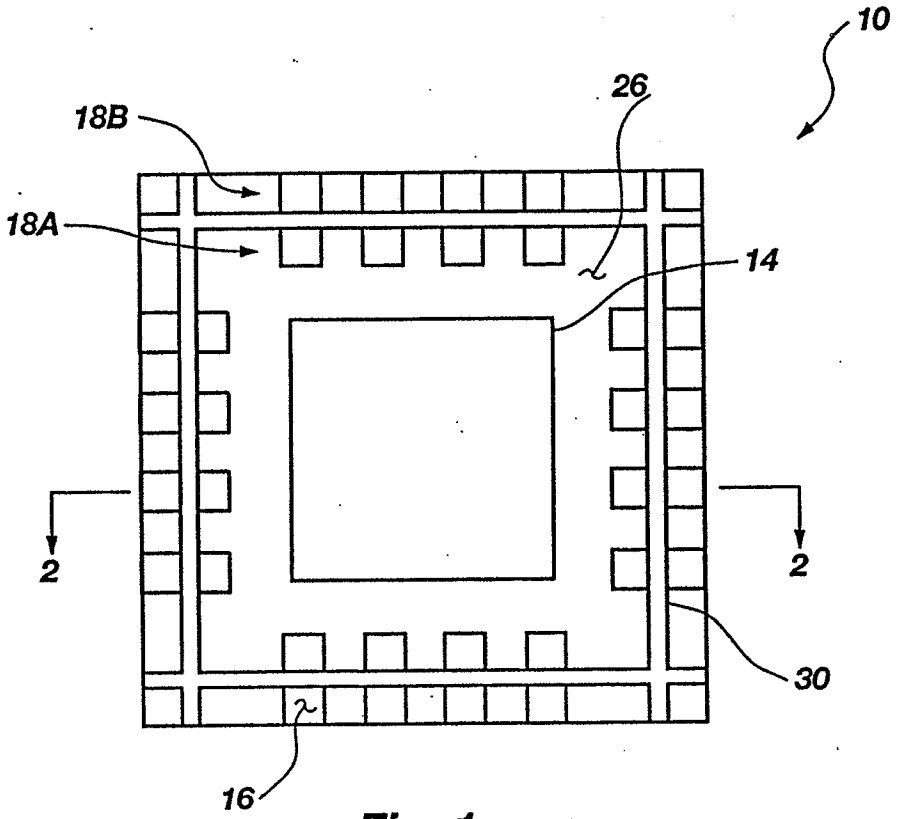

10
26
18B
18A
14
2
2
30
16

Fig. 1

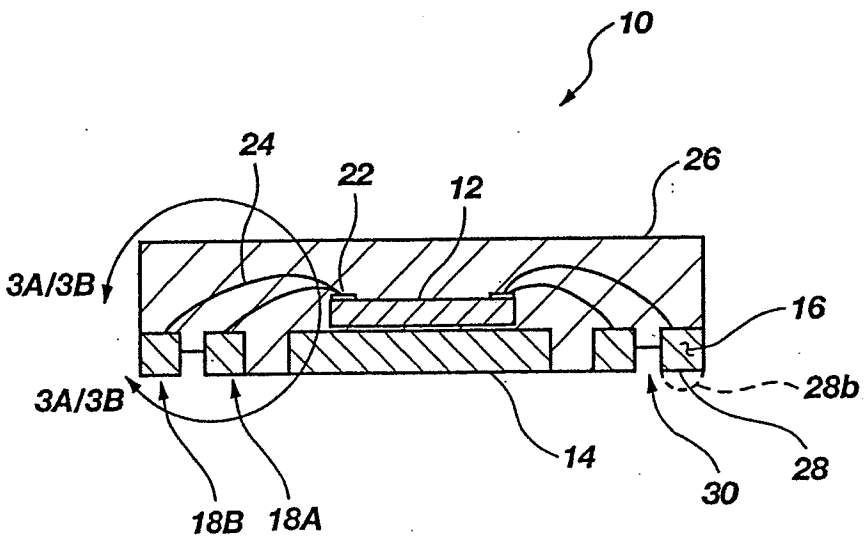

10
24
22
12
26
3A/3B
16
3A/3B
28b
14
28
30
18B
18A

Fig. 2

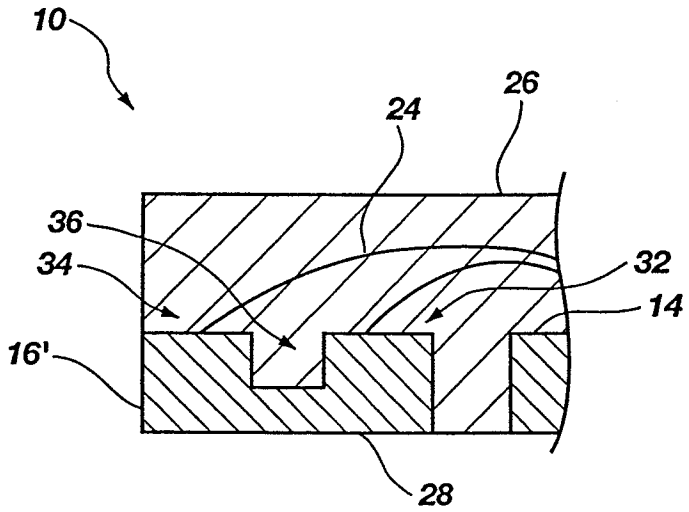


Fig. 3A

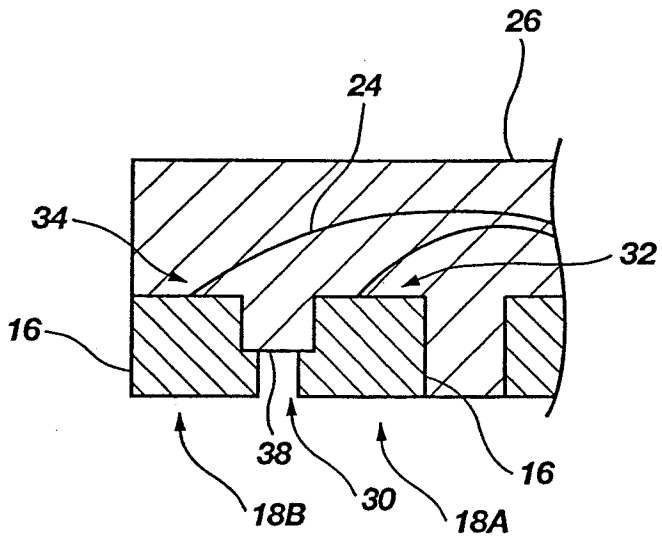


Fig. 3B

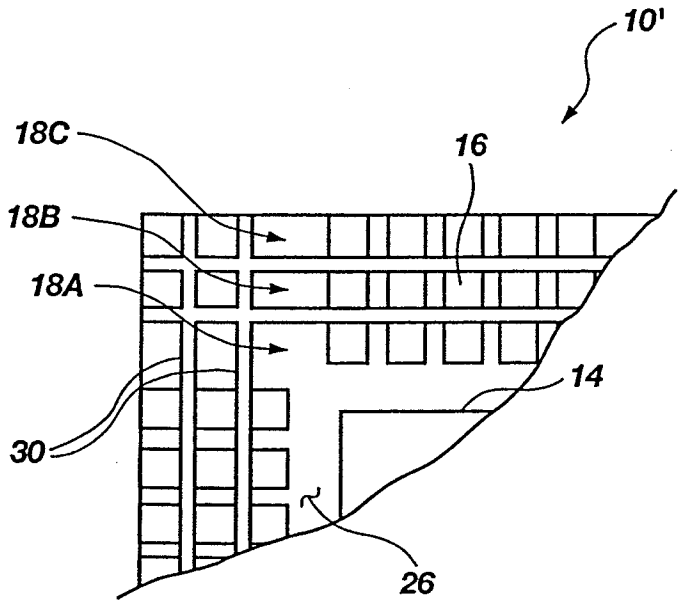


Fig. 4A

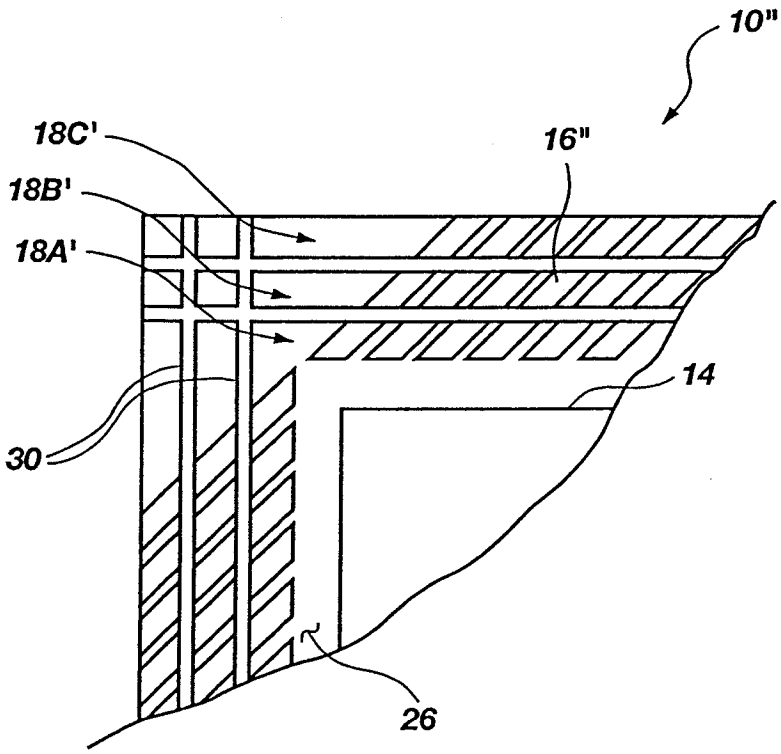


Fig. 4B

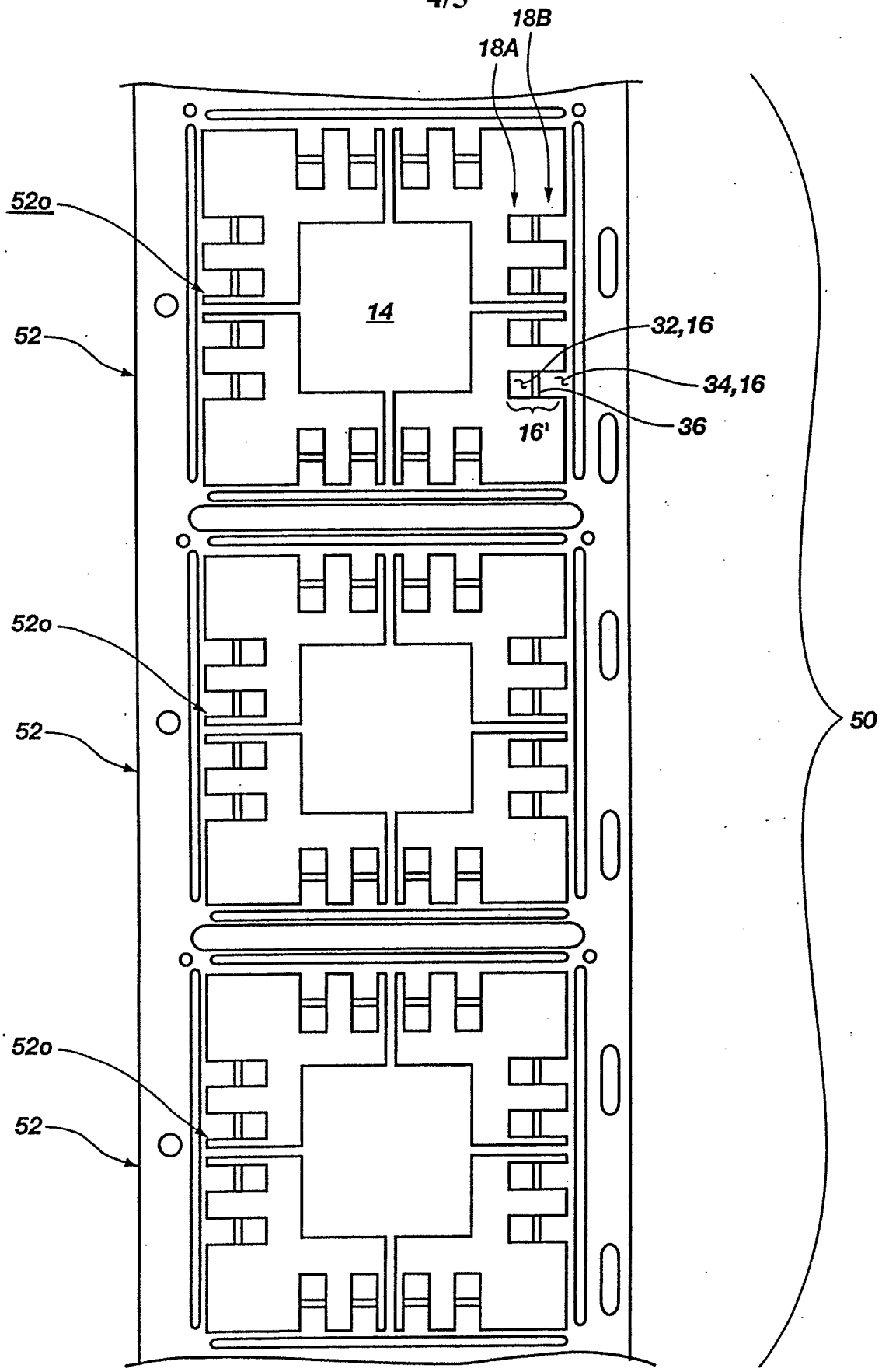


Fig. 5

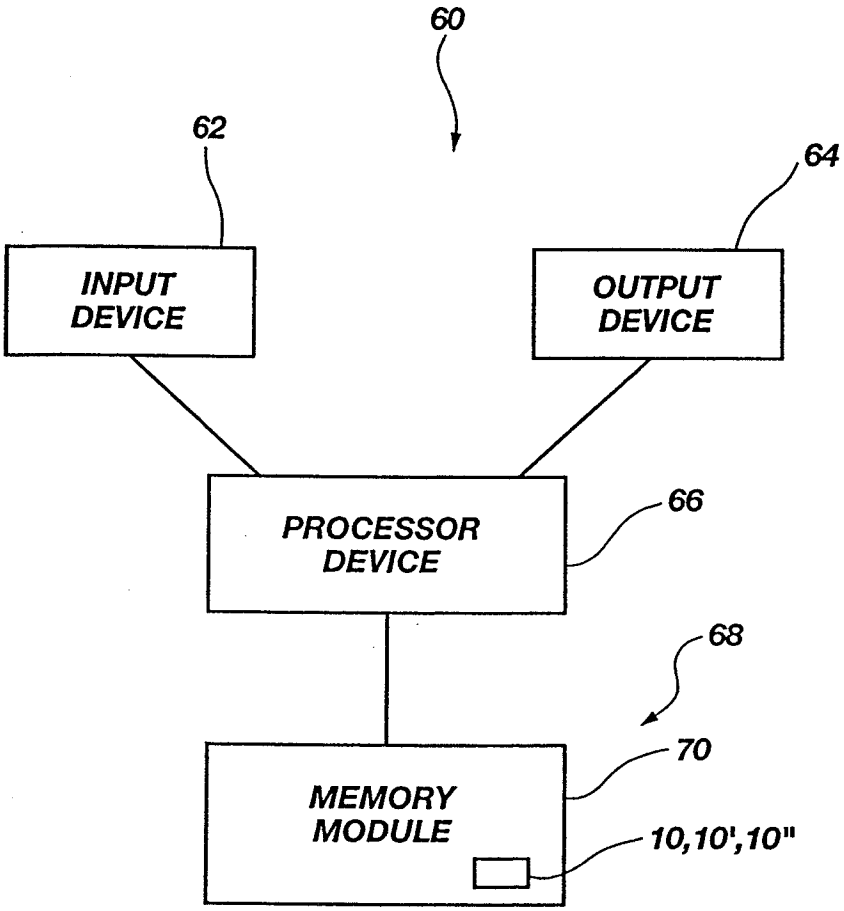

60
62
64
INPUT DEVICE
OUTPUT DEVICE
PROCESSOR DEVICE
66
68
70
MEMORY MODULE
10,10',10"

Fig. 6