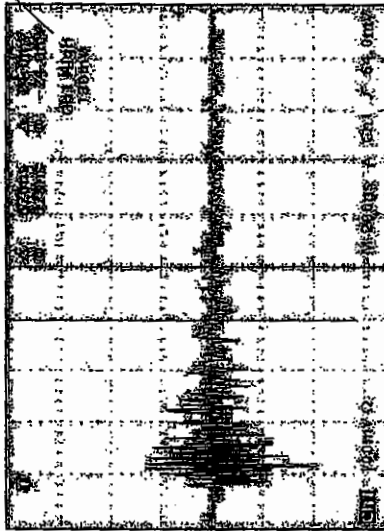
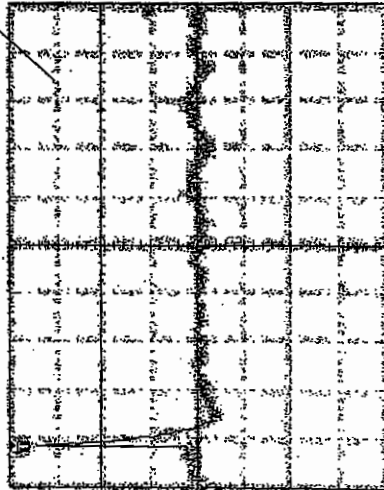


12



10



b)

a)

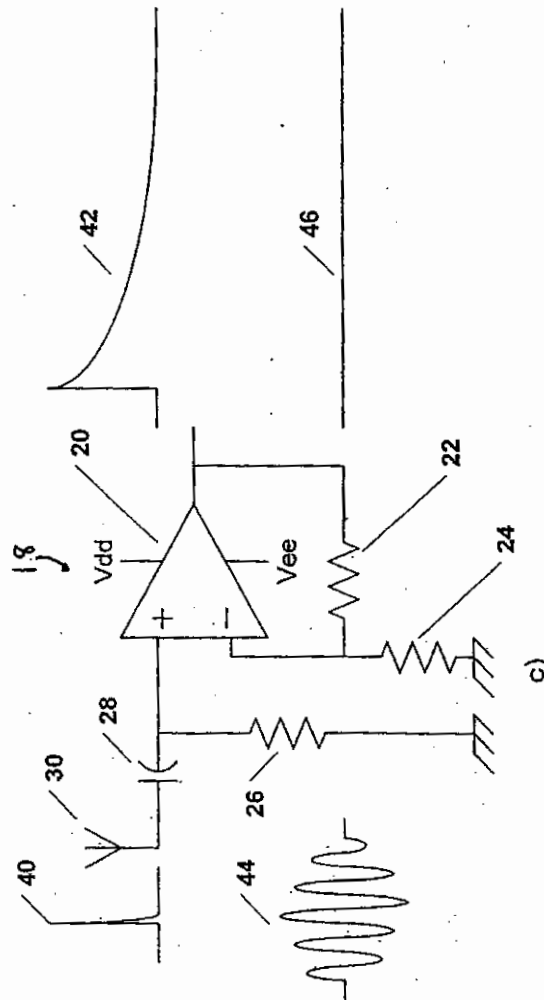


Figure 1

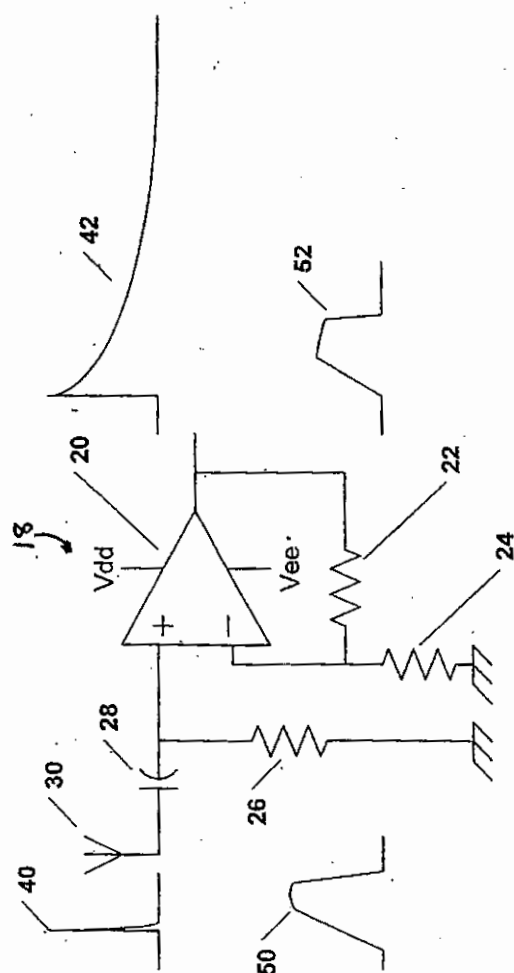


Figure 2

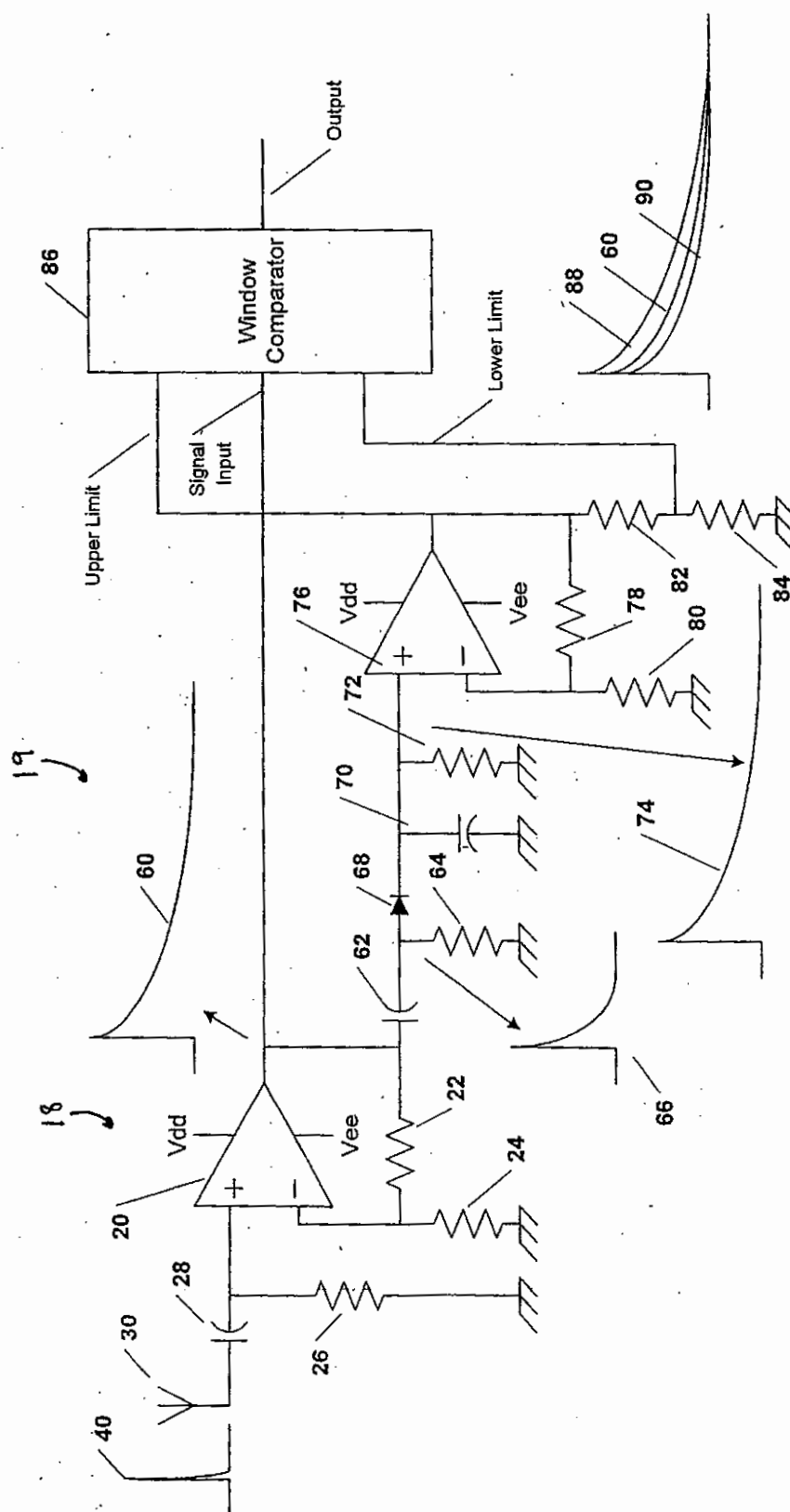


Figure 3

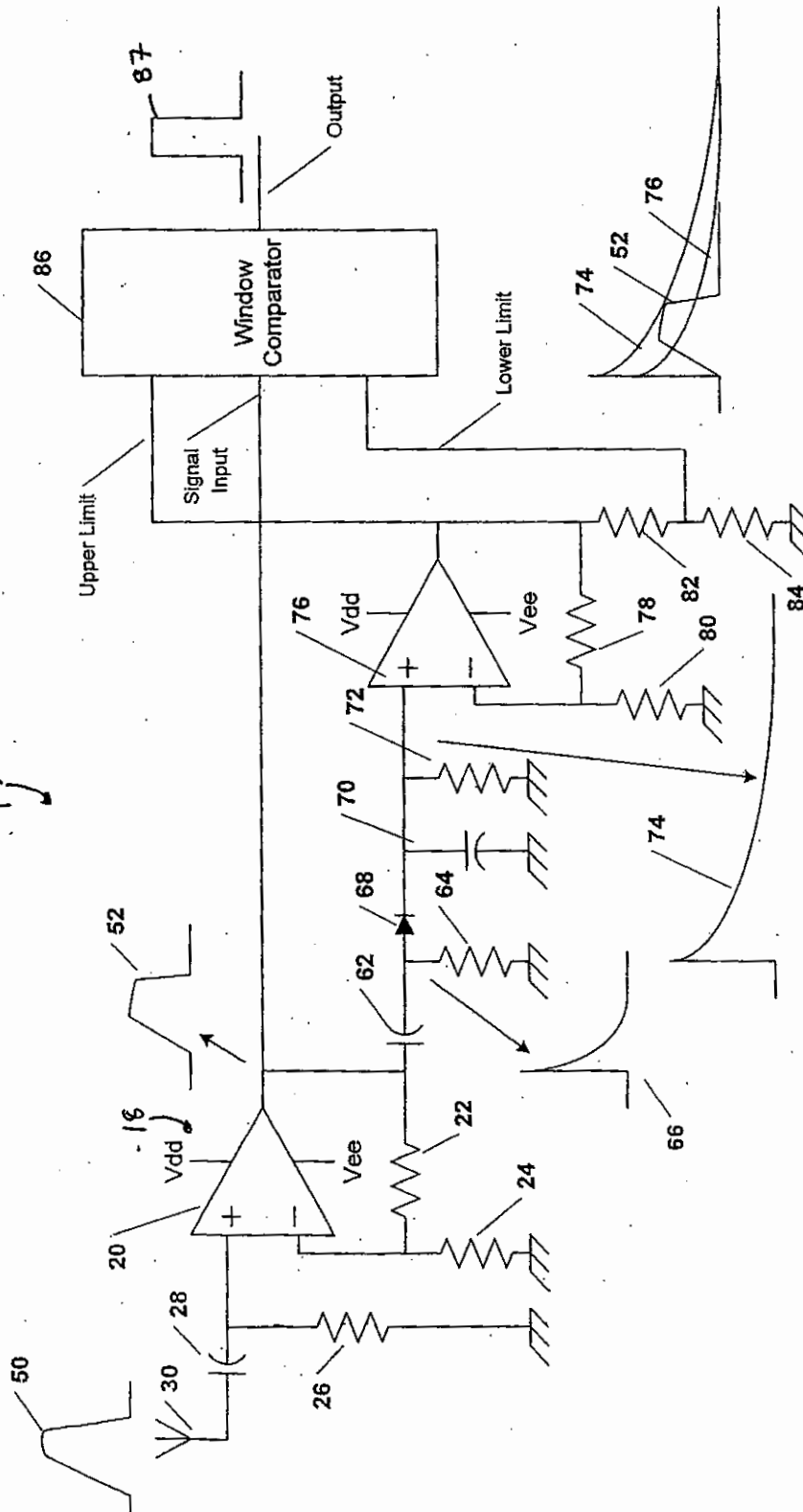


Figure 4

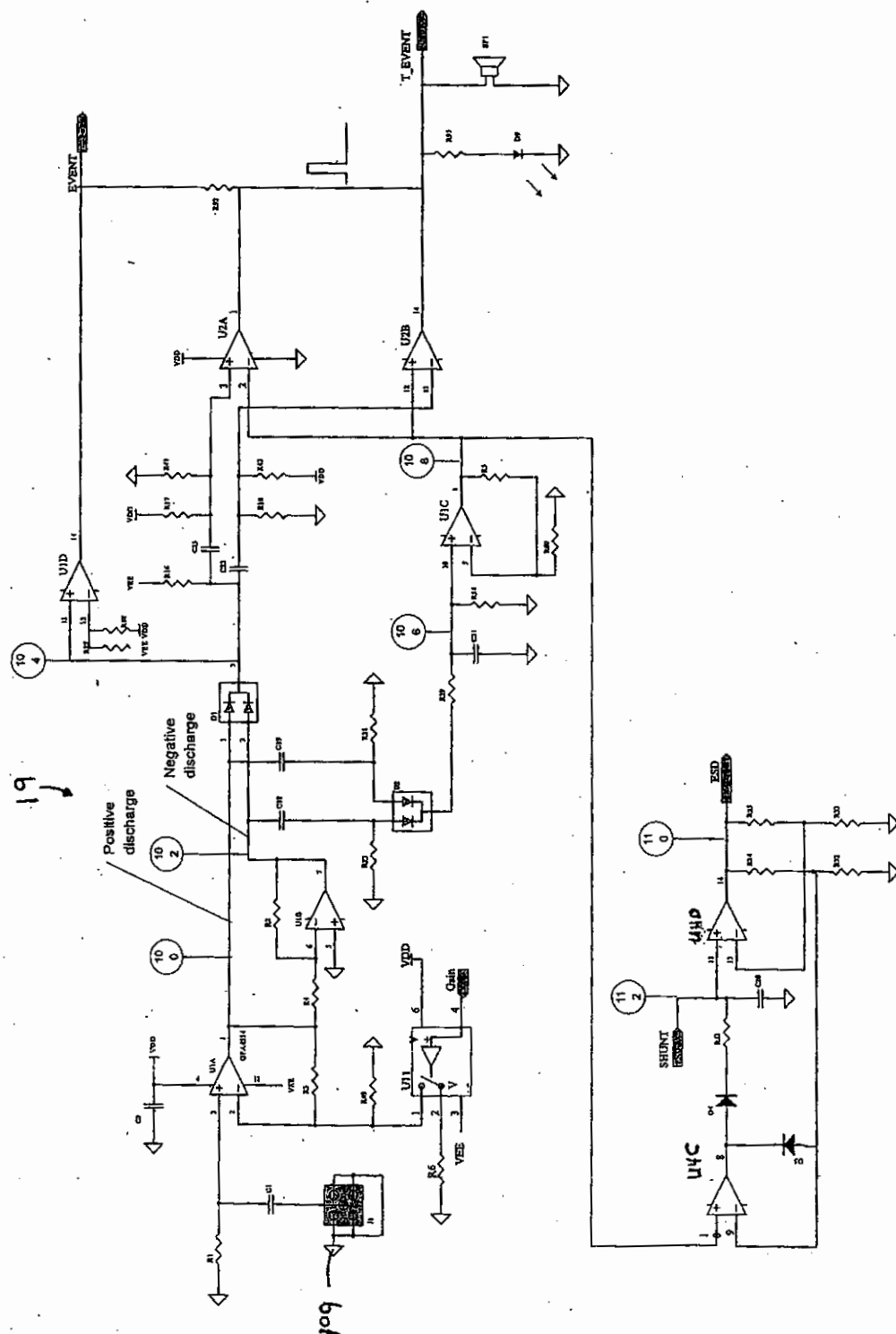


Figure 5

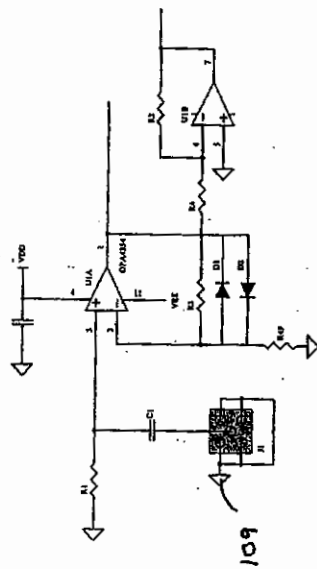


Figure 5a

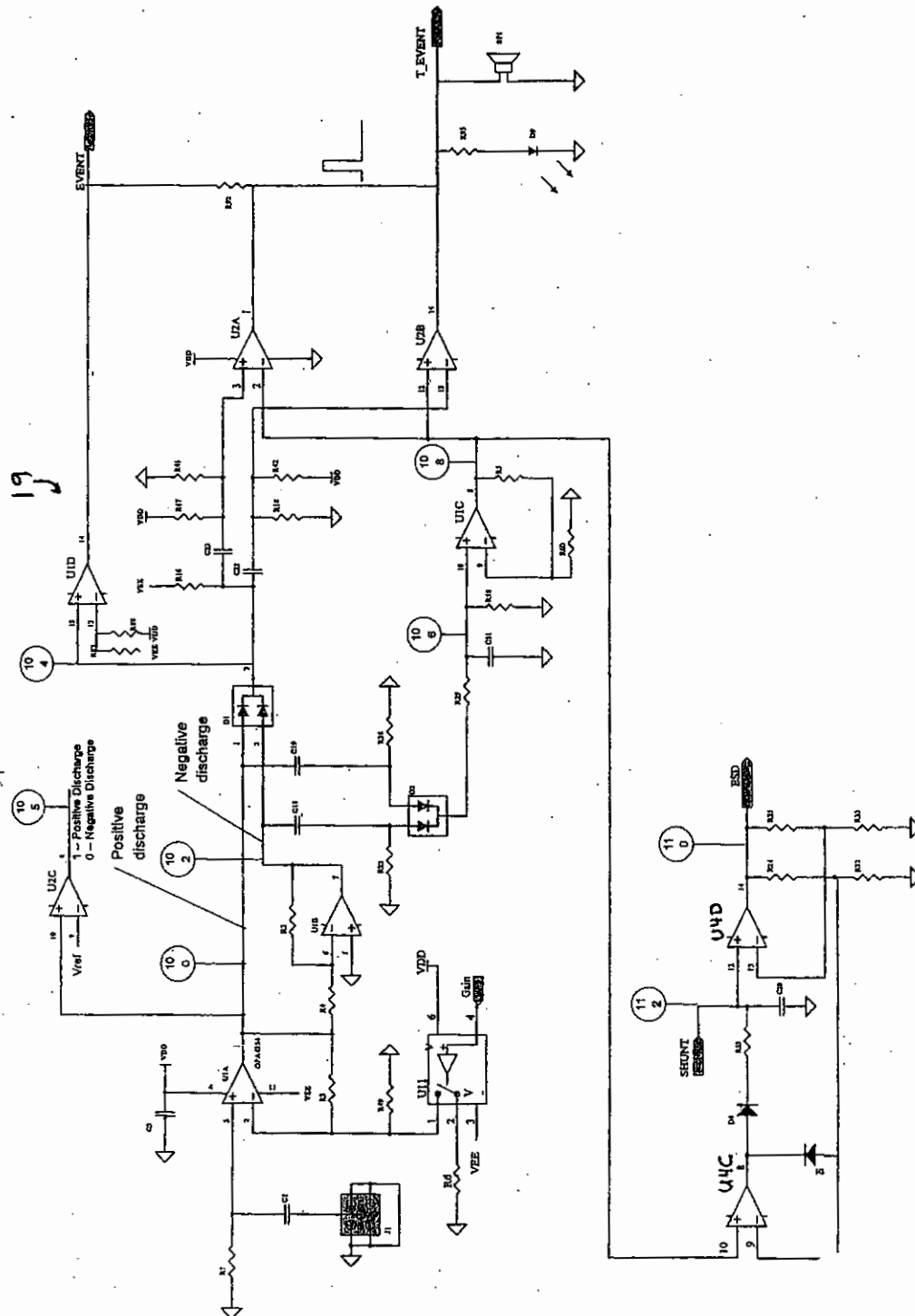


Figure 5b

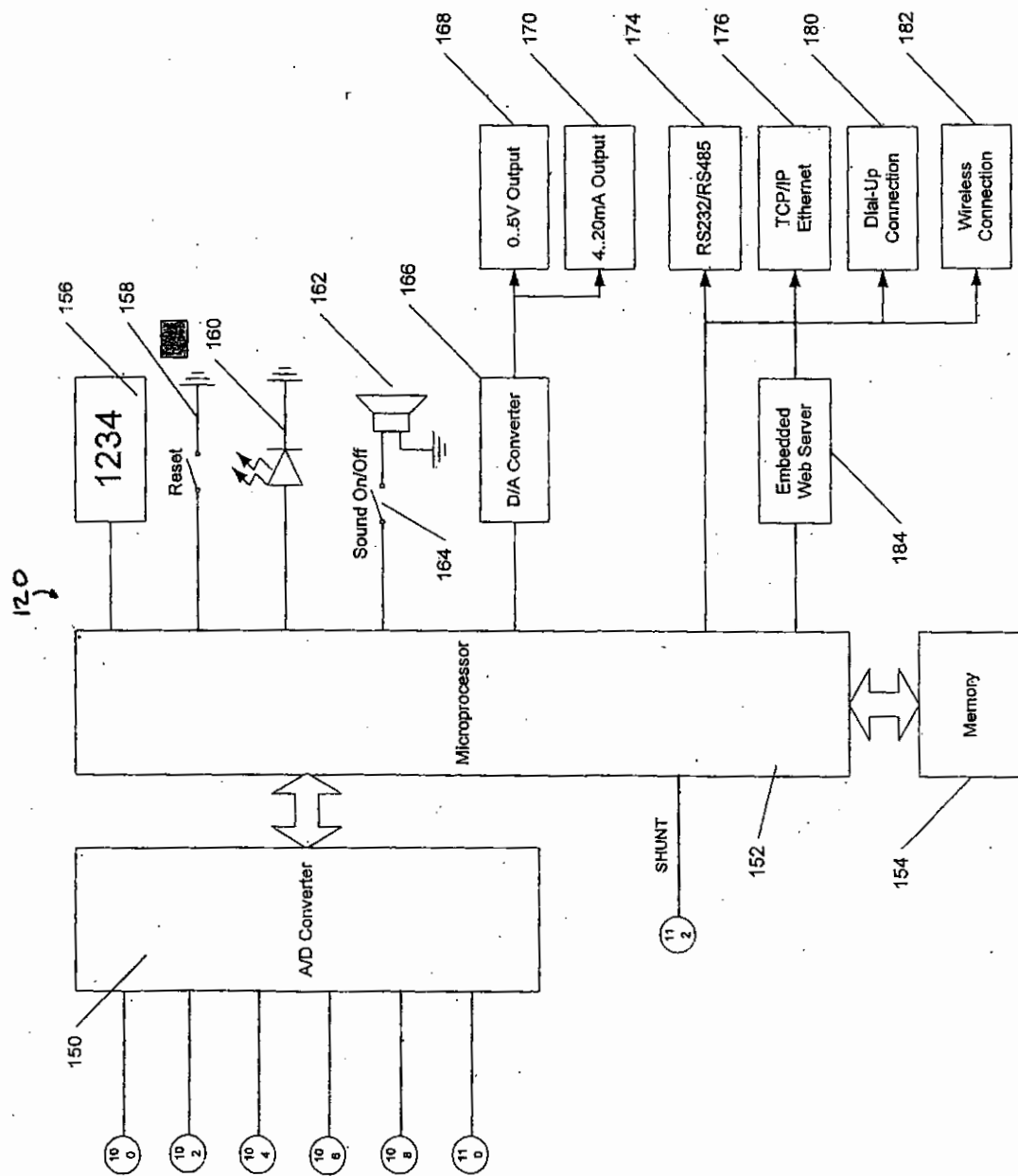


Figure 6

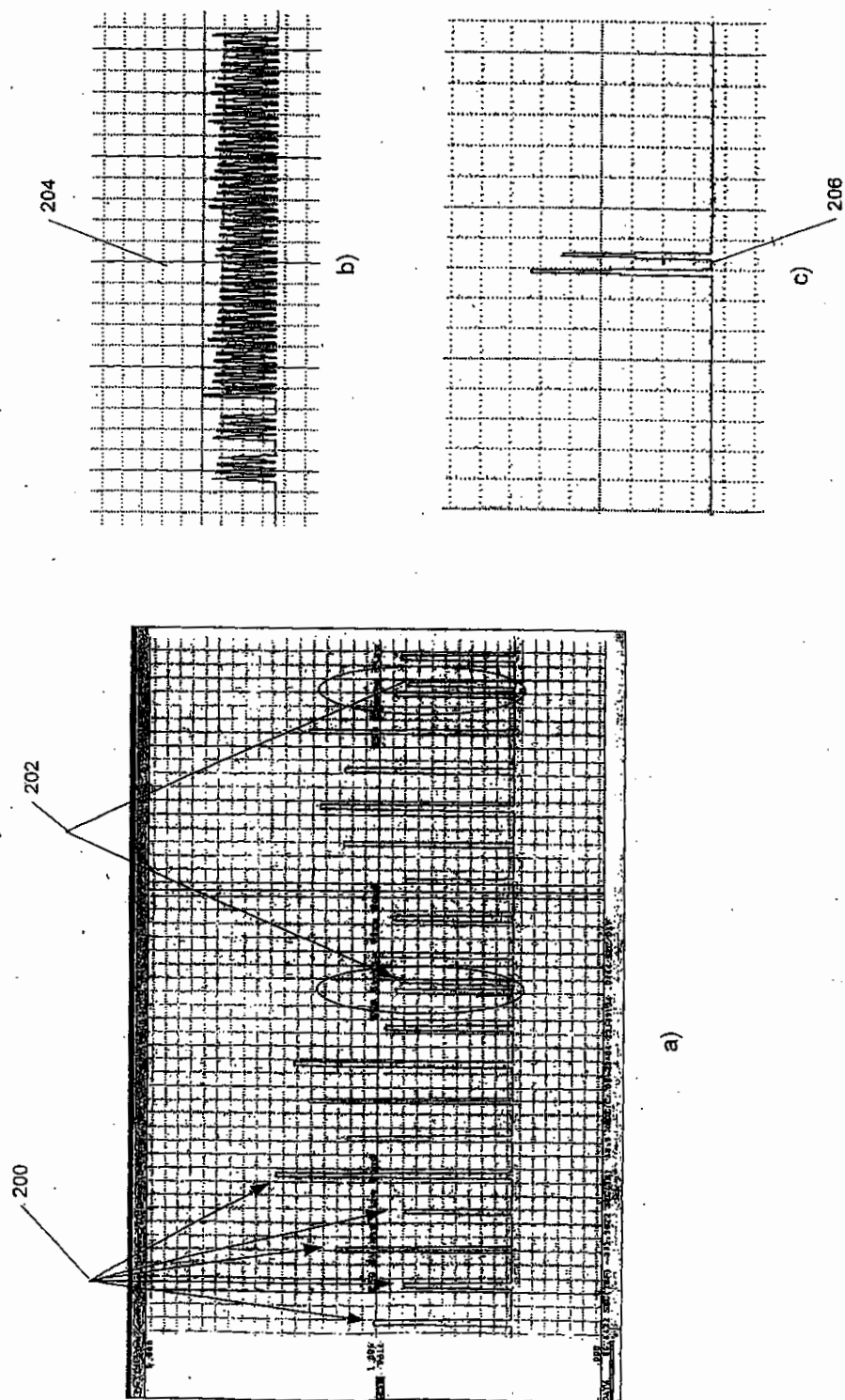


Figure 7

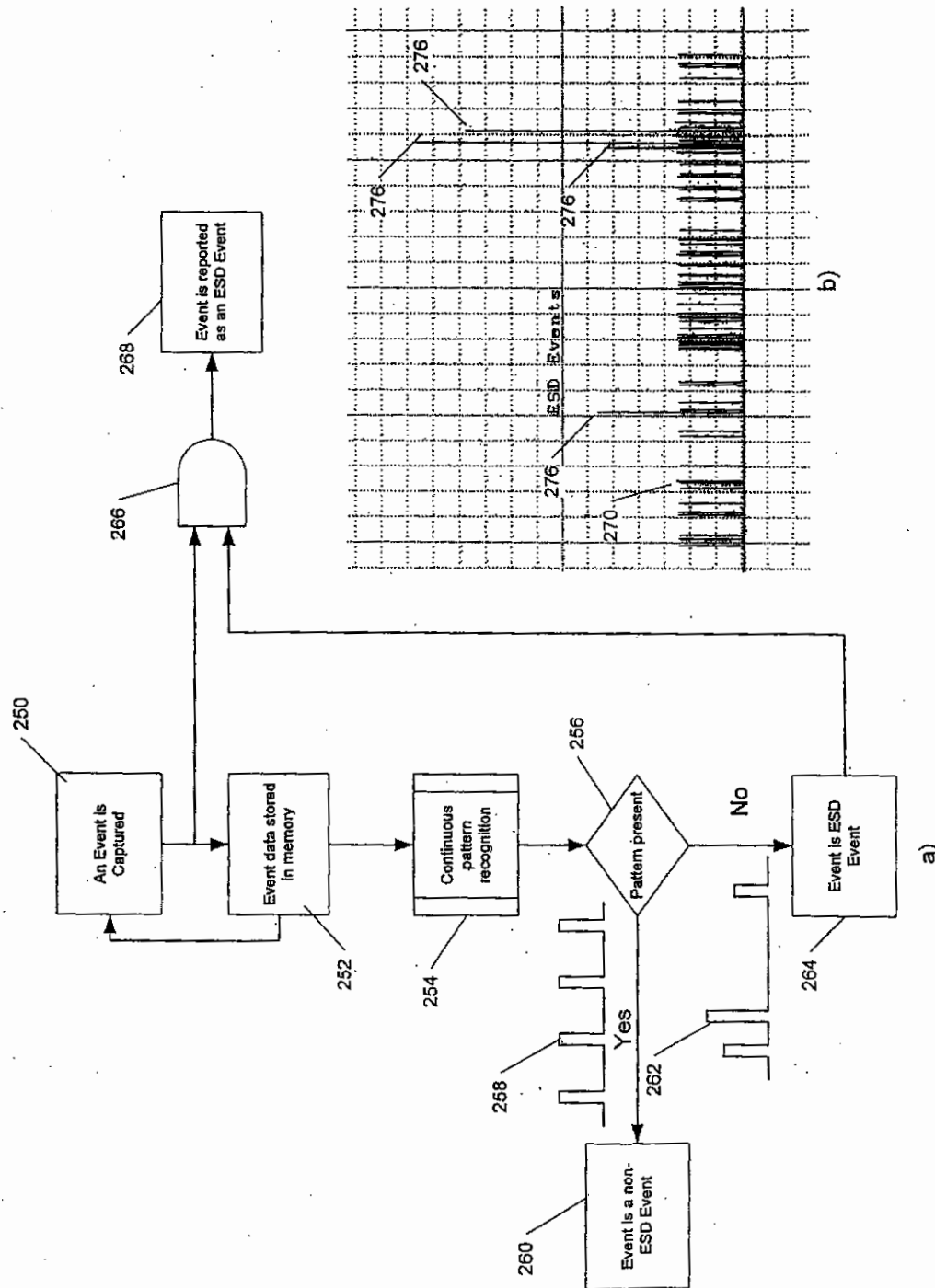


Figure 8

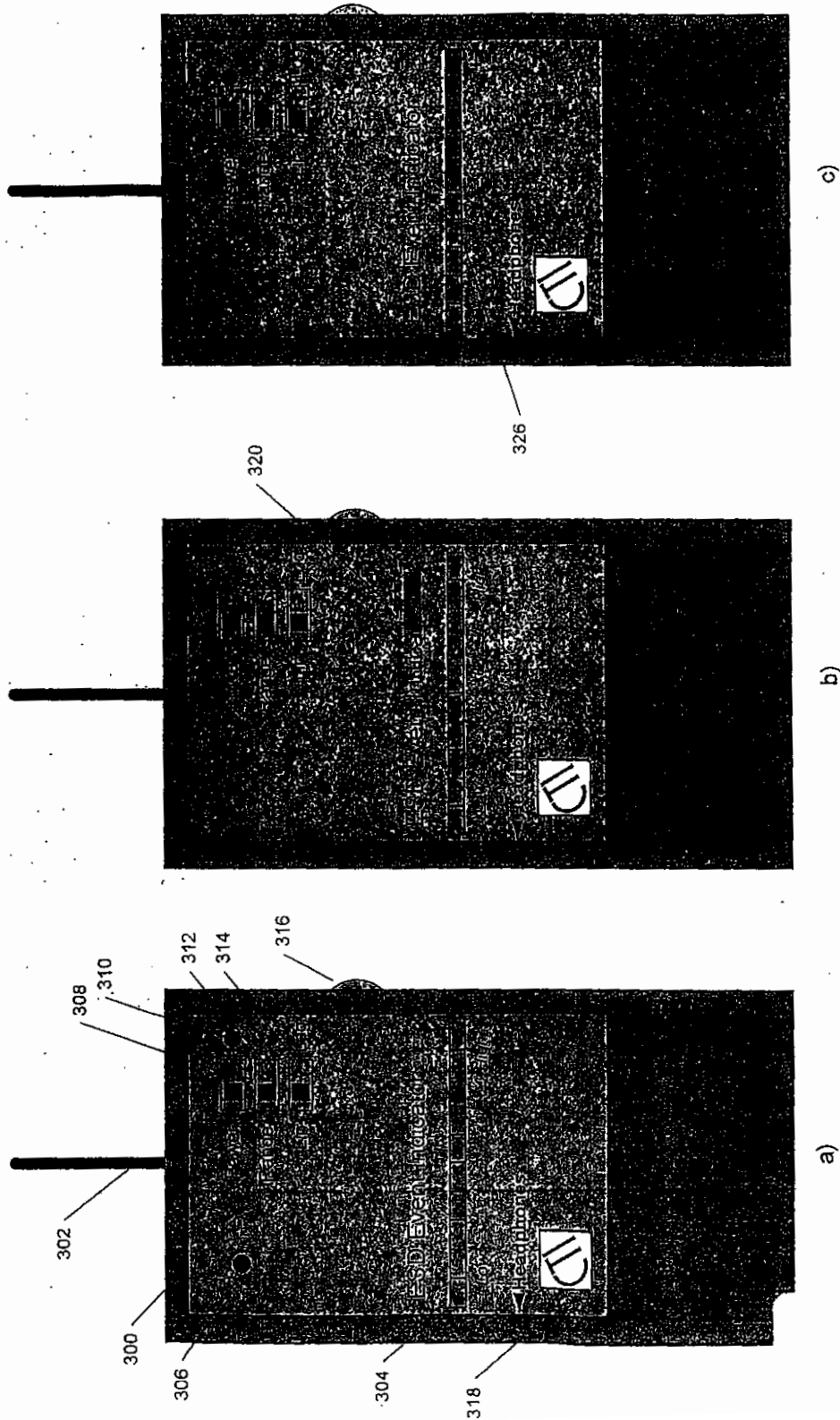


Figure 9

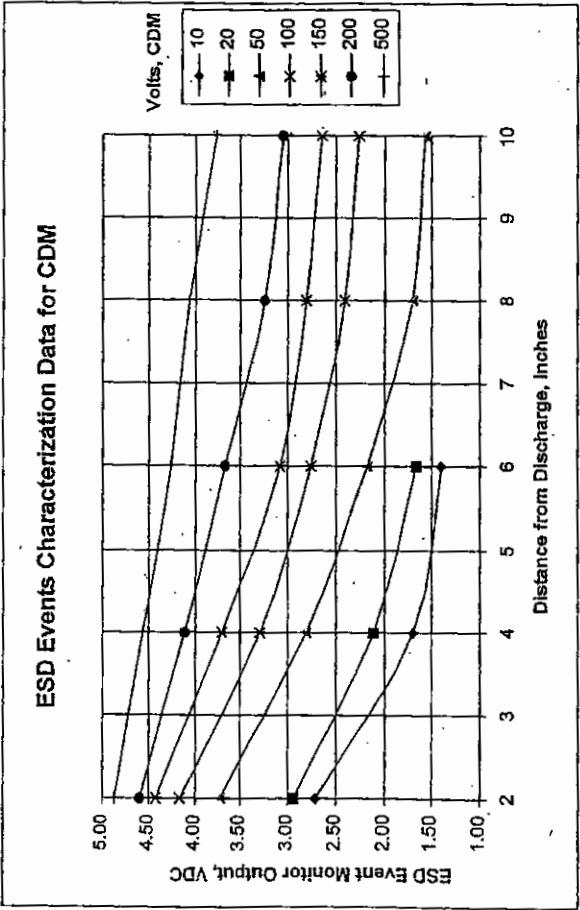


Figure 10

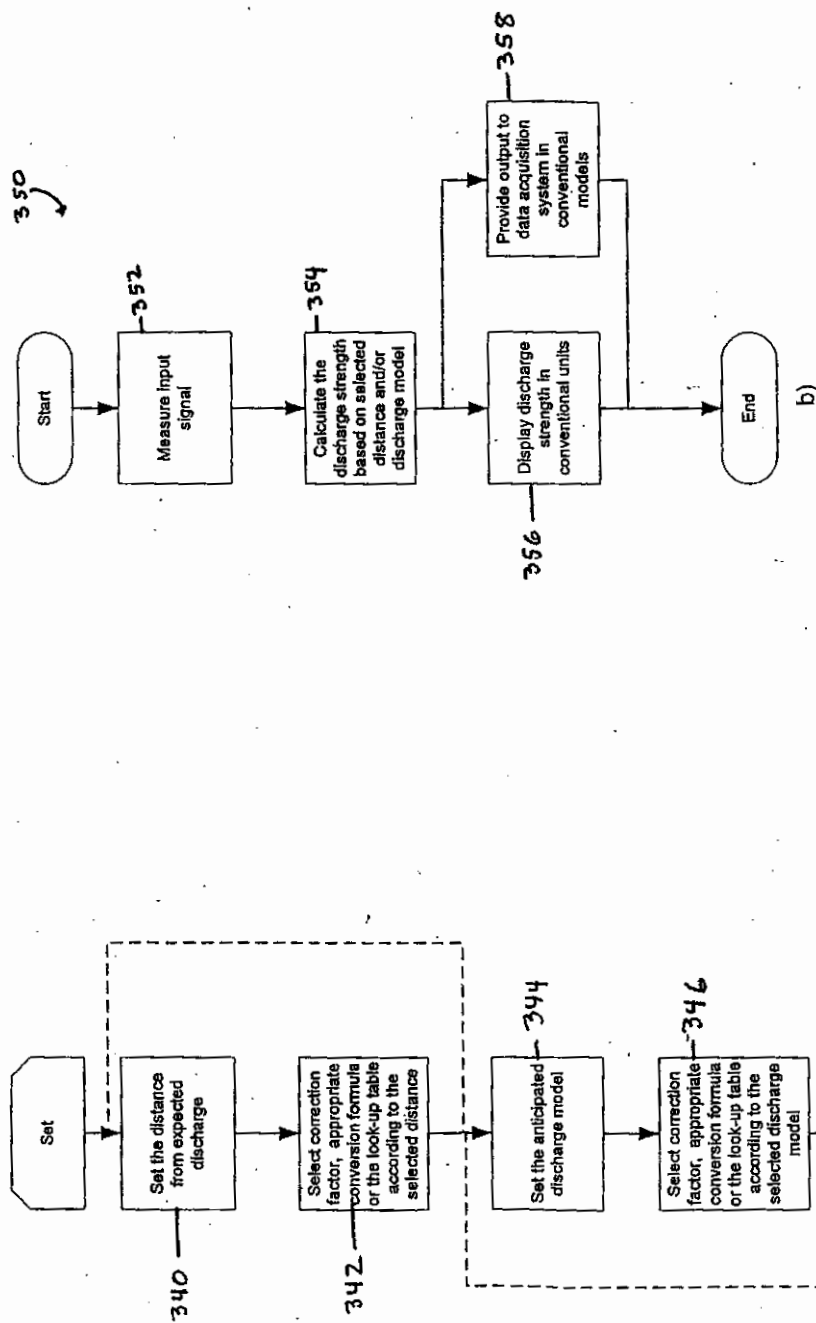


Figure 11

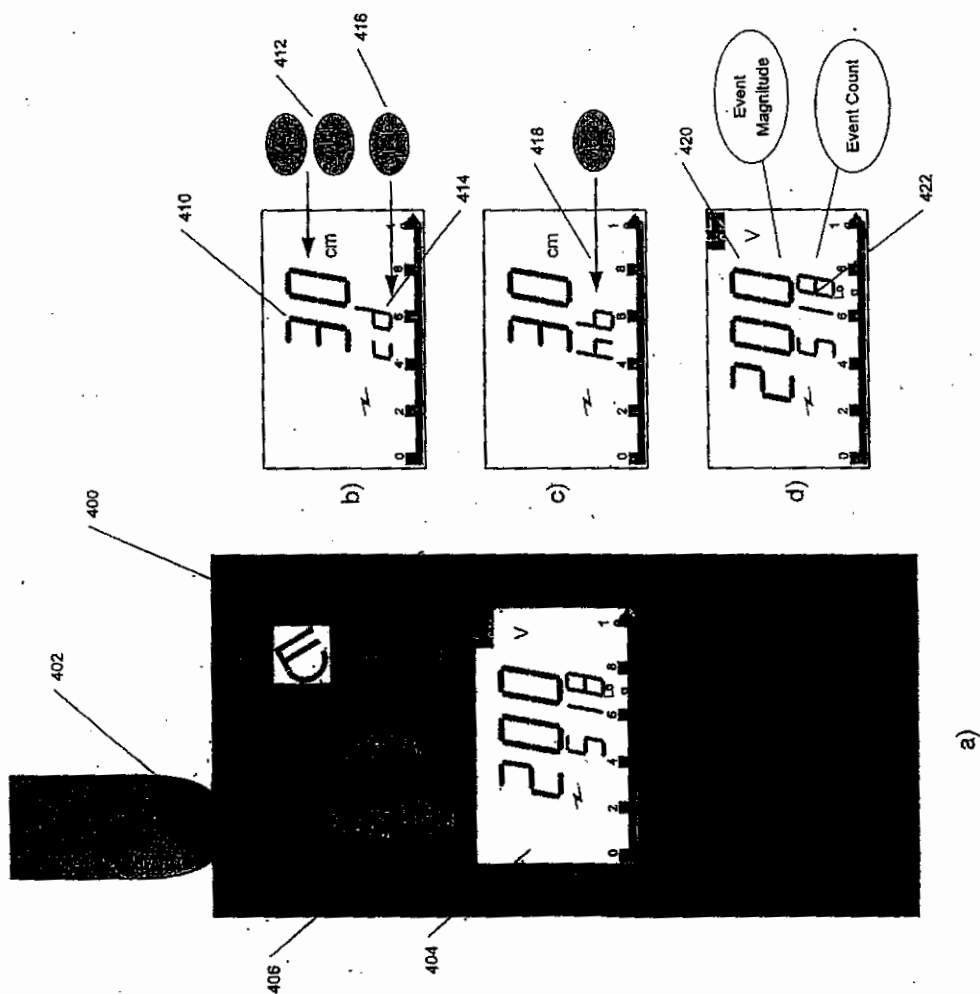
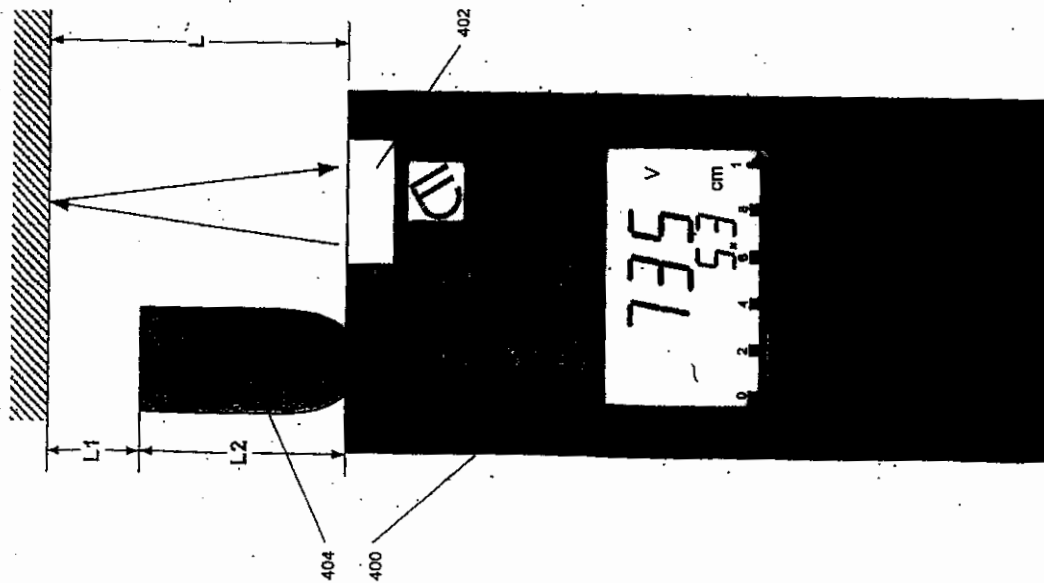
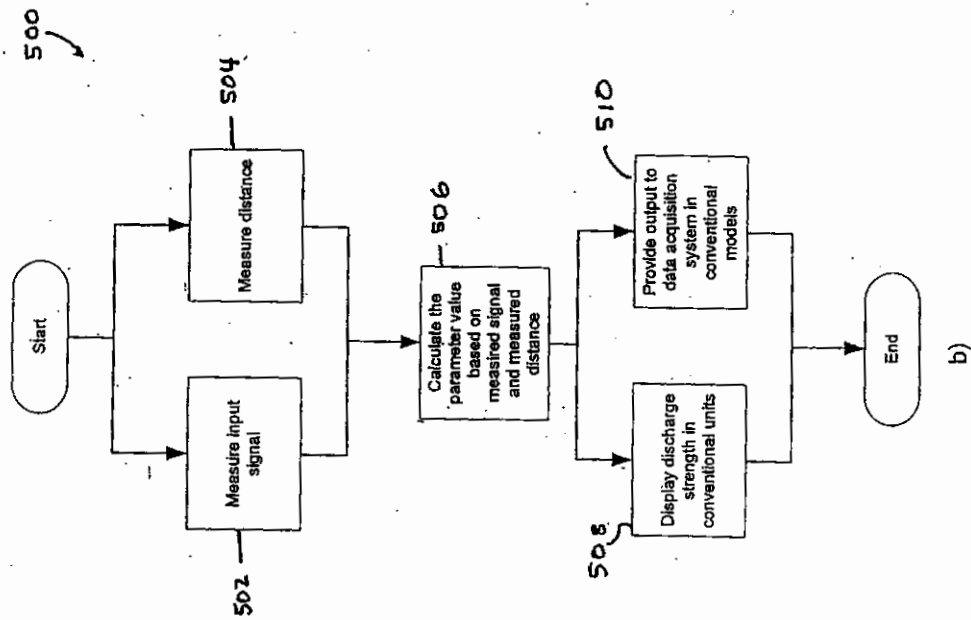


Figure 12



a)



b)

Figure 13

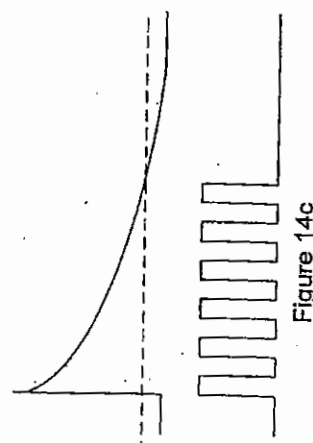
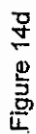
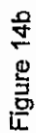
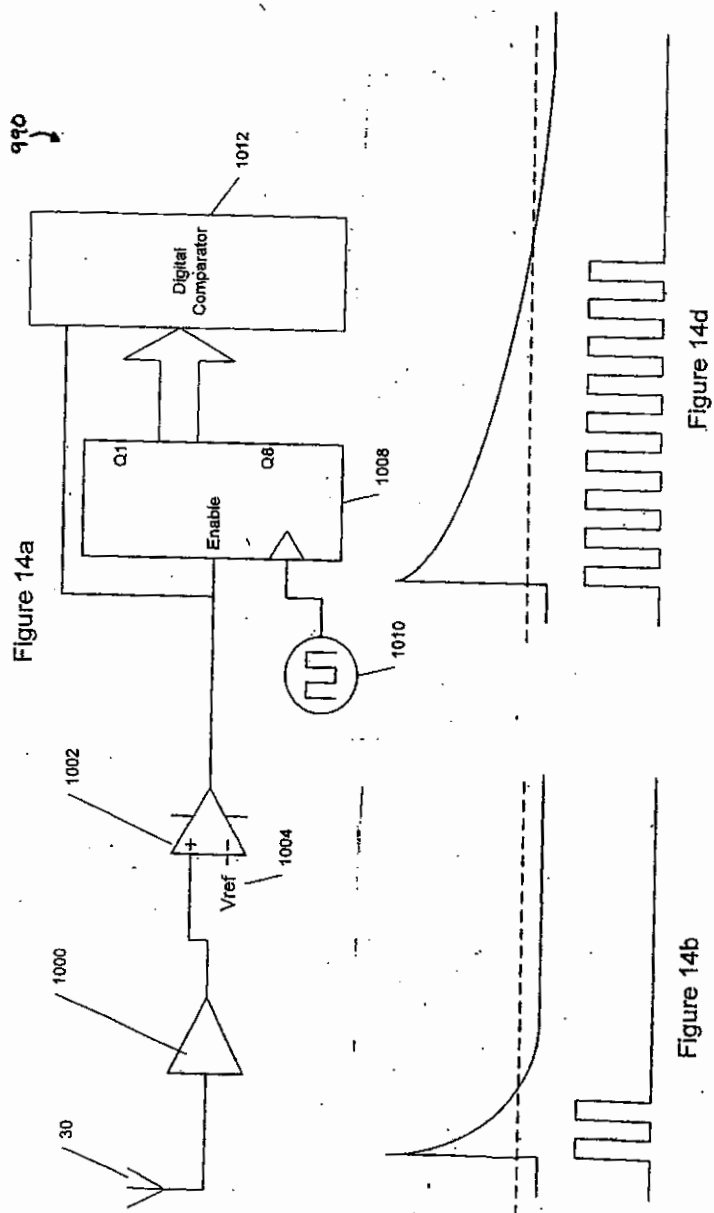


Figure 14

Figure 16a

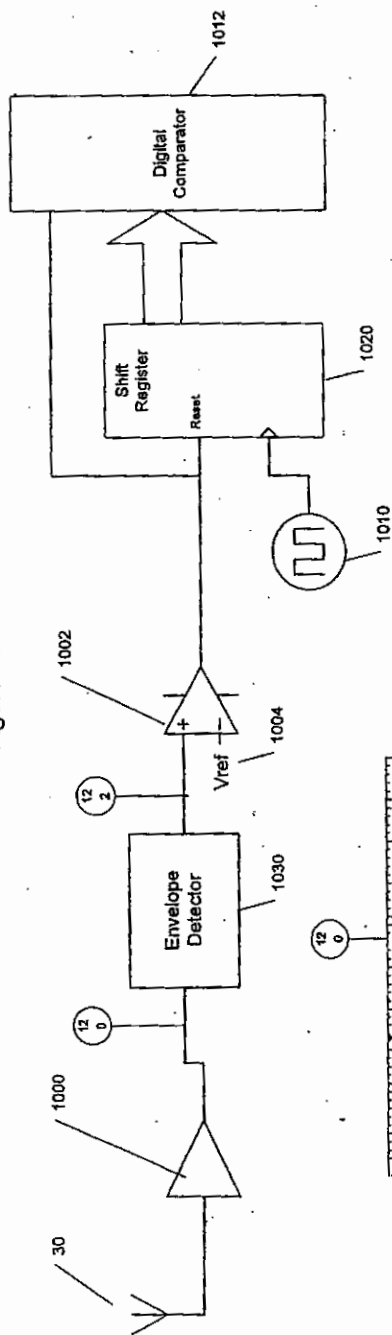


Figure 16b
Raw Signal

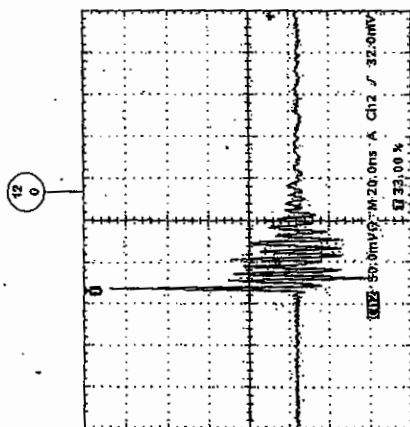


Figure 16c
Signal Envelope

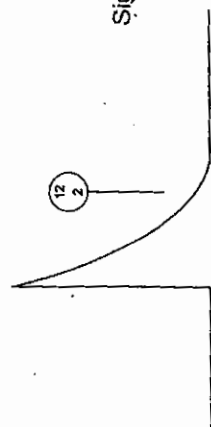


Figure 16

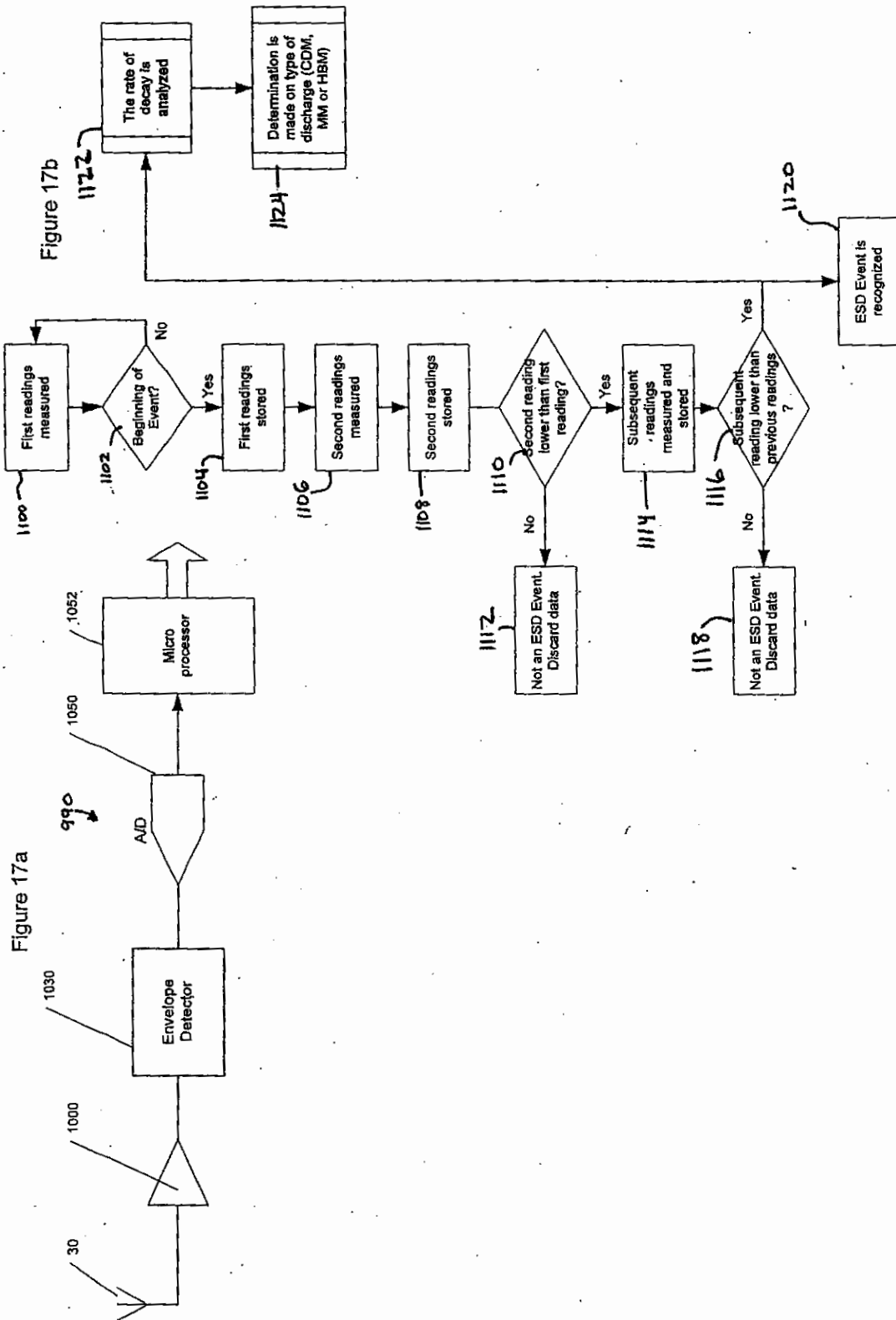


Figure 17

Figure 18a

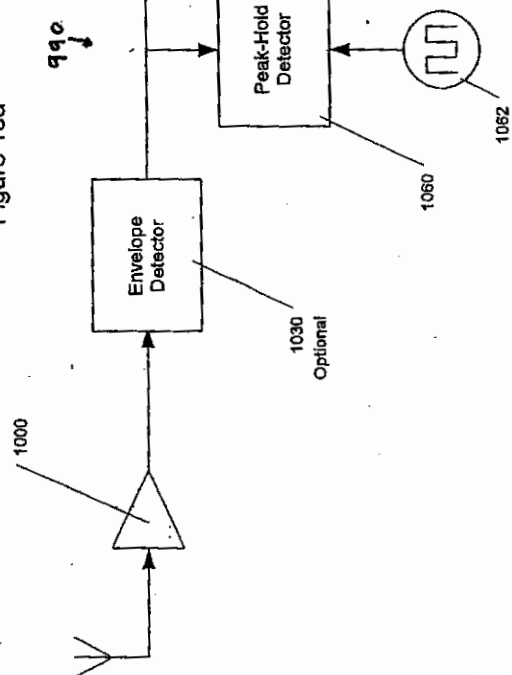


Figure 18b

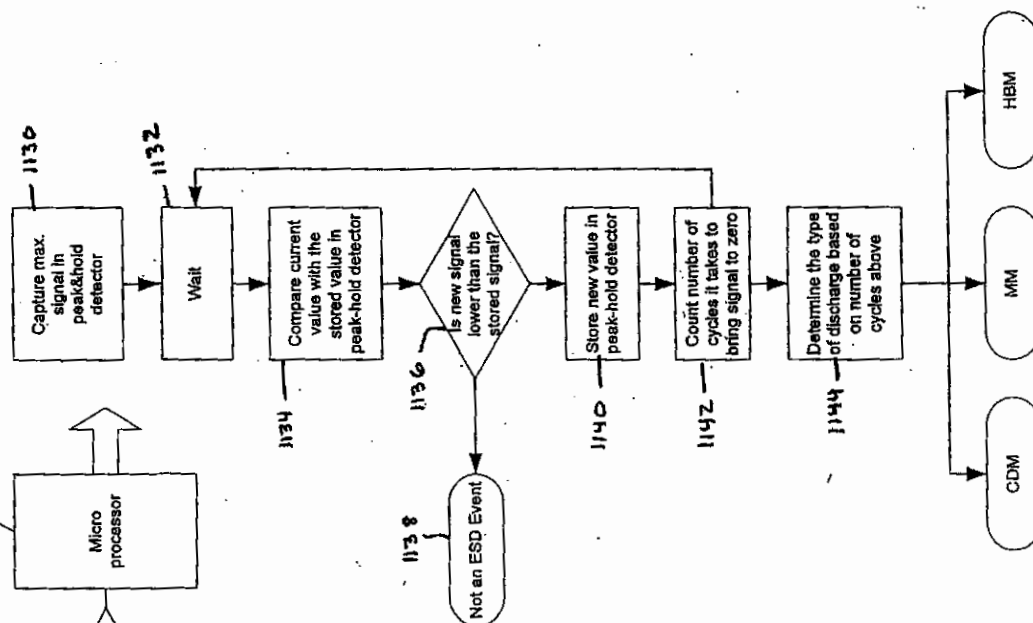


Figure 18