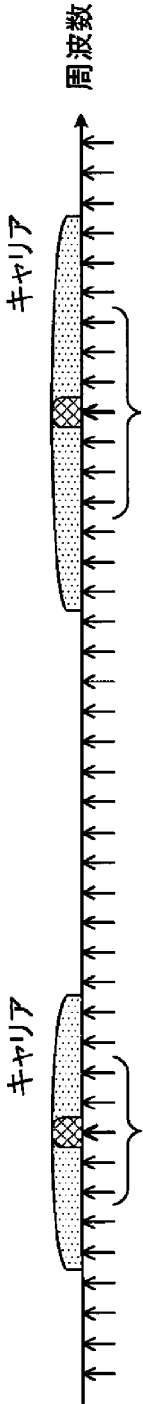
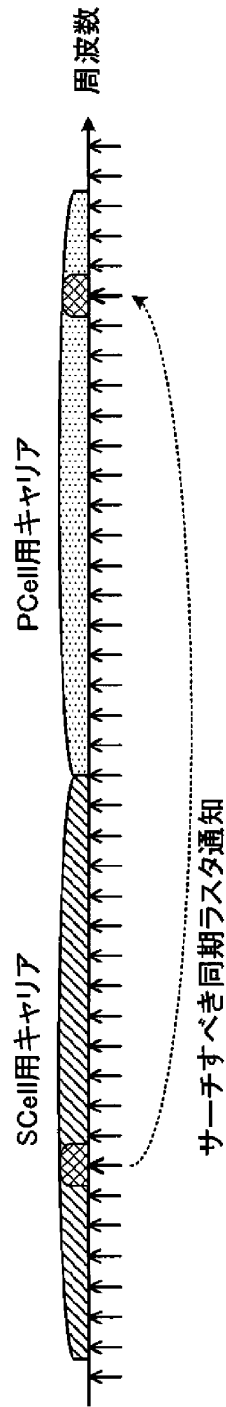


[図1]



[図2]



[図3]

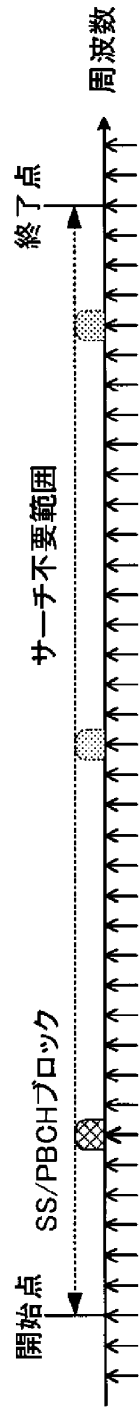
図3A

k_{SSB}	RMSI-PDCCH-Config	N_{SSB}^{PDCCH}
24	0, 1, ..., 255	1, 2, ..., 255
25	0, 1, ..., 255	257, 258, ..., 512
26	0, 1, ..., 255	513, 514, ..., 768
27	0, 1, ..., 255	-1, -2, ..., -255
28	0, 1, ..., 255	-257, -258, ..., -512
29	0, 1, ..., 255	-513, -514, ..., -768
30	0, 1, ..., 255	Reserved, Reserved, ..., Reserved

図3B

k_{SSB}	RMSI-PDCCH-Config	N_{SSB}^{PDCCH}
12	0, 1, ..., 255	1, 2, ..., 255
13	0, 1, ..., 255	-1, -2, ..., -255
14	0, 1, ..., 255	Reserved, Reserved, ..., Reserved

[図4]



[図5]

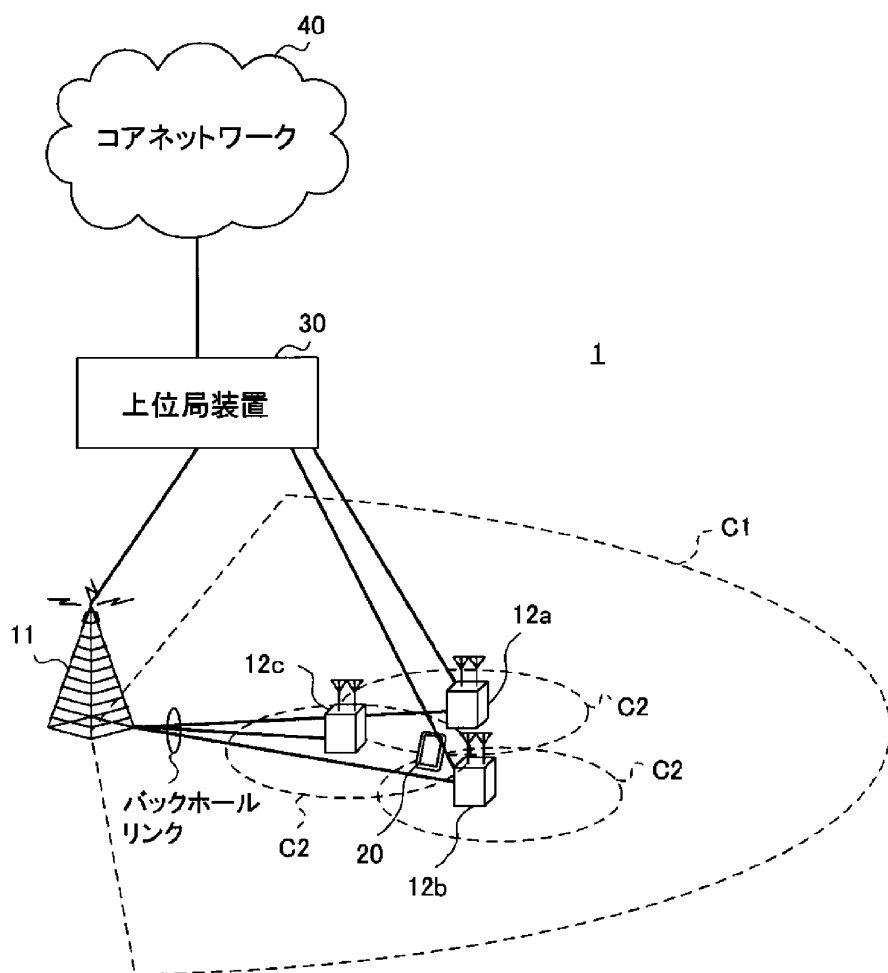
図5A

k_{SSB}	RMSI-PDCCH-Config	N_{RB}^{SSB}
24	0, 1, ..., 255	1, 2, ..., 255
25	0, 1, ..., 255	257, 258, ..., 512
26	0, 1, ..., 255	513, 514, ..., 768
27	0, 1, ..., 255	-1, -2, ..., -255
28	0, 1, ..., 255	-257, -258, ..., -512
29	0, 1, ..., 255	-513, -514, ..., -768
30	0, 1, ..., 255	0, Reserved, ..., Reserved

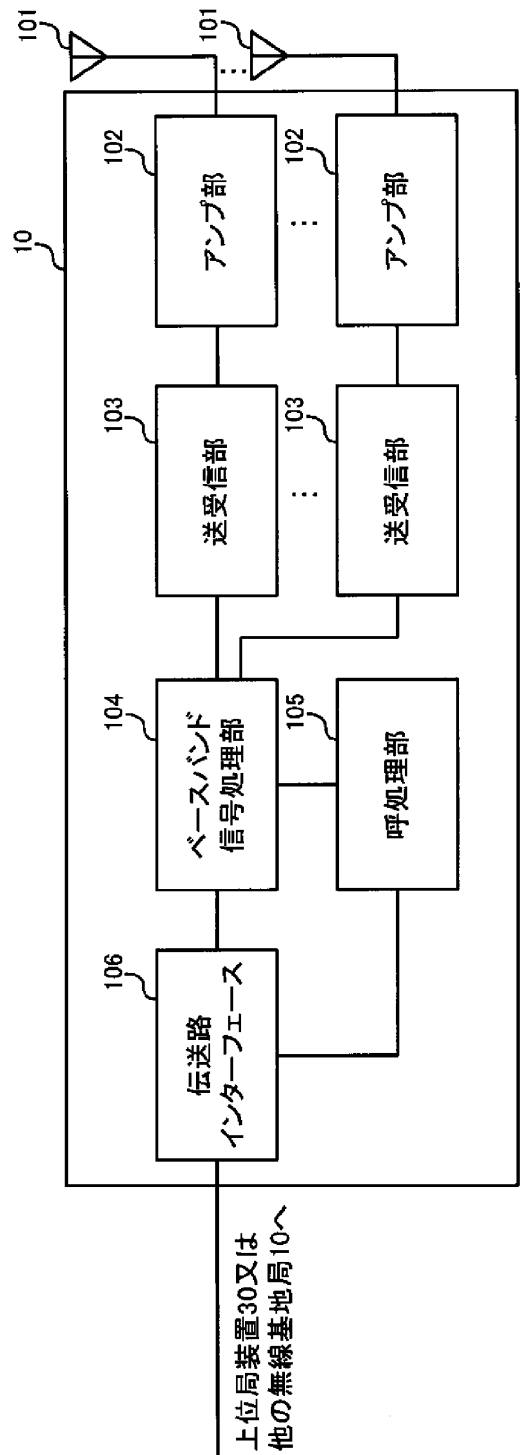
図5B

k_{SSB}	RMSI-PDCCH-Config	N_{RB}^{SSB}
12	0, 1, ..., 255	1, 2, ..., 255
13	0, 1, ..., 255	-1, -2, ..., -255
14	0, 1, ..., 255	0, Reserved, ..., Reserved

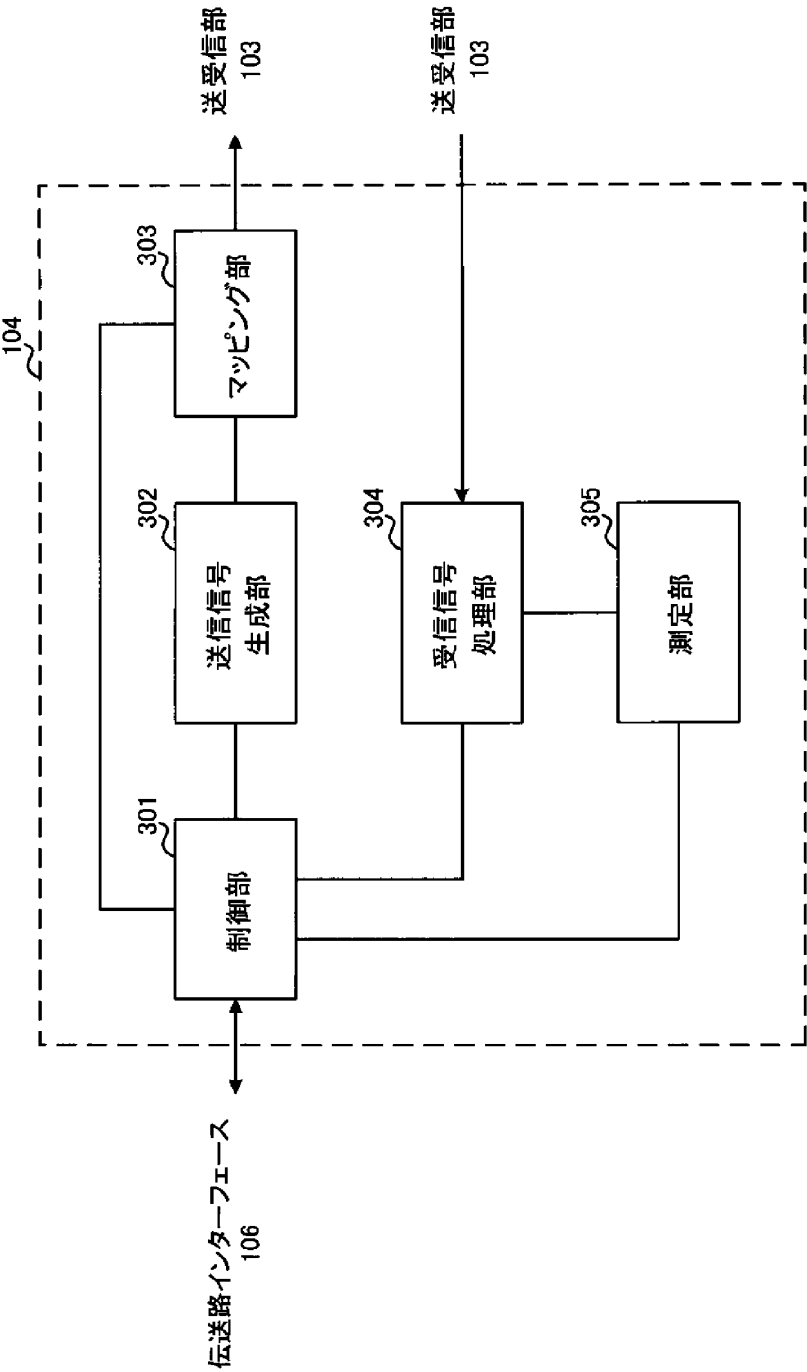
[図6]



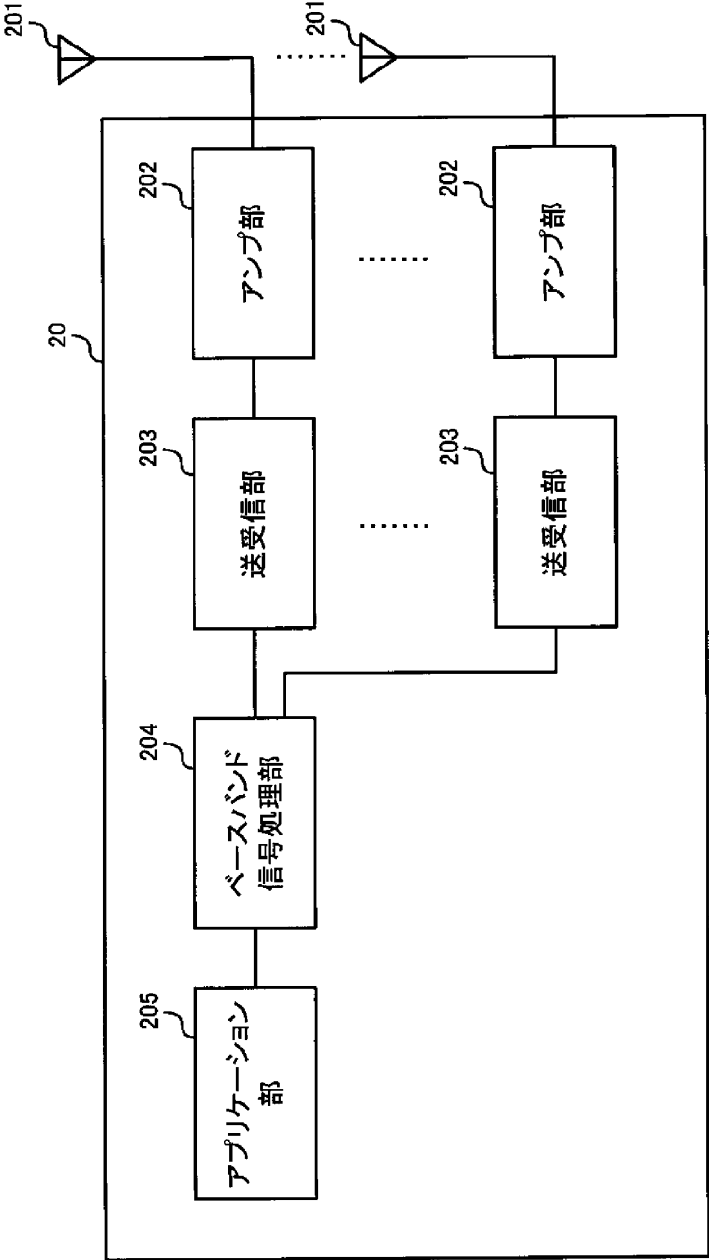
[図7]



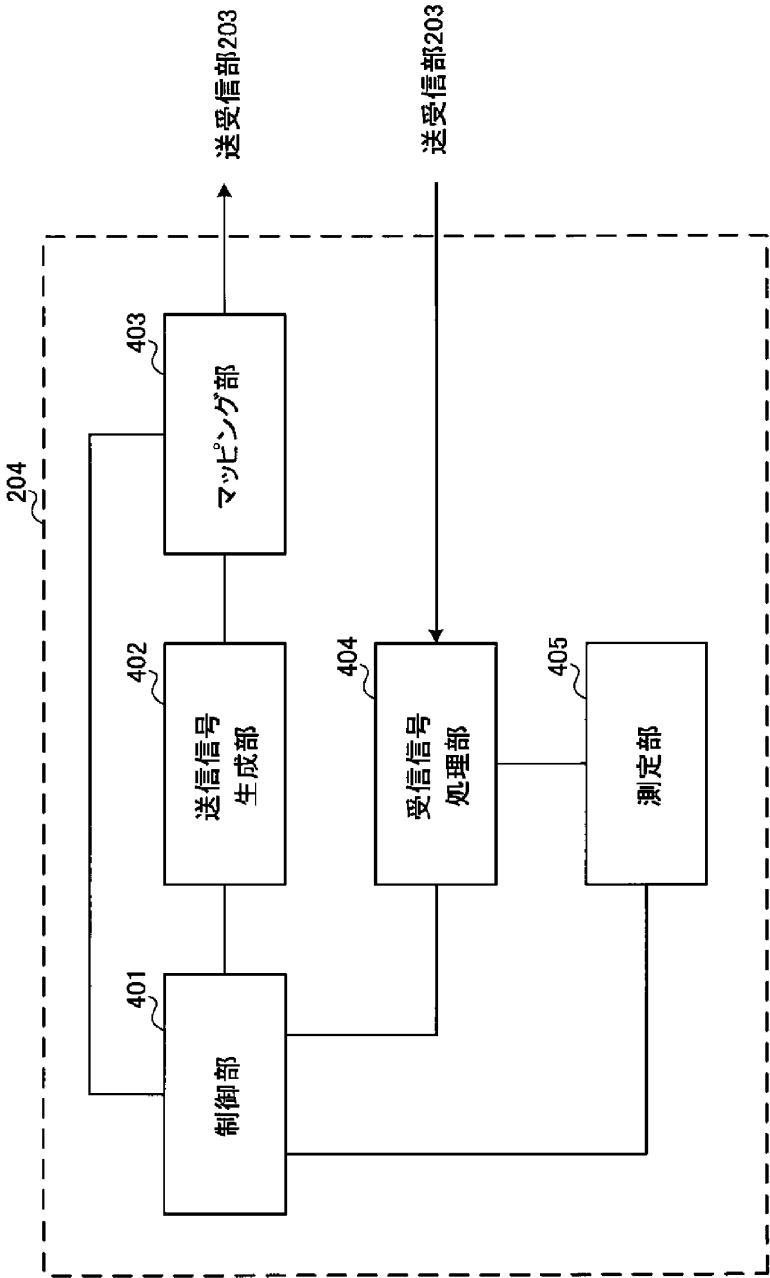
[図8]



[図9]



[図10]



[図11]

