CLAIMS

1. A method of encoding video data into a bitstream, comprising:

for a coding block, transmitting a first control flag in the bitstream, wherein the first control flag indicates whether to use a first technique, wherein the first technique comprises using a separate merge list for subblock merge candidates; and

in response to determining the first technique is not used for the coding block, transmitting a second control flag in the bitstream, wherein the second control flag indicates whether to use a second technique, wherein the second technique comprises multi-hypothesis prediction for intra and inter mode, wherein the second control flag is transmitted if and only if the first technique is not used for the coding block.

2. The method according to claim 1, further comprising: if the coding block is coded in merge mode, transmitting the second control flag.

3. The method according to claim 2, further comprising: if the coding block is not coded in merge mode, determining that the second control flag is not to be transmitted.

4. The method according to claim 1, further comprising: if the coding block is coded in skip mode, transmitting the second control flag.

5. The method according to claim 4, further comprising: if the coding block is not coded in skip mode or in merge mode, determining that the second control flag is not to be transmitted.

6. A method of decoding video data received in a bitstream, comprising:

for a coding block, receiving a first control flag from the bitstream, wherein the first control flag indicates whether to use a first technique, wherein the first technique comprises using a separate merge list for subblock merge candidates; and

in response to determining the first technique is not used for the coding block, receiving a second control flag from the bitstream, wherein the second control flag indicates whether to use a

second technique, wherein the second technique comprises multi-hypothesis prediction for intra and inter mode, wherein only if the first technique is not used, the second control flag is received.

7. The method according to claim 6, further comprising: if the coding block is coded in merge mode, receiving the second control flag.

8. The method according to claim 7, further comprising: if the coding block is not coded in merge mode, determining that the second control flag is not to be received.

9. The method according to claim 6, further comprising: if the coding block is coded in skip mode, receiving the second control flag.

10. The method according to claim 9, further comprising: if the coding block is not coded in skip mode or in merge mode, determining that the second control flag is not to be received.

11. An encoder comprising processing circuitry for carrying out the method according to claim 1.

12. A decoder comprising processing circuitry for carrying out the method according to claim 6.

13. A computer program comprising program code for performing a method according to claim 1 when executed on a computer or a processor.