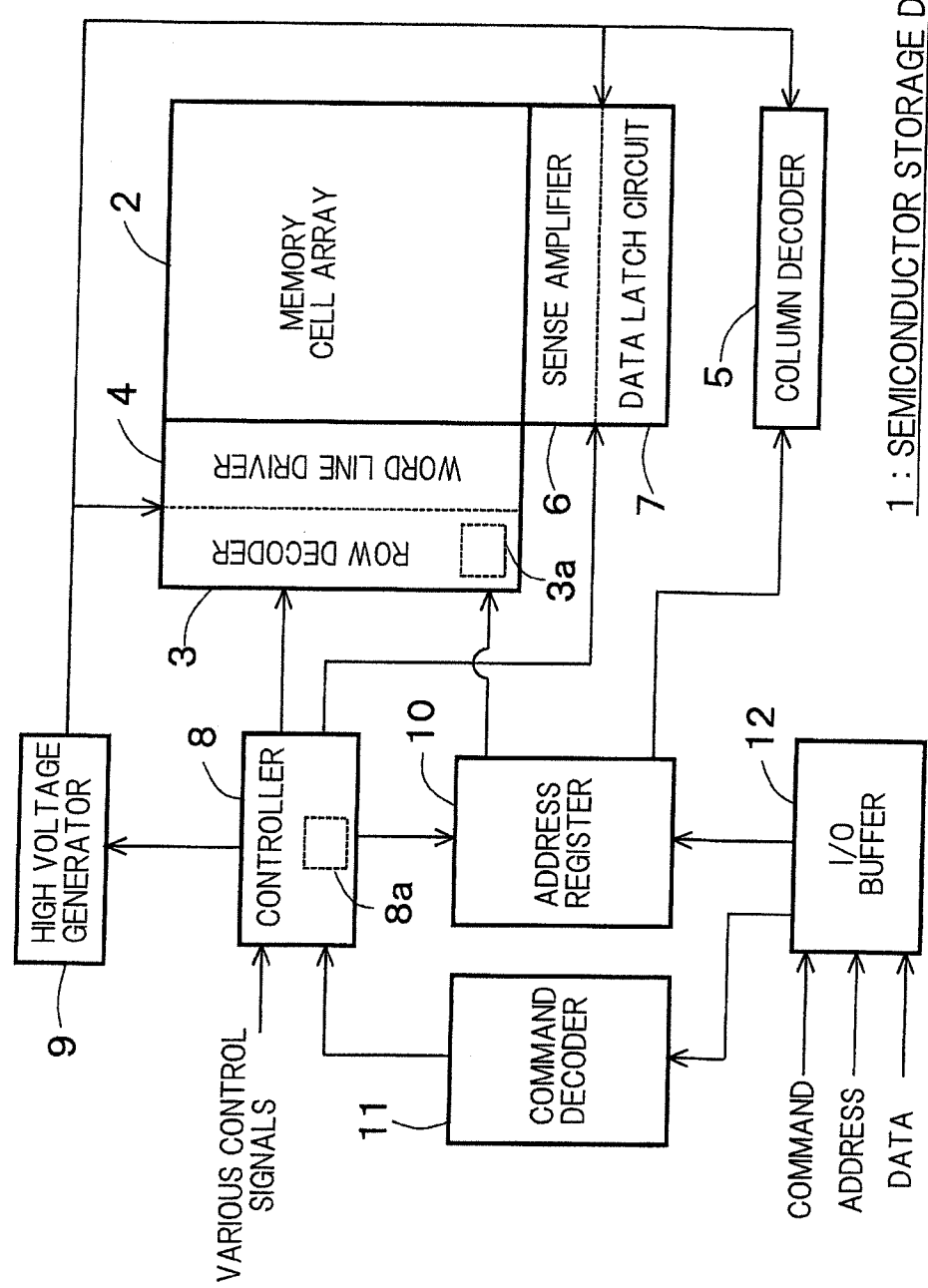




1 : SEMICONDUCTOR STORAGE DEVICE

FIG.1

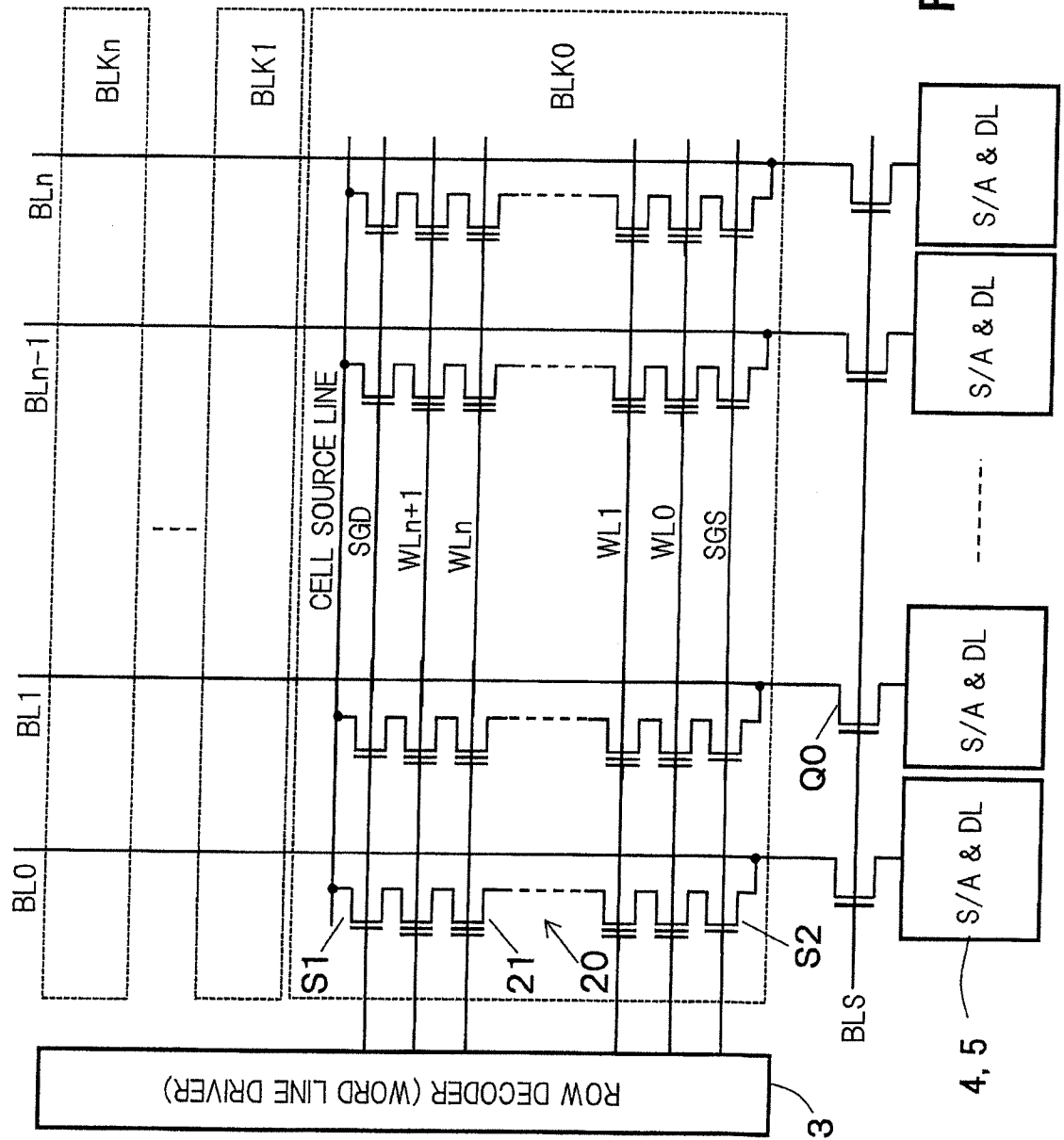


FIG. 2

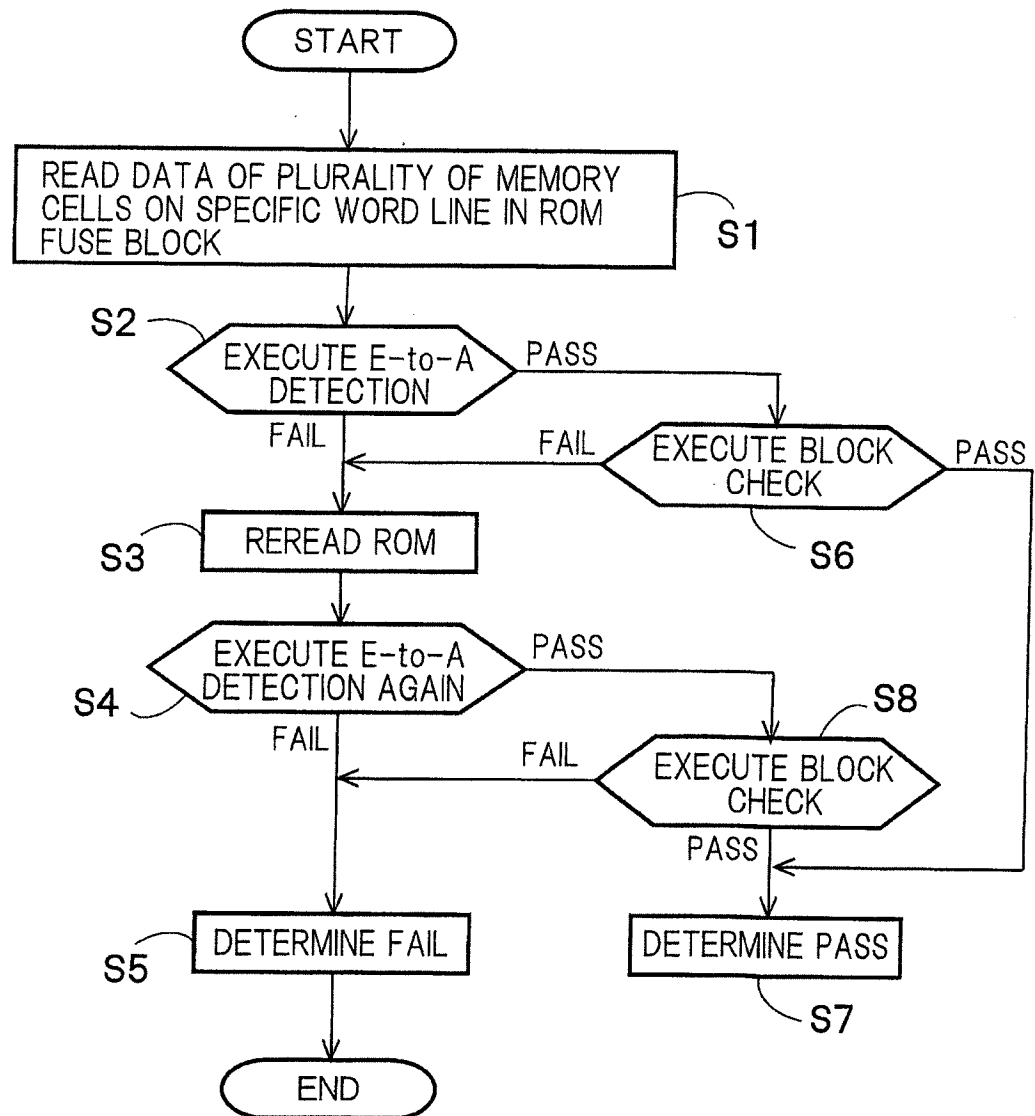


FIG. 3

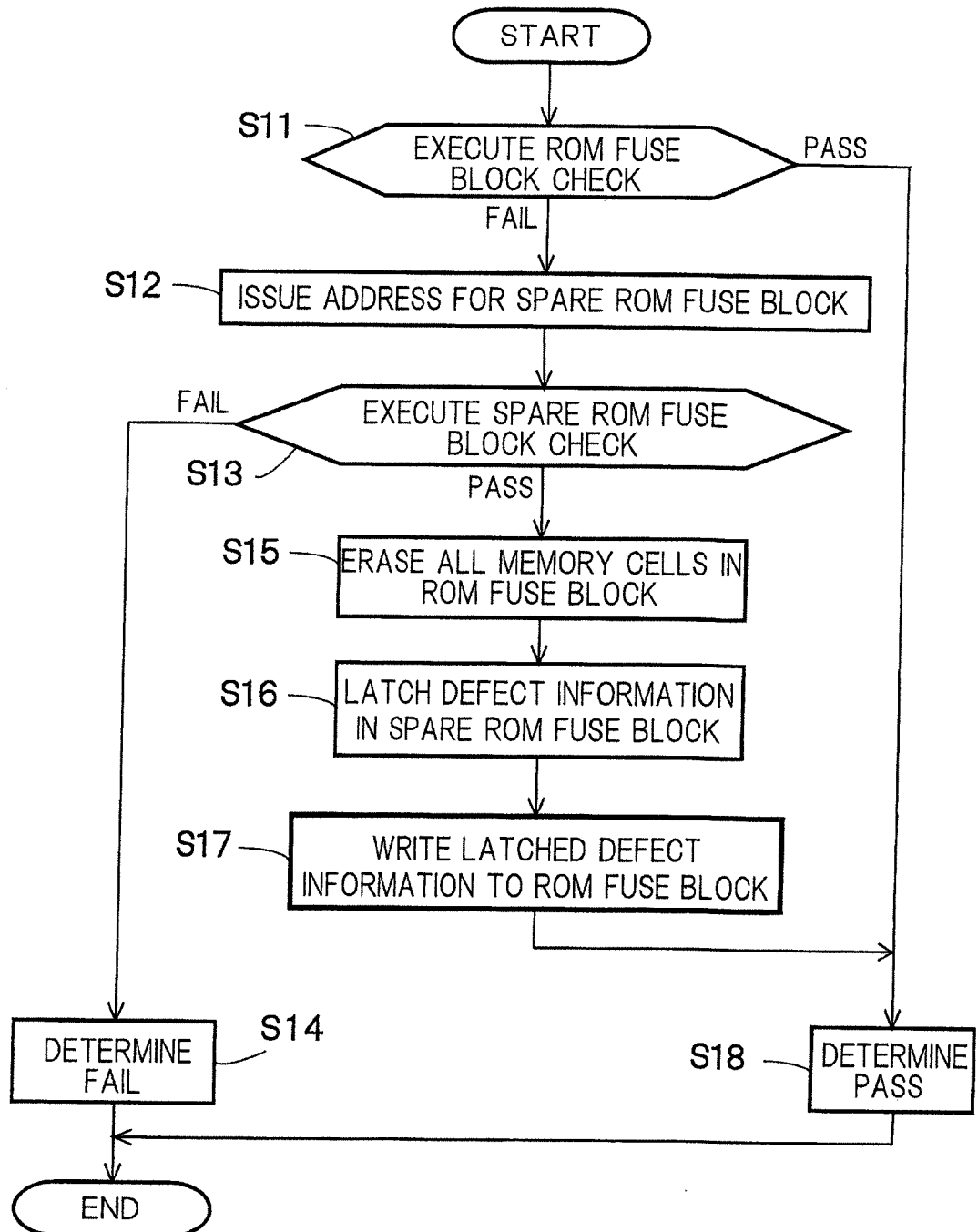


FIG. 4

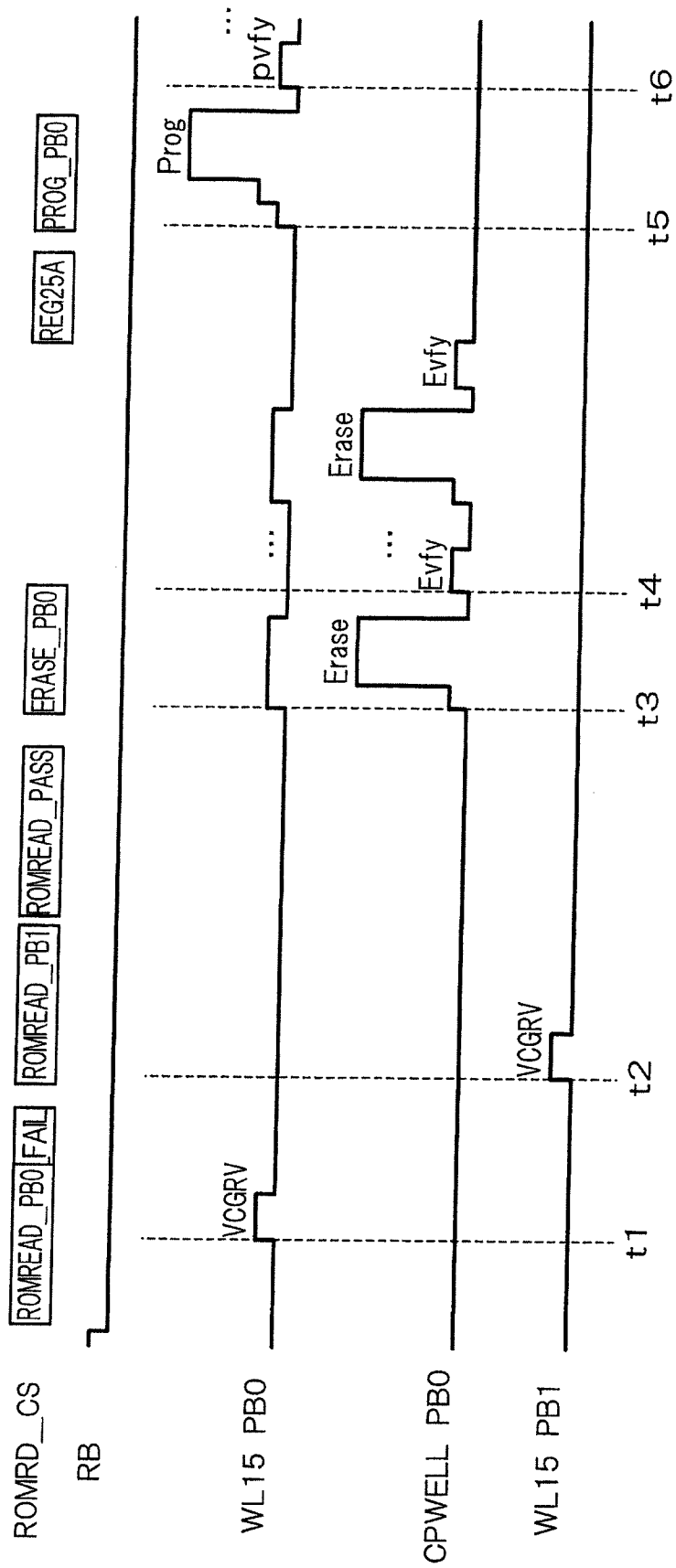


FIG. 5

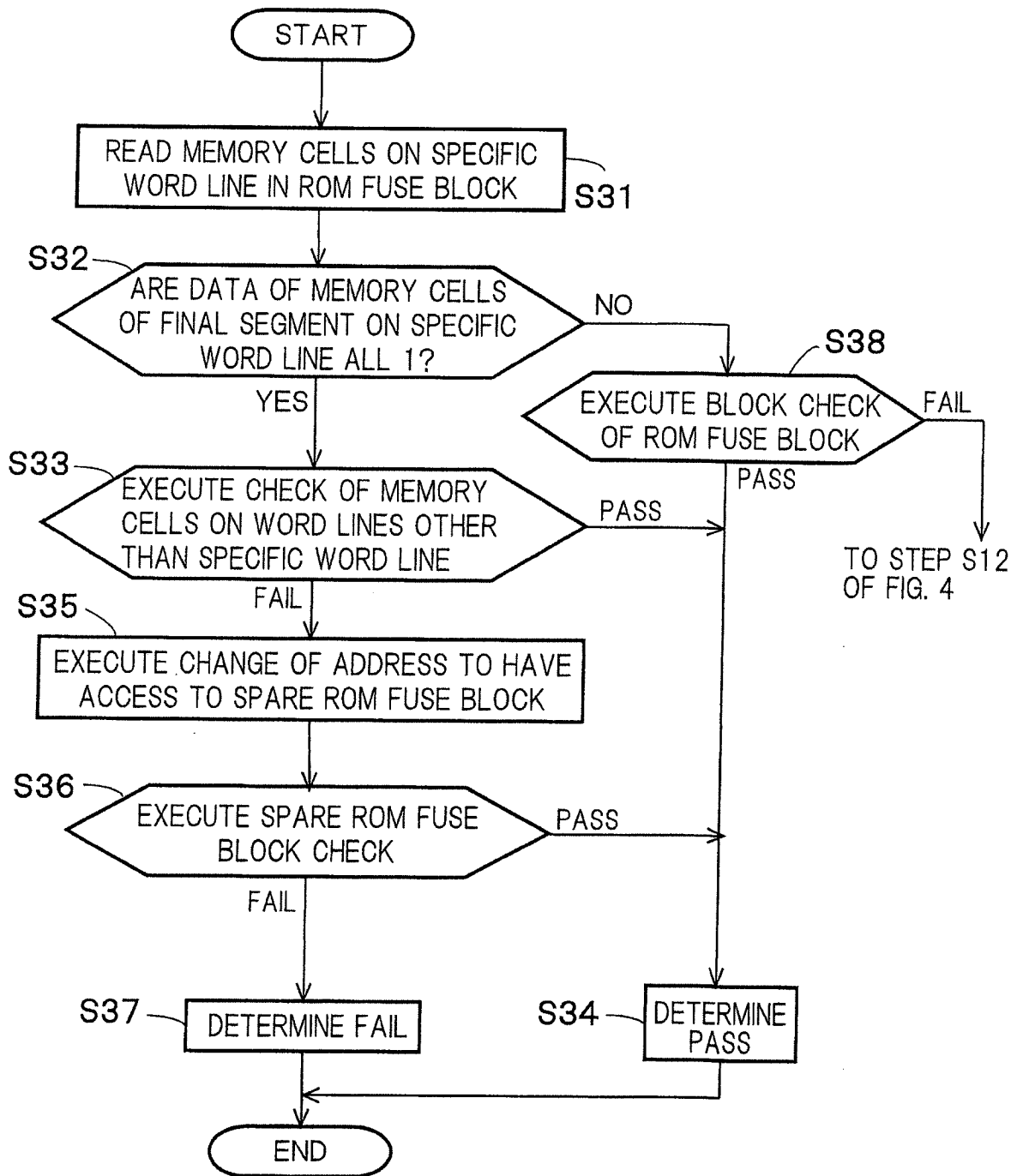


FIG. 6

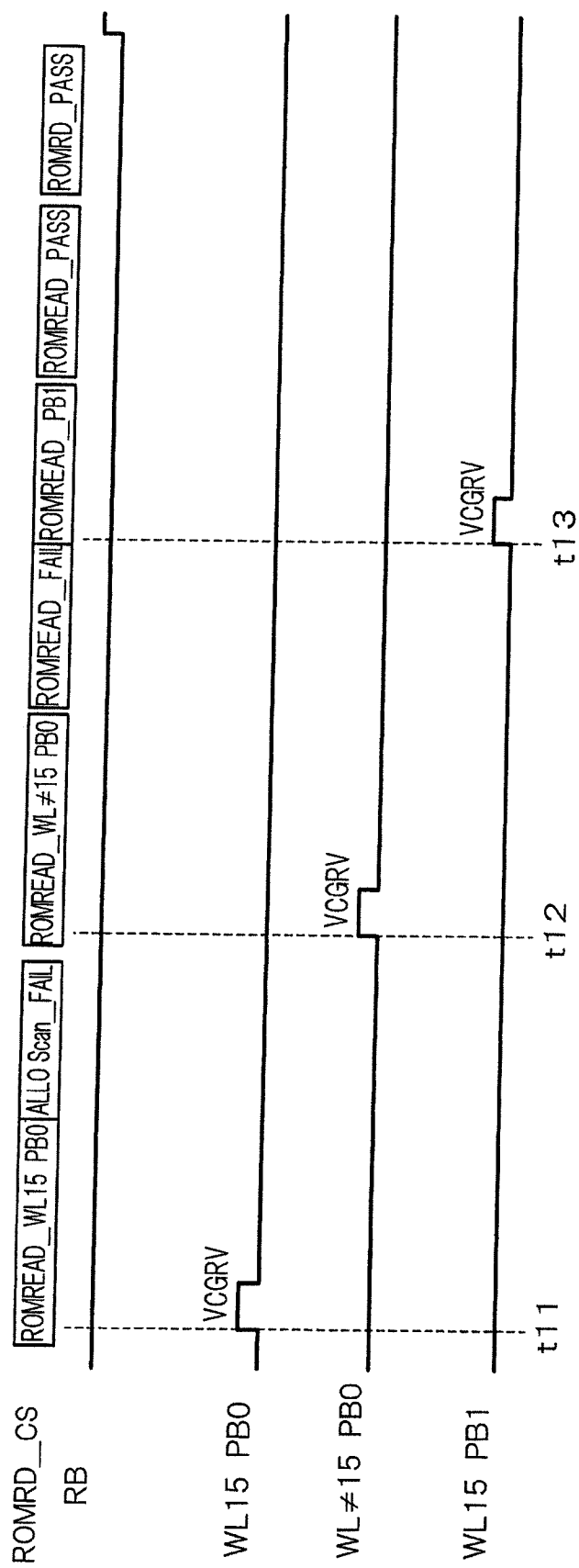


FIG. 7

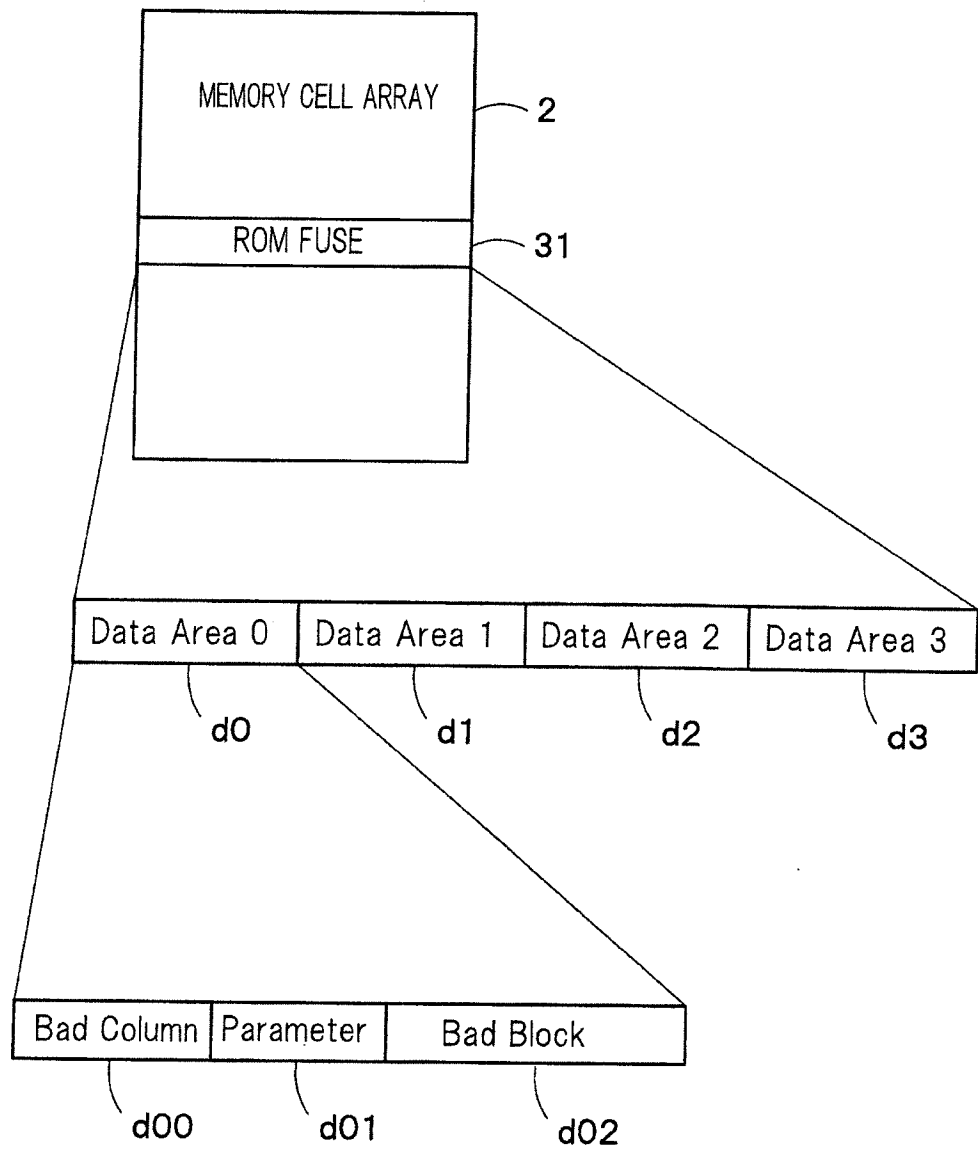


FIG. 8

FIG. 9A

IO	IO 7	IO 6	IO 5	IO 4	IO 3	IO 2	IO 1	IO 0
Data Latch 0	Block Add X[7]	Block Add X[6]	Block Add X[5]	Block Add X[4]	Block Add X[3]	Block Add X[2]	Block Add X[1]	Block Add X[0]
Data Latch 1	/Block Add X[7]	/Block Add X[6]	/Block Add X[5]	/Block Add X[4]	/Block Add X[3]	/Block Add X[2]	/Block Add X[1]	/Block Add X[0]
Data Latch 2	Block IO [7]	Block IO [6]	Block IO [5]	Block IO [4]	Block IO [3]	Block IO [2]	Block IO [1]	Block IO [0]
Data Latch 3	/Block IO [7]	/Block IO [6]	/Block IO [5]	/Block IO [4]	/Block IO [3]	/Block IO [2]	/Block IO [1]	/Block IO [0]

Block Add X = QUOTIENT OBTAINED BY DIVIDING BLOCK ADDRESS BY 8
Block IO = ALLOCATE 8 BLOCKS OF BLOCK X TO INDIVIDUAL BITS

FIG. 9B

	Block Add X	Block IO
Block 0	b'0000_0000	b'0000_0001
Block 1	b'0000_0000	b'0000_0010
Block 2	b'0000_0000	b'0000_0100
Block 3	b'0000_0000	b'0000_1000
Block 4	b'0000_0000	b'0001_0000
Block 5	b'0000_0000	b'0010_0000
Block 6	b'0000_0000	b'0100_0000
Block 7	b'0000_0000	b'1000_0000
Block 8	b'0000_0001	b'0000_0001
Block 9	b'0000_0001	b'0000_0010
Block 10	b'0000_0001	b'0000_0100
Block 11	b'0000_0001	b'0000_1000
Block 12	b'0000_0001	b'0001_0000
Block 13	b'0000_0001	b'0010_0000
Block 14	b'0000_0001	b'0100_0000
Block 15	b'0000_0001	b'1000_0000

Block 16	b'0000_0010	b'0000_0001
Block 17	b'0000_0010	b'0000_0010
Block 18	b'0000_0010	b'0000_0100
Block 19	b'0000_0010	b'0000_1000
⋮	⋮	⋮
Block 24	b'0000_0011	b'0000_0001
Block 25	b'0000_0011	b'0000_0010
Block 26	b'0000_0011	b'0000_0100
Block 27	b'0000_0011	b'0000_1000
⋮	⋮	⋮

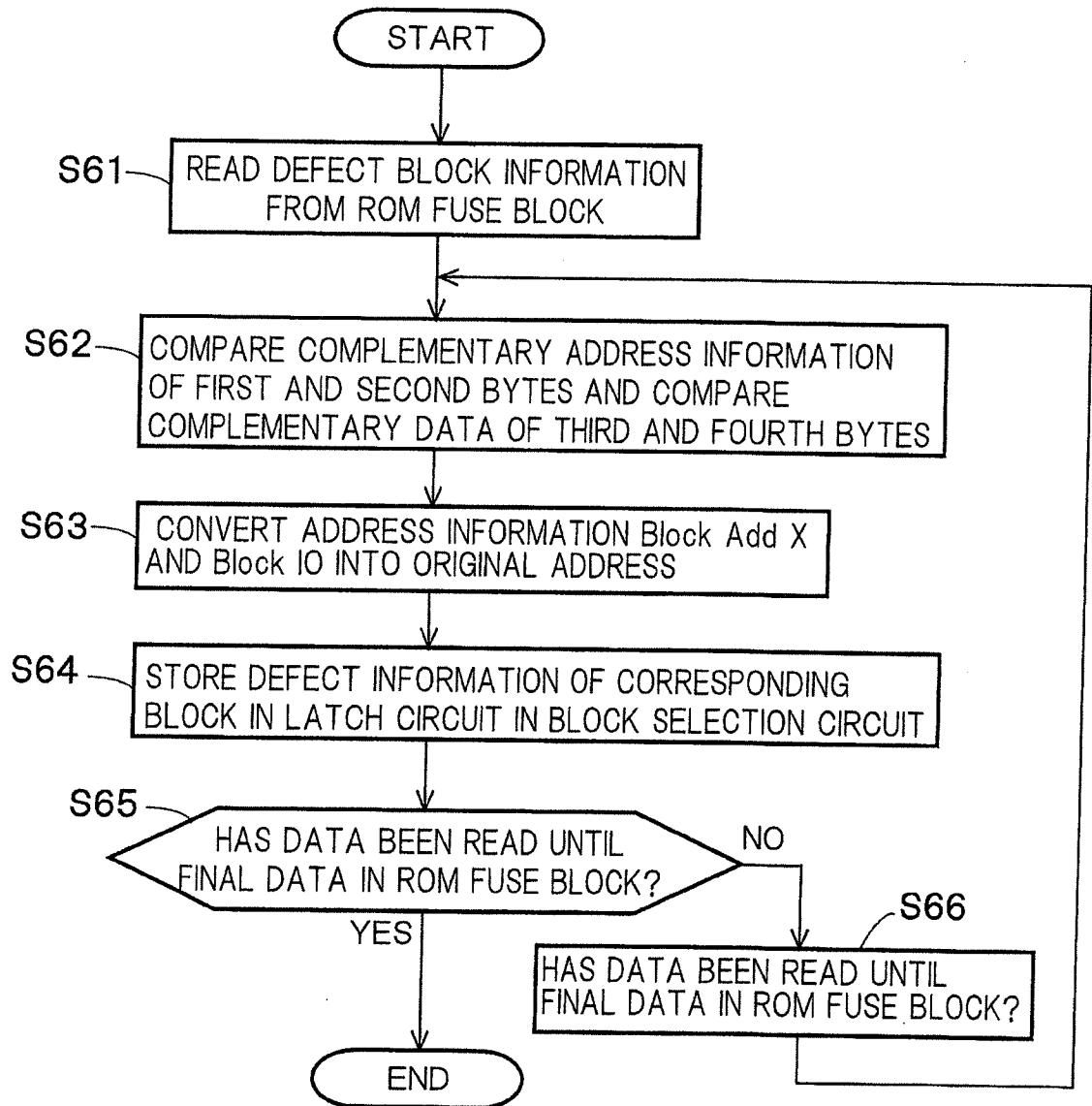


FIG.10

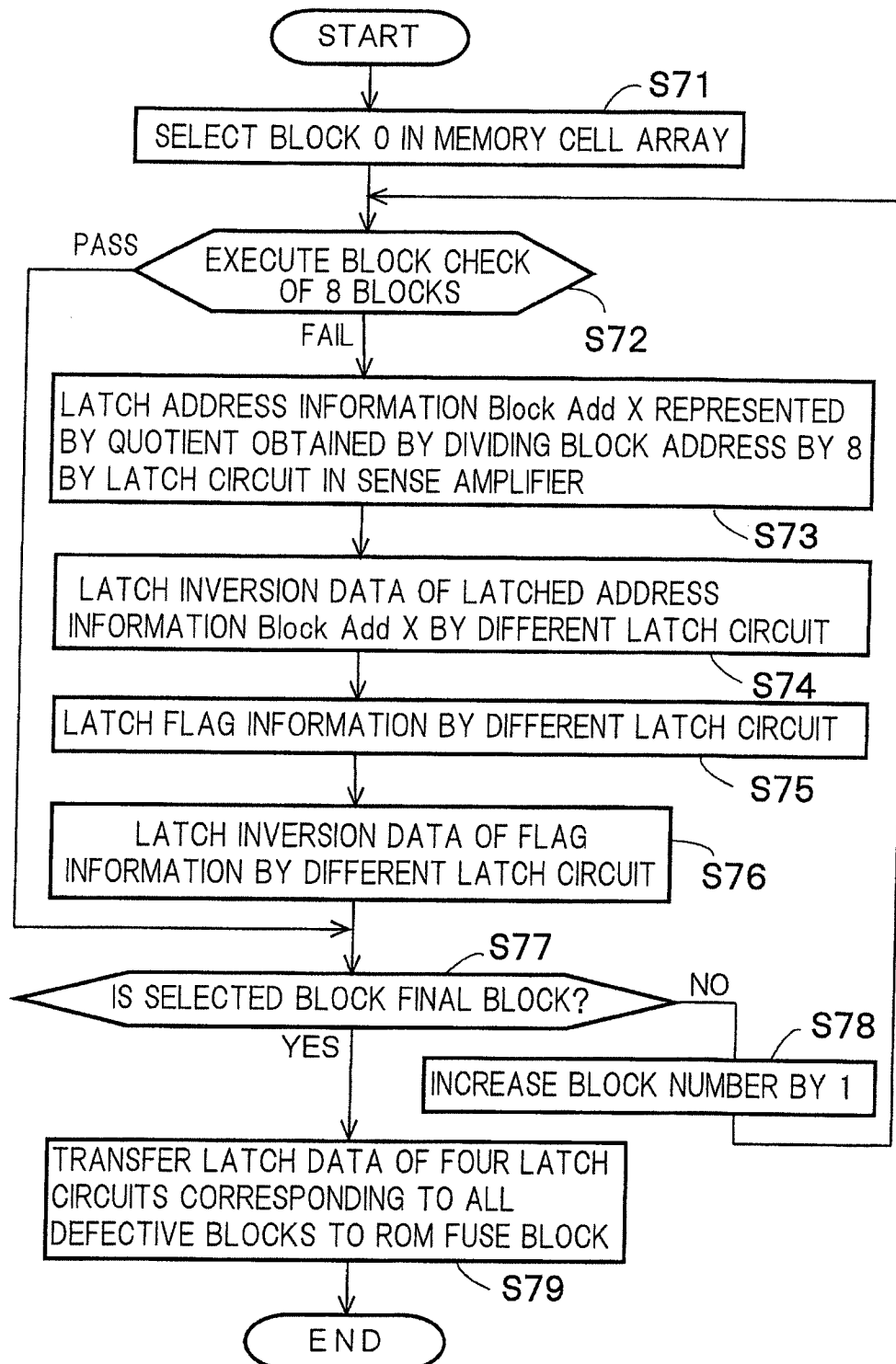


FIG. 11