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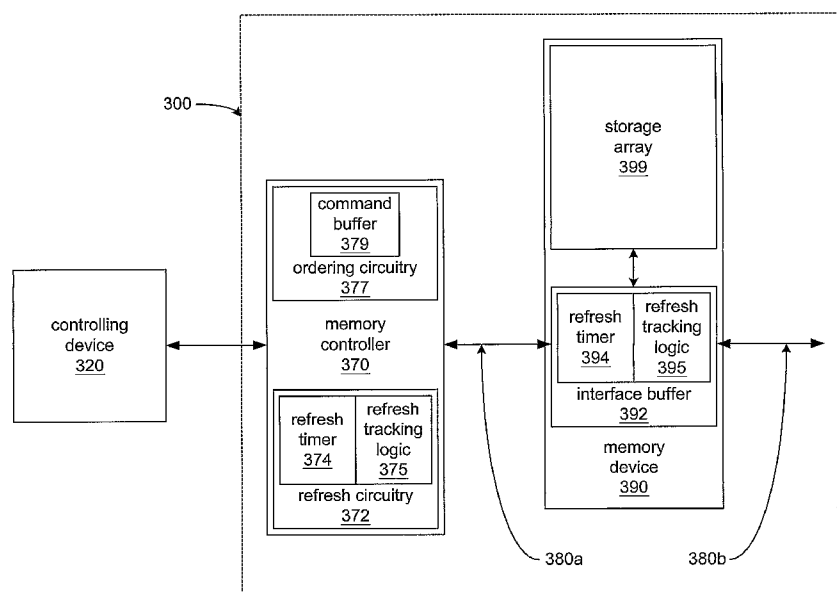
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[Continued on next page]

(54) Title: MEMORY BUFFER DEVICE INTEGRATING REFRESH



(57) Abstract: Apparatus and method to carry out refresh operations on rows of memory cells within a memory device independently of a memory controller during times when there is no activity on a memory bus coupling the memory device to the memory controller that involves the memory device.

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