

ABSTRACT

INFORMATION PROCESSING DEVICE AND COMMUNICATION SWITCHING METHOD

A connection destination of a second PHY (32) through a second data bus (6) is switched to either a second MAC (12) or a third MAC (21), a connection of a control bus (7) is switched such that either a first MAC (11) or the third MAC (21) accesses the second PHY (32) according to the connection destination of the second PHY (32) through the second data bus (6), and a connection of an interrupt bus (8) is switched according to the connection destination of the second PHY (32) through the second data bus (6) to notify a first CPU (10) that interrupt processing for the first CPU (10) is transmitted from both a first PHY (30) and the second PHY (32) or only from the first PHY (30).

Fig. 1