FIG. 1

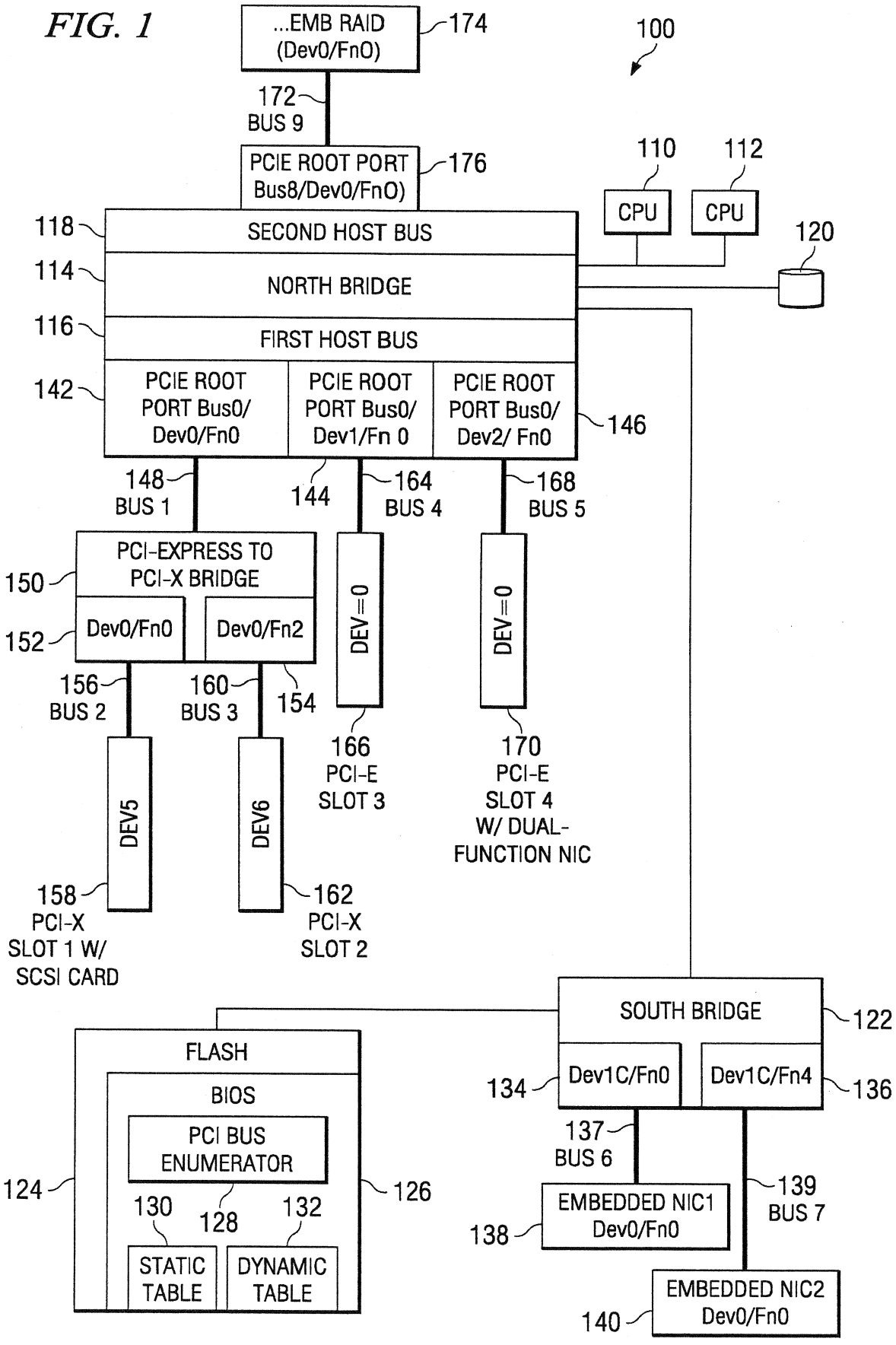

...EMB RAID
(Dev0/Fn0)
174
100
172
BUS 9
PCIE ROOT PORT
Bus8/Dev0/Fn0)
176
110
112
CPU
CPU
120
118
SECOND HOST BUS
114
NORTH BRIDGE
116
FIRST HOST BUS
142
PCIE ROOT PORT Bus0/ Dev0/Fn0
PCIE ROOT PORT Bus0/ Dev1/Fn 0
PCIE ROOT PORT Bus0/ Dev2/ Fn0
146
148
BUS 1
144
164
BUS 4
168
BUS 5
PCI-EXPRESS TO PCI-X BRIDGE
150
Dev0/Fn0
Dev0/Fn2
152
DEV=0
DEV=0
156
BUS 2
160
BUS 3
154
166
PCI-E SLOT 3
170
PCI-E SLOT 4 W/ DUAL-FUNCTION NIC
DEV5
DEV6
158
PCI-X SLOT 1 W/ SCSI CARD
162
PCI-X SLOT 2
SOUTH BRIDGE
122
FLASH
BIOS
PCI BUS ENUMERATOR
124
126
130
128
132
STATIC TABLE
DYNAMIC TABLE
134
Dev1C/Fn0
Dev1C/Fn4
136
137
BUS 6
139
BUS 7
EMBEDDED NIC1 Dev0/Fn0
138
EMBEDDED NIC2 Dev0/Fn0
140

FIG. 2A

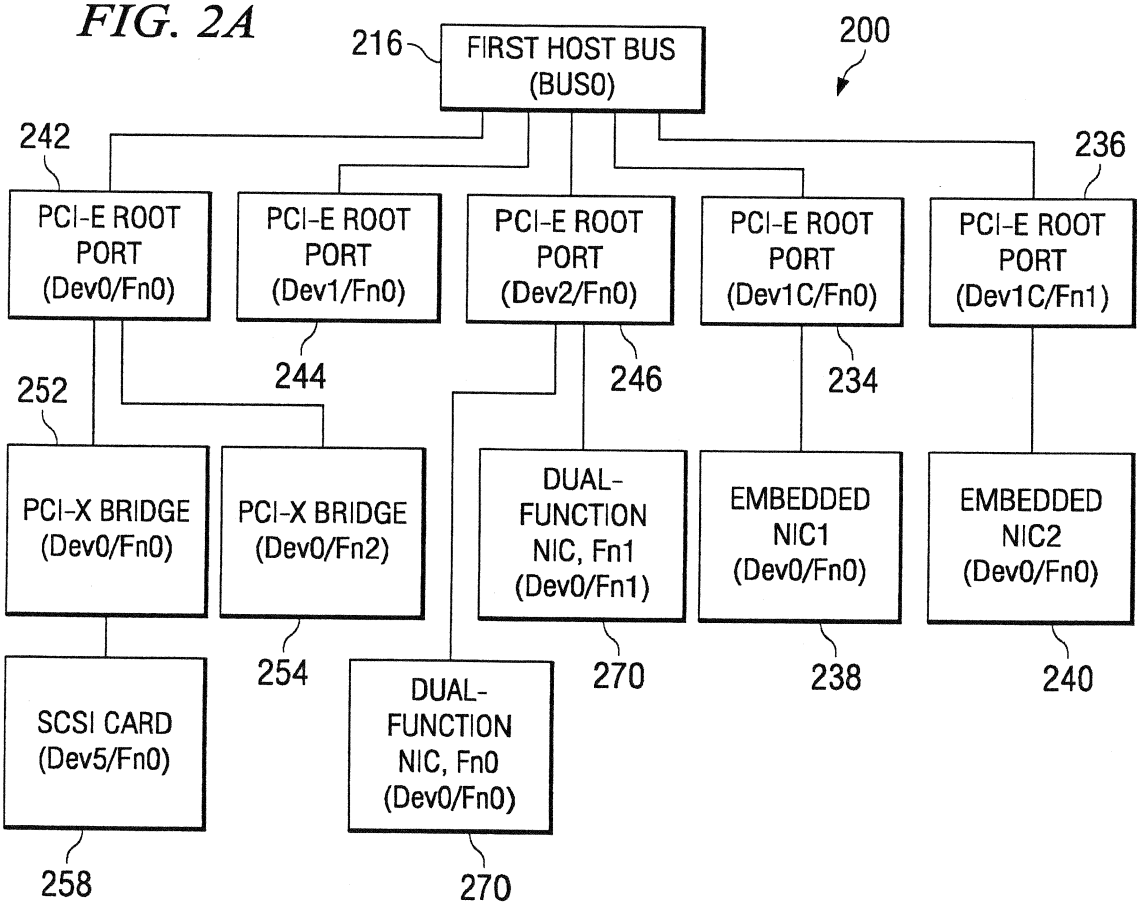

216
FIRST HOST BUS (BUS0)
200
242
236
PCI-E ROOT PORT (Dev0/Fn0)
PCI-E ROOT PORT (Dev1/Fn0)
PCI-E ROOT PORT (Dev2/Fn0)
PCI-E ROOT PORT (Dev1C/Fn0)
PCI-E ROOT PORT (Dev1C/Fn1)
244
246
234
252
PCI-X BRIDGE (Dev0/Fn0)
PCI-X BRIDGE (Dev0/Fn2)
DUAL-FUNCTION NIC, Fn1 (Dev0/Fn1)
EMBEDDED NIC1 (Dev0/Fn0)
EMBEDDED NIC2 (Dev0/Fn0)
254
270
238
240
SCSI CARD (Dev5/Fn0)
DUAL-FUNCTION NIC, Fn0 (Dev0/Fn0)
258
270

FIG. 2B

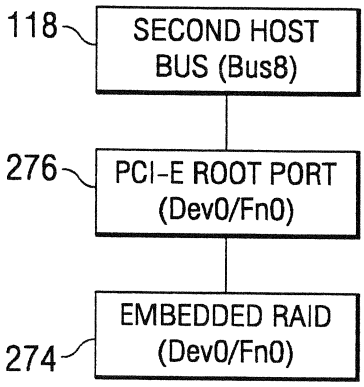

118
SECOND HOST BUS (Bus8)
276
PCI-E ROOT PORT (Dev0/Fn0)
274
EMBEDDED RAID (Dev0/Fn0)

FIG. 3

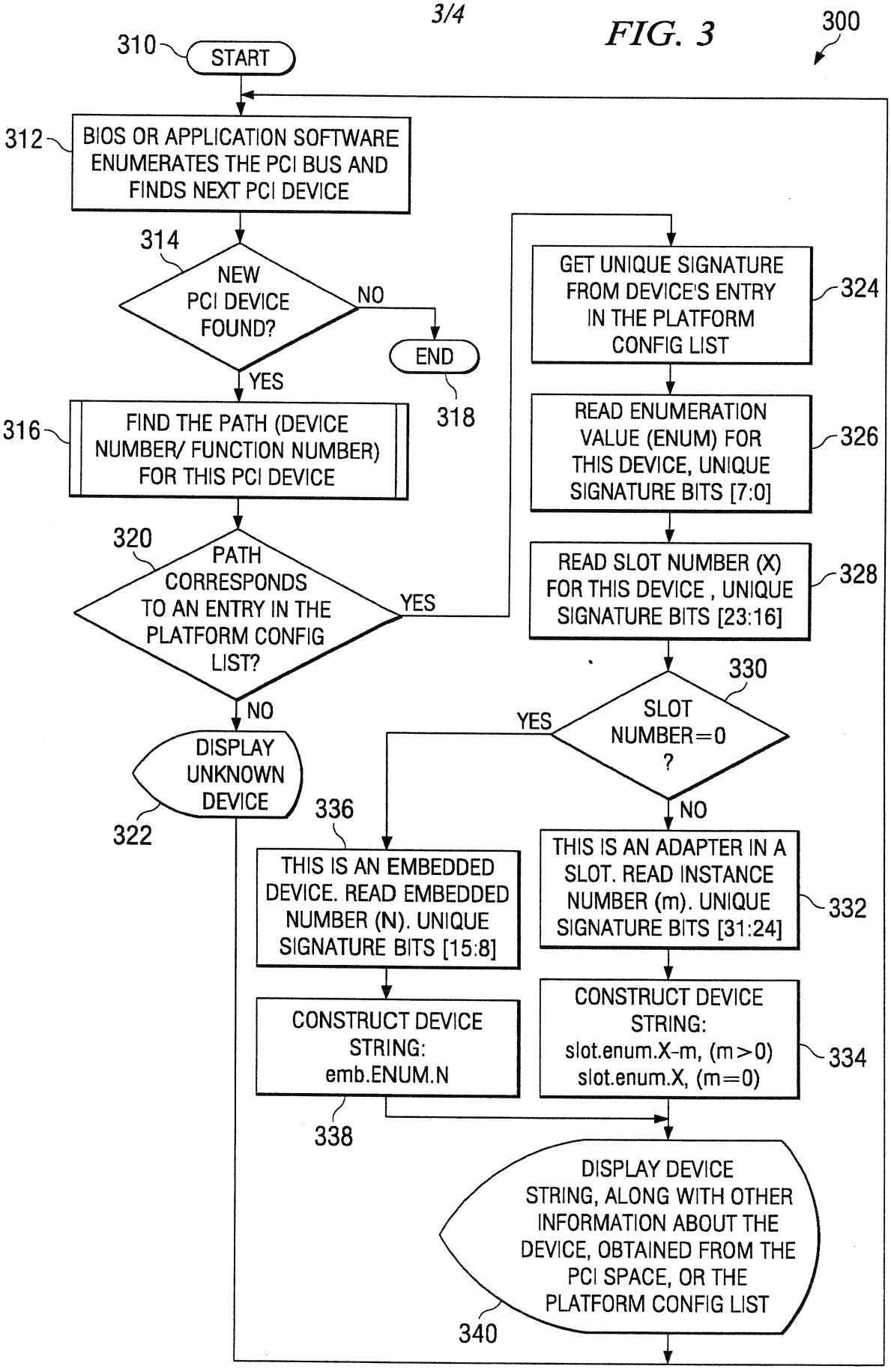

300
310
START
312
BIOS OR APPLICATION SOFTWARE ENUMERATES THE PCI BUS AND FINDS NEXT PCI DEVICE
314
NEW PCI DEVICE FOUND?
NO
END
318
YES
316
FIND THE PATH (DEVICE NUMBER/ FUNCTION NUMBER) FOR THIS PCI DEVICE
320
PATH CORRESPONDS TO AN ENTRY IN THE PLATFORM CONFIG LIST?
YES
NO
DISPLAY UNKNOWN DEVICE
322
GET UNIQUE SIGNATURE FROM DEVICE'S ENTRY IN THE PLATFORM CONFIG LIST
324
READ ENUMERATION VALUE (ENUM) FOR THIS DEVICE, UNIQUE SIGNATURE BITS [7:0]
326
READ SLOT NUMBER (X) FOR THIS DEVICE , UNIQUE SIGNATURE BITS [23:16]
328
330
SLOT NUMBER=0 ?
YES
NO
336
THIS IS AN EMBEDDED DEVICE. READ EMBEDDED NUMBER (N). UNIQUE SIGNATURE BITS [15:8]
THIS IS AN ADAPTER IN A SLOT. READ INSTANCE NUMBER (m). UNIQUE SIGNATURE BITS [31:24]
332
CONSTRUCT DEVICE STRING: emb.ENUM.N
CONSTRUCT DEVICE STRING: slot.enum.X-m, (m>0) slot.enum.X, (m=0)
334
338
DISPLAY DEVICE STRING, ALONG WITH OTHER INFORMATION ABOUT THE DEVICE, OBTAINED FROM THE PCI SPACE, OR THE PLATFORM CONFIG LIST
340

FIG. 4

410 DEVICE	412 Bus:Dev:Fun	414 PATH	416 32-BIT UNIQUE SIGNATURE	418 STRING DISPLAYED BY APPLICATION SOFTWARE
SCSI CARD	2:5:0	0-0/0-0/0-0/0-5/0	0x00010001	slot.scsi.1: Adaptec 7890 controller
DUAL-FUNCTION NIC, Fn0	5:0:0	0-0/0-2/0-0/0	0x01040002	slot.nic.4-1: Intel Pro 100 NIC
DUAL-FUNCTION NIC, Fn1	5:0:1	0-0/0-2/0-0/1	0x02040002	slot.nic.4-2: Intel Pro 100 NIC
EMBEDDED NIC1	6:0:0	0-0/0-1c/0-0/0	0x00010102	emb.nic.1: Broadcom NetExtreme Gigabit NIC
EMBEDDED NIC2	7:0:0	0-0/0-1c/1-0/0	0x00020102	emb.nic.2: Broadcom NetExtreme Gigabit NIC
EMBEDDED RAID	9:0:0	1-0/0-0/0	0x00000103	emb.raid.1: LSI PERC/5i controller

400

420

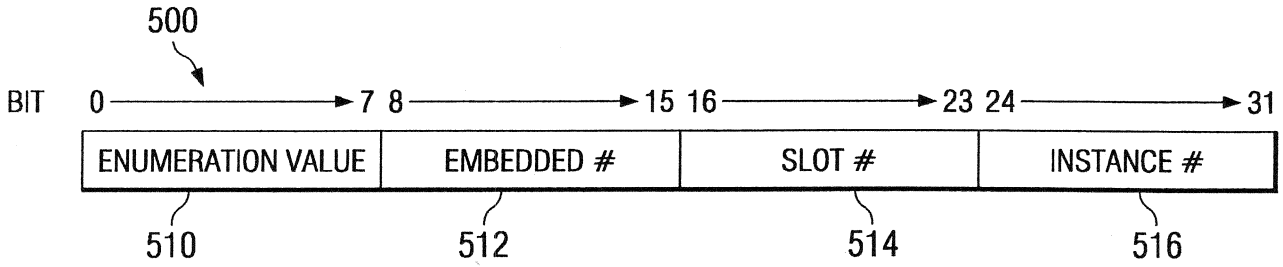


FIG. 5A

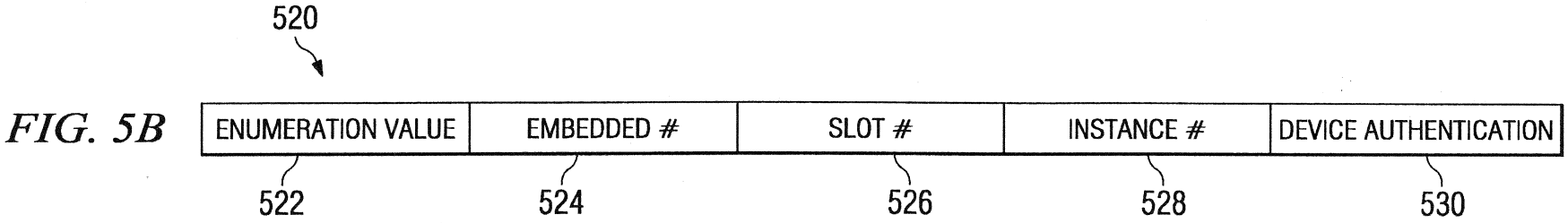


FIG. 5B