

HYBRID DATA PROCESSING SYSTEM INCLUDING PULSED-POWER-SUPPLY CMOS CIRCUITS

Abstract

- An advantageous hybrid data processing system includes both
- 5 conventional CMOS circuitry (10,12) and low-power-dissipation pulsed-power-supply (PPS) CMOS circuitry (14). To enable the different types of circuitry in the system to communicate with each other, data signal representations that are in a PPS format (from 14) must be converted (in 14 or 16) to corresponding conventional CMOS-formatted data signals before being processed by the CMOS circuitry (12).
- 10 Similarly, conventional CMOS data signals (from 10) must be converted (in 14) to corresponding PPS-formatted representations before the signals can be processed by the PPS circuitry (14).

Fig. 1