

ABSTRACT

Provided are an accelerator, an acceleration method, and an electronic device. The accelerator includes a calculating unit, the calculating unit includes a general operation logic composed of a basic operation logic, the general operation logic perform an operation of different operation types in response to different inputs. The accelerator further includes: an acquiring unit, configured to acquire a target microcode instruction generated according to a target type operation to be implemented; where the target microcode instruction is used to indicate target input data required to enable the general operation logic to implement the target type operation; a parsing unit, configured to parse the target microcode instruction to obtain a parsing result corresponding to the target input data; and a controlling unit, configured to, in response to the parsing result, control the calculating unit to enable the calculating unit to acquire the target input data and run the general operation logic based on the target input data so as to complete the target type operation. Therefore, the configuration difficulty for the accelerator is reduced, and the hardware resource is saved.

Fig. 1