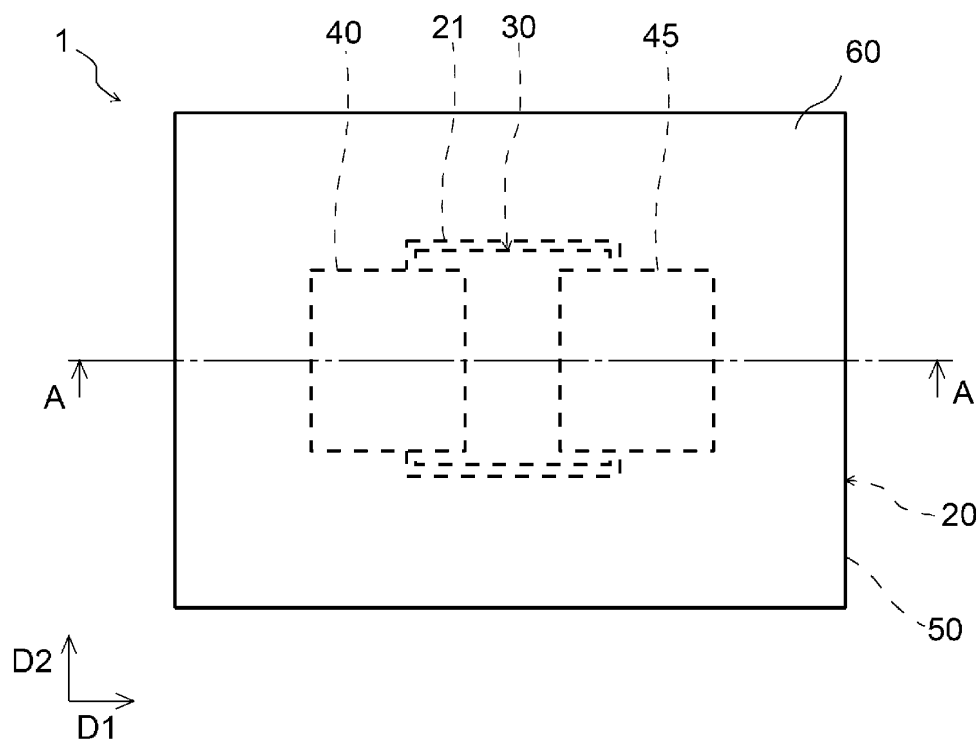
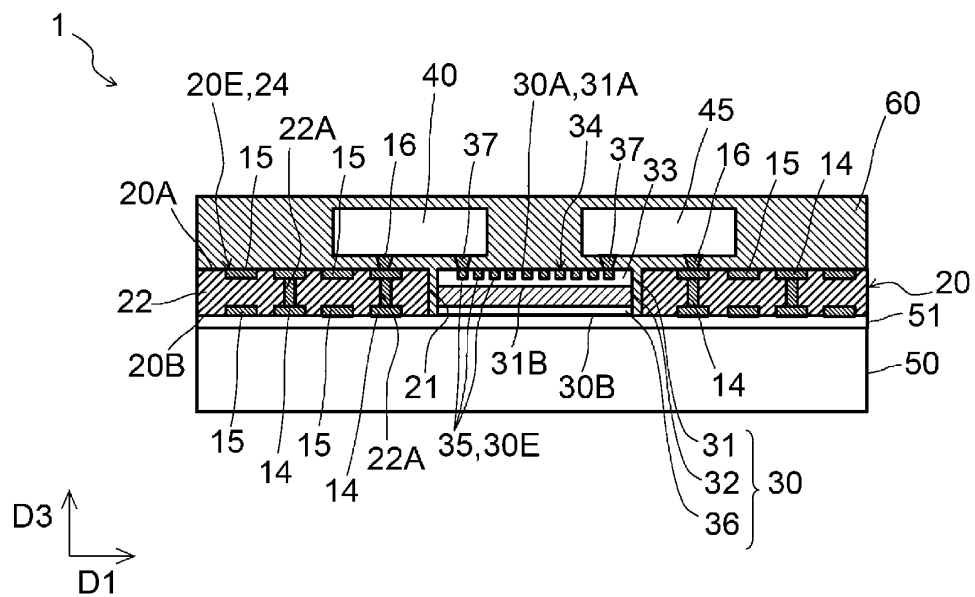


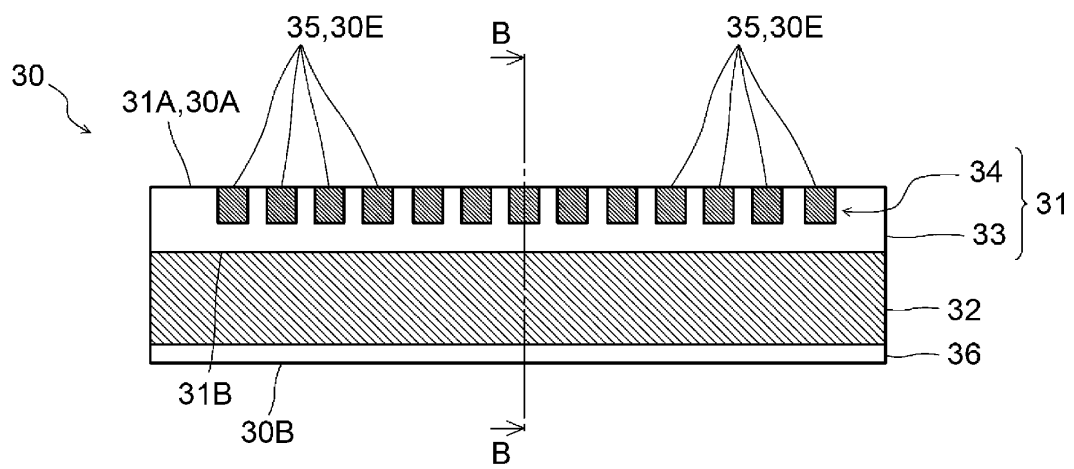
[図1]



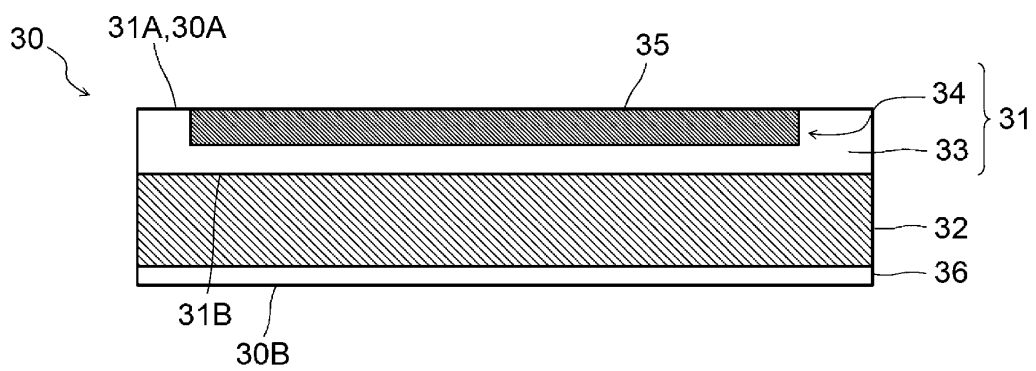
[図2]



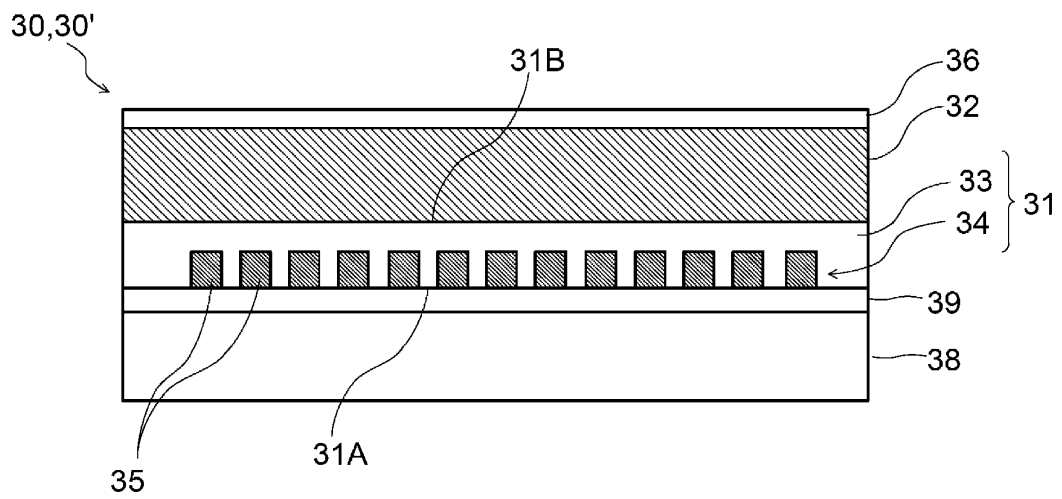
[図3]



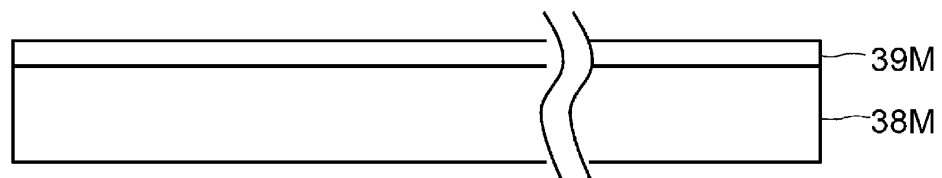
[図4]



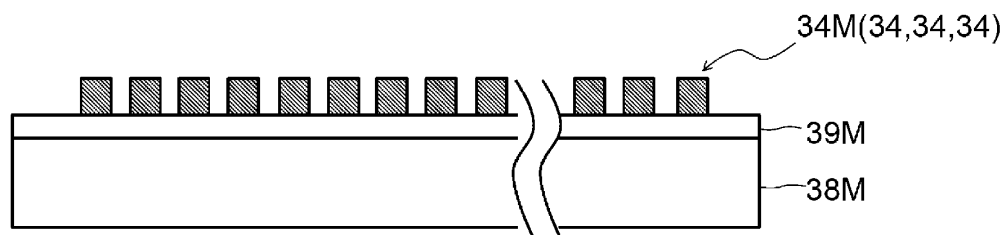
[図5]



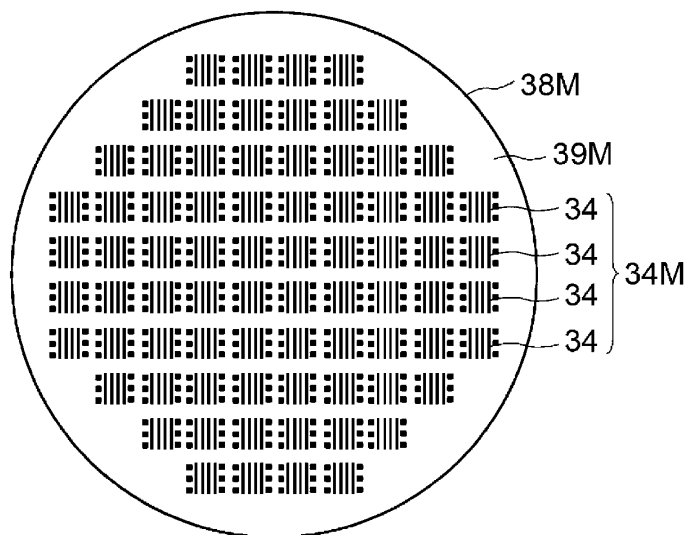
[図6]



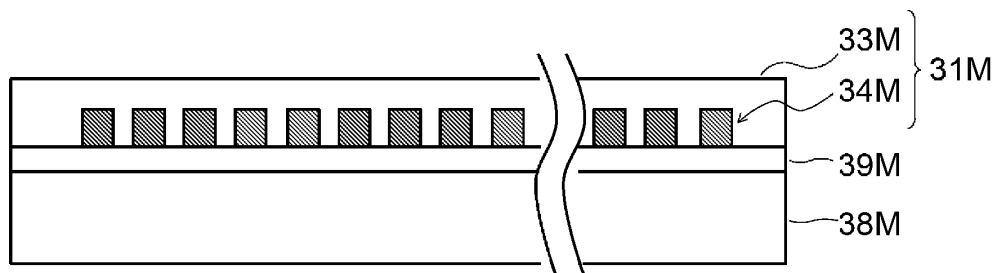
[図7A]



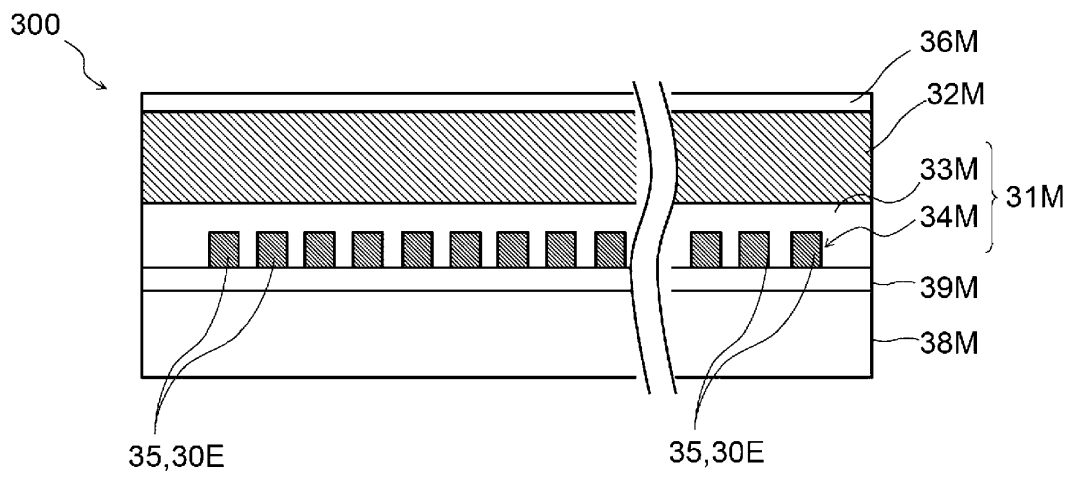
[図7B]



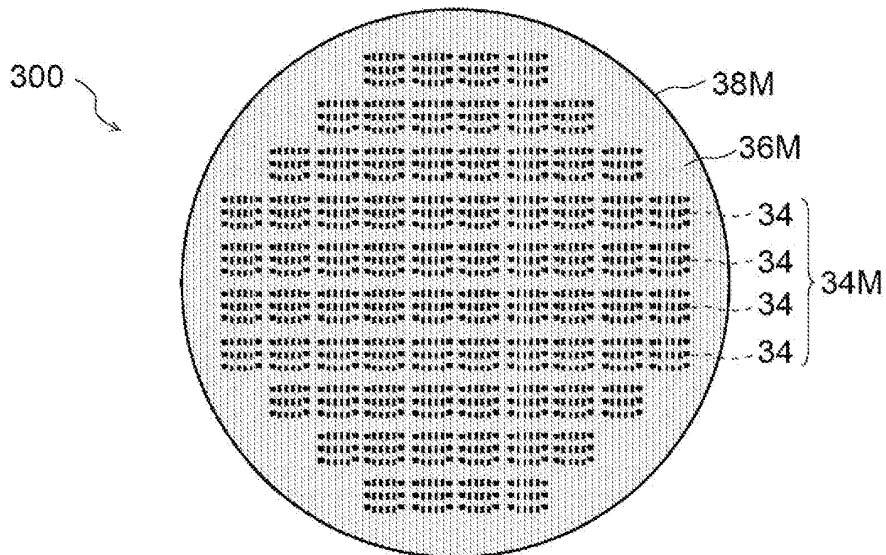
[図8]



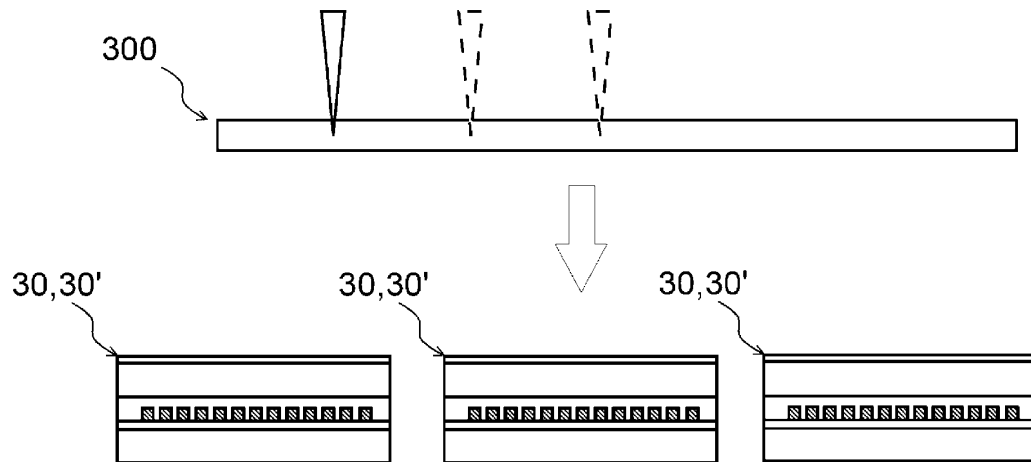
[図9A]



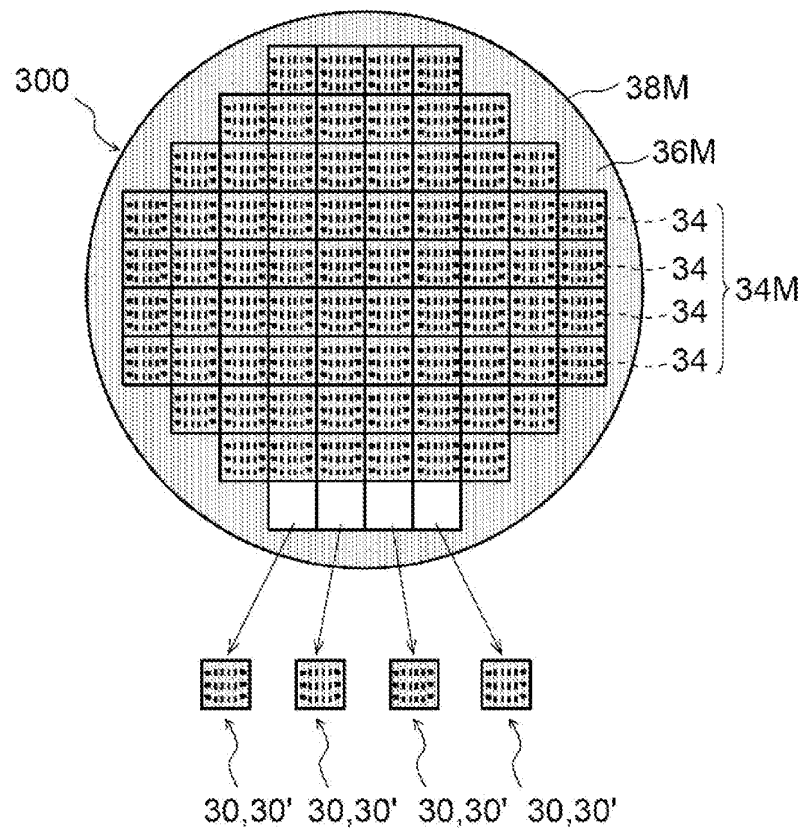
[図9B]



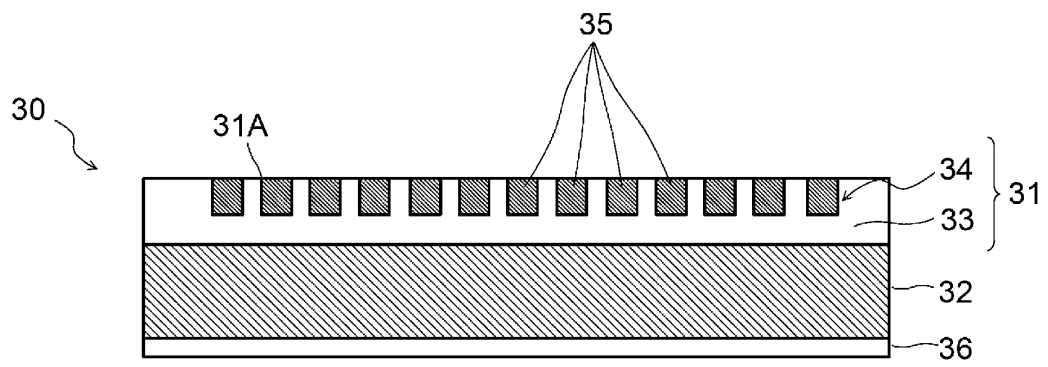
[図10A]



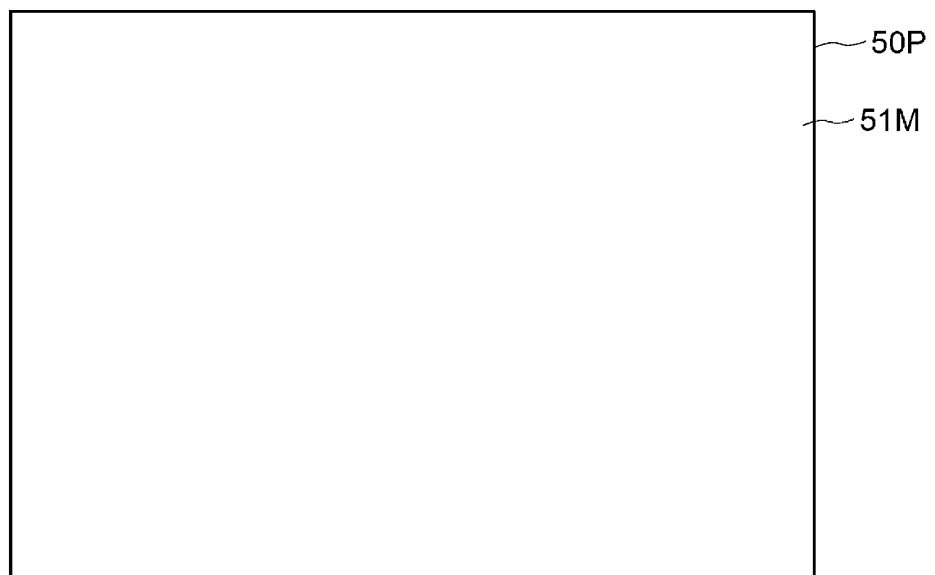
[図10B]



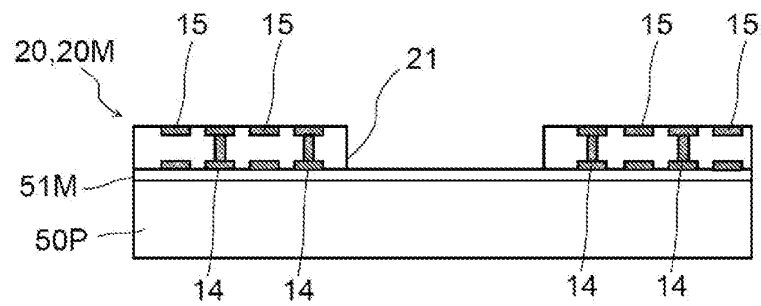
[図11]



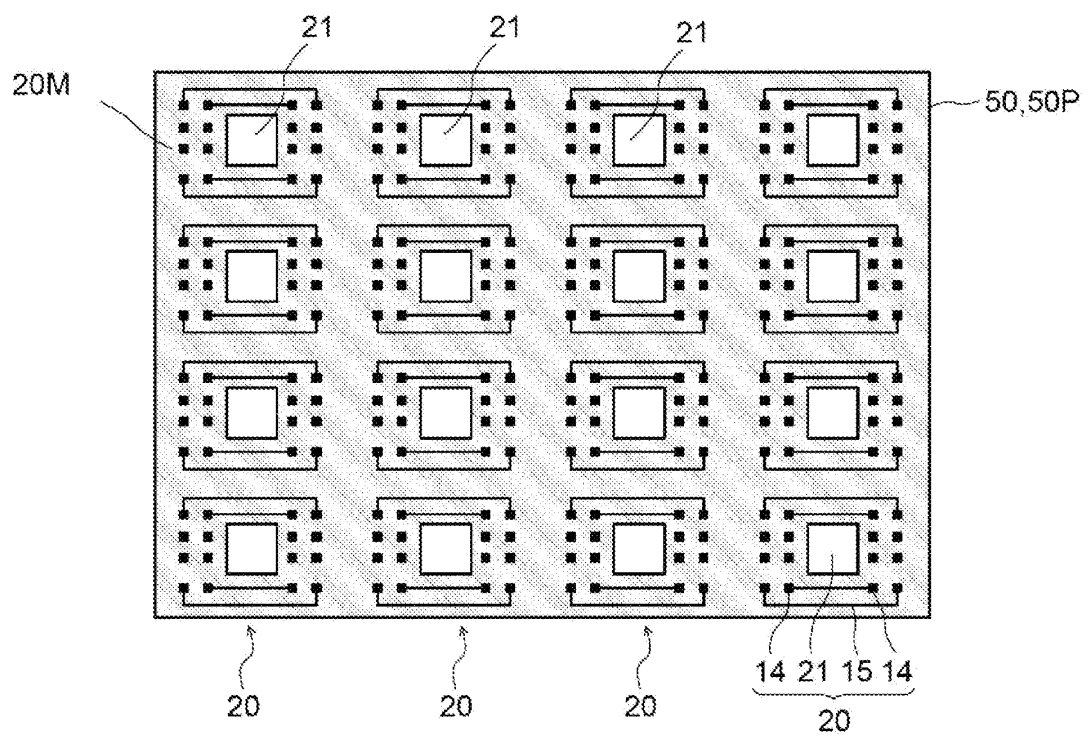
[図12A]



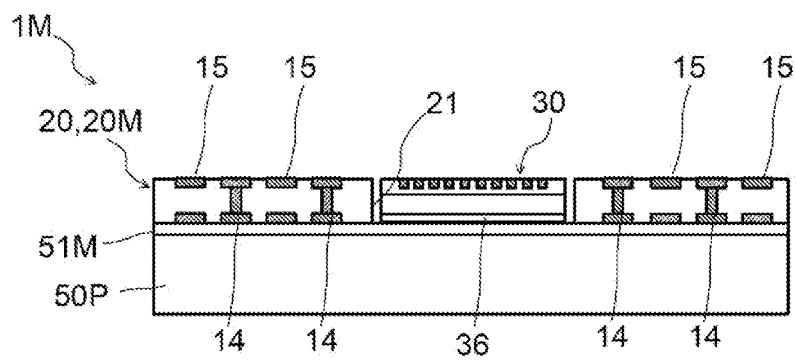
[図12B]



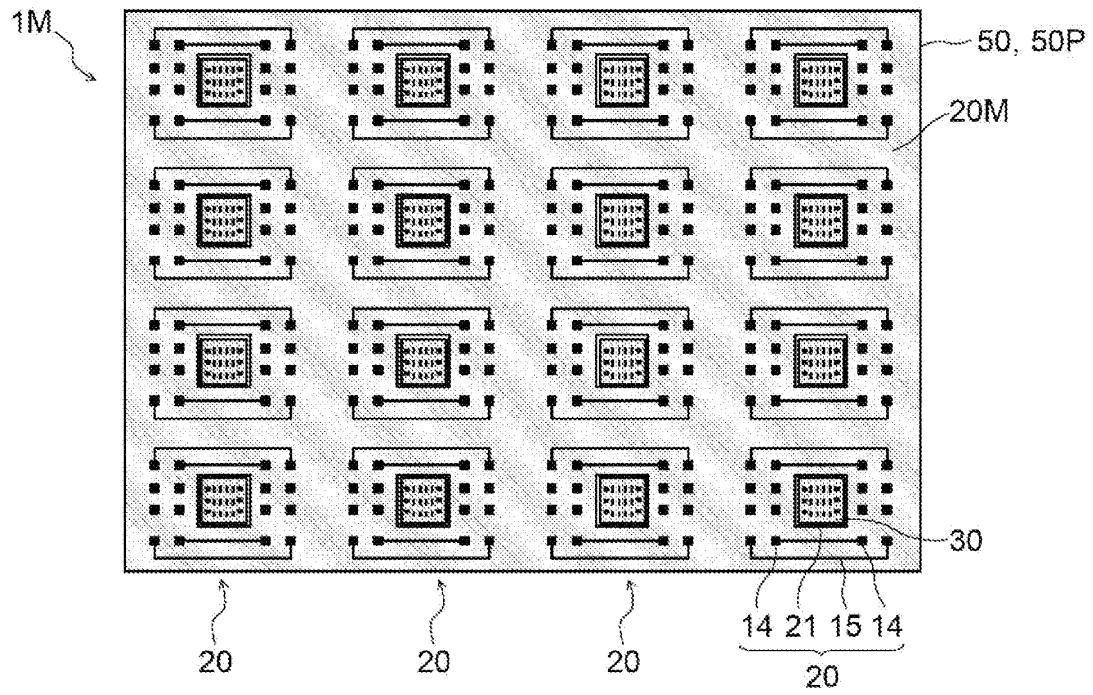
[図12C]



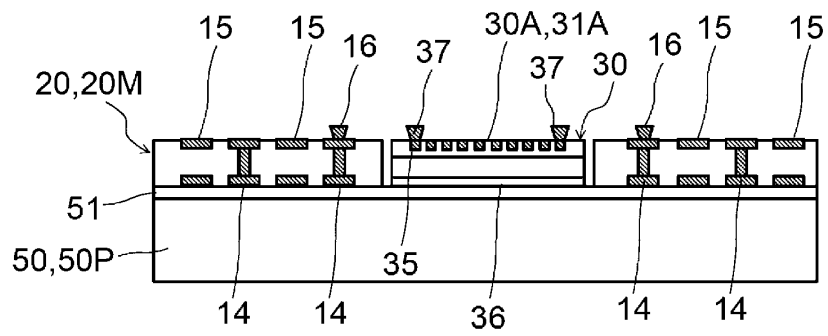
[図13A]



[図13B]

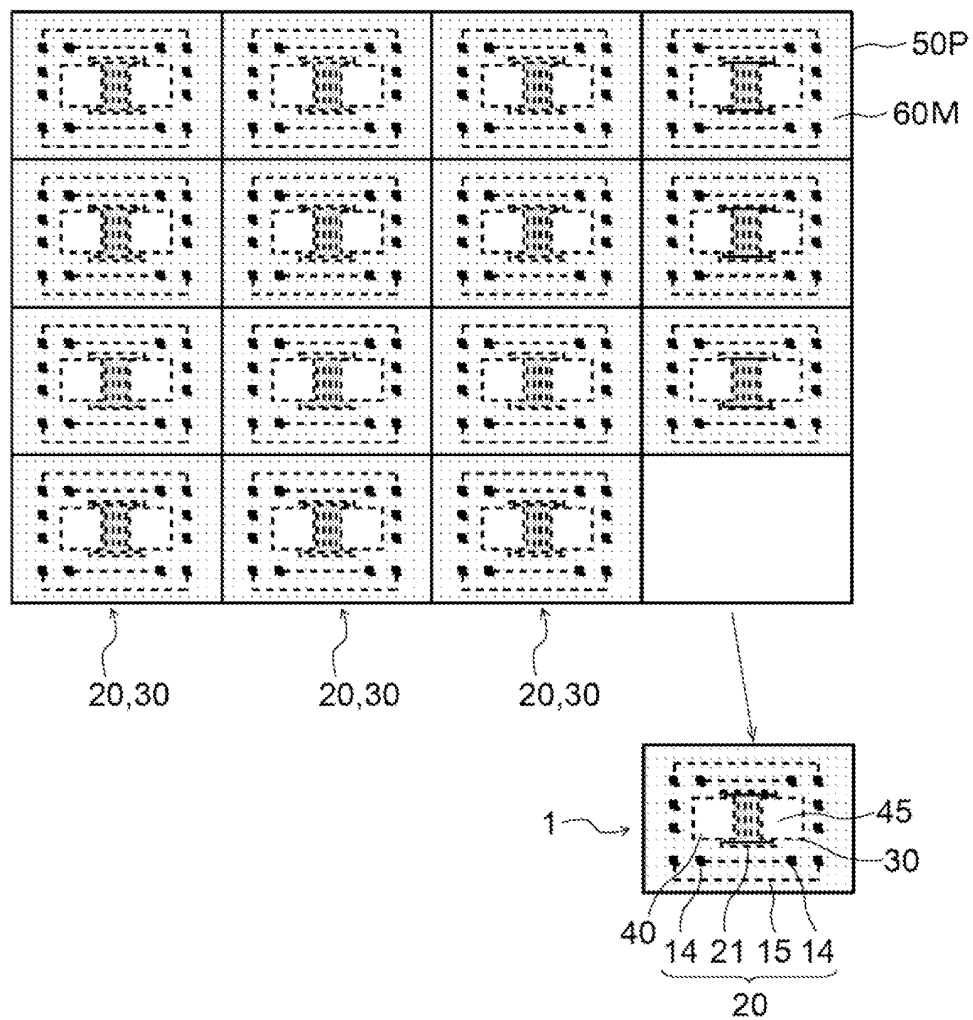


[図14]



This cross-sectional view shows a semiconductor device 100. It features a substrate 50 with a top layer 51. A series of gate electrodes 14 are formed on the top layer 51. Above the gate electrodes, there are two main regions: a first region 20 and a second region 20M. The first region 20 contains a gate stack 40 and a gate electrode 15. The second region 20M contains a gate stack 45 and a gate electrode 16. A channel layer 30 is located between the gate electrodes 14. A source/drain region 35 is formed in the substrate 50. A contact layer 36 is formed on the top layer 51. The device is also labeled with 50, 50P and 14.

[図16A]



[図16B]

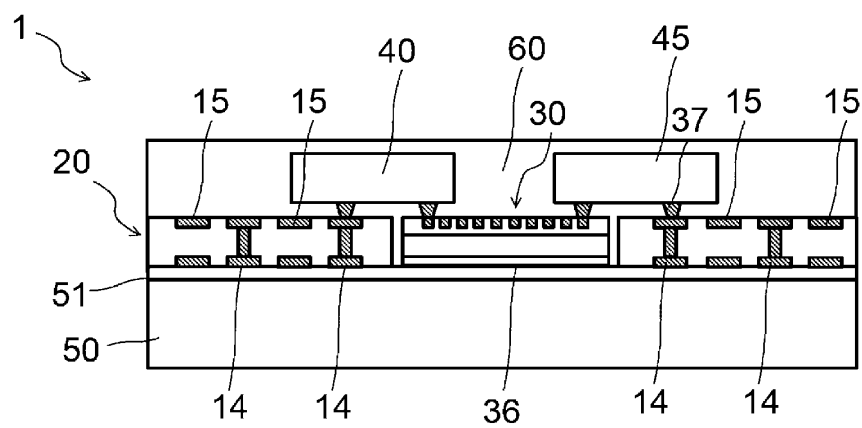
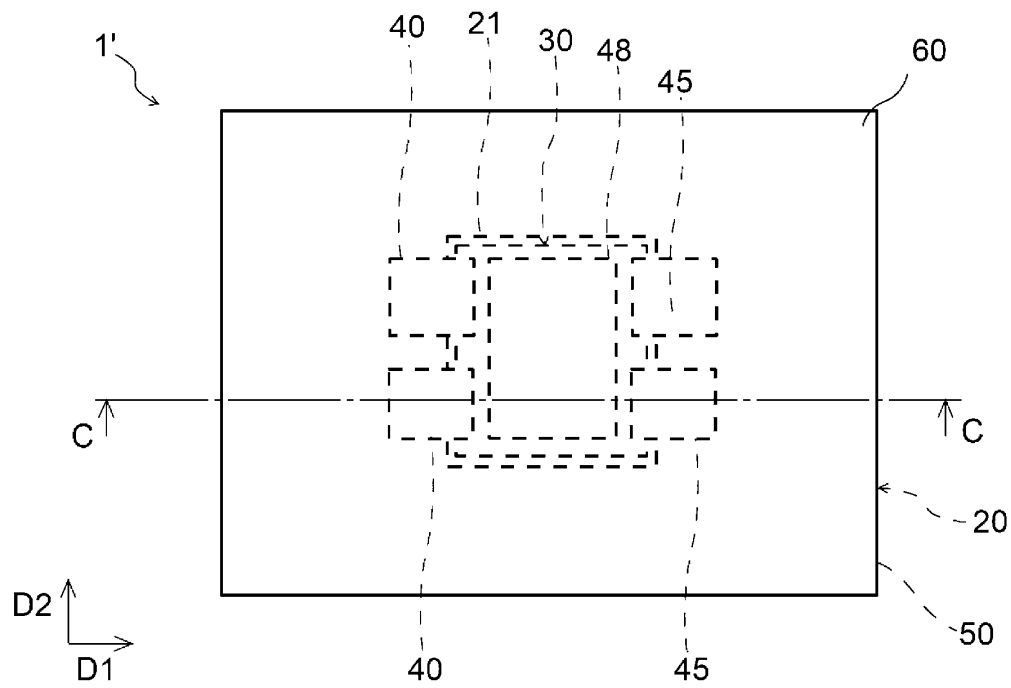
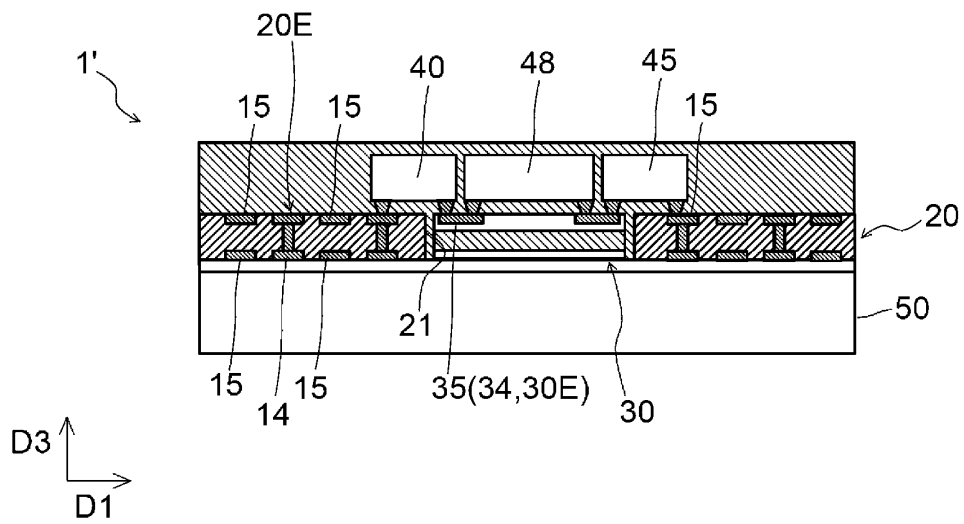


Fig. 1 is a cross-sectional view of a semiconductor device. The device includes a substrate 50 with a layer 51. A patterned layer 20 is formed on layer 51, containing openings 21. A layer 30 is formed on layer 20, with openings 40 and 45. A layer 60 is formed on layer 30.

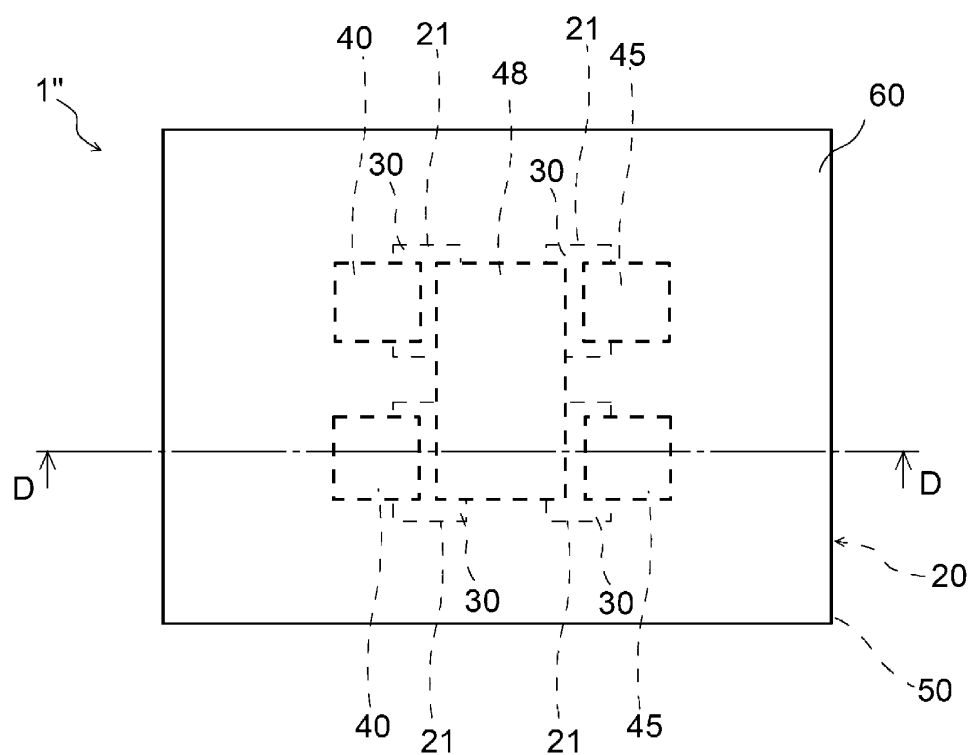
[図19]



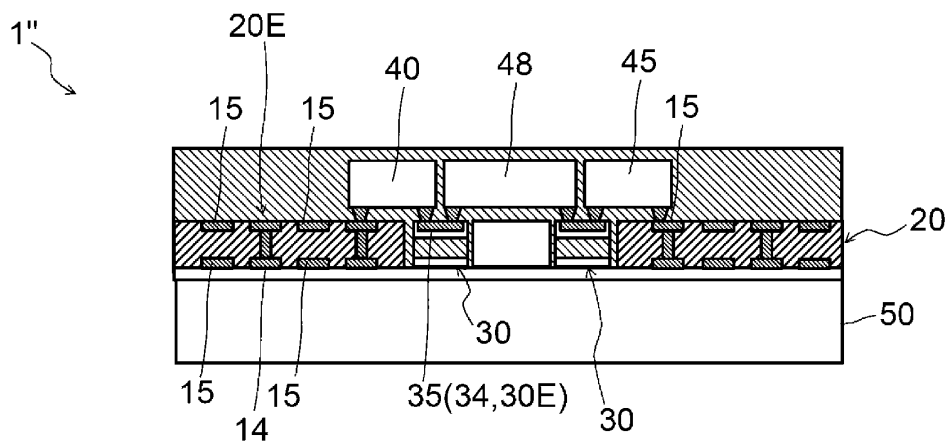
[図20]



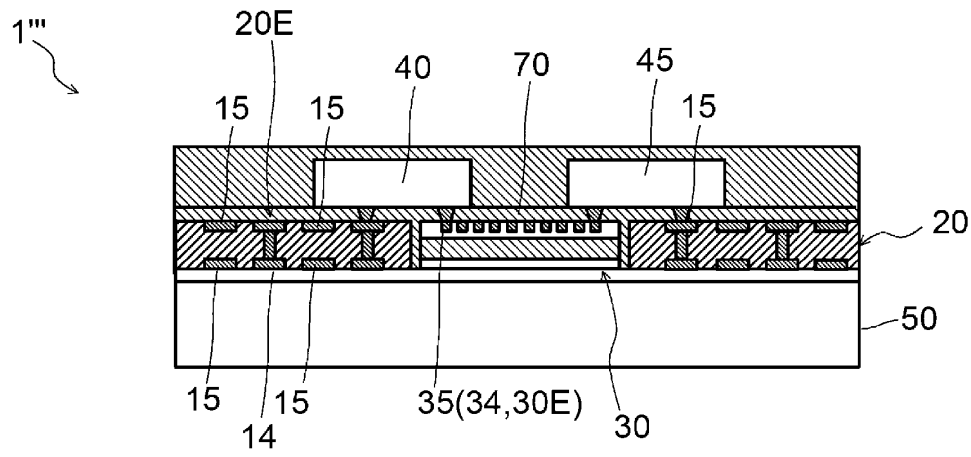
[図21]



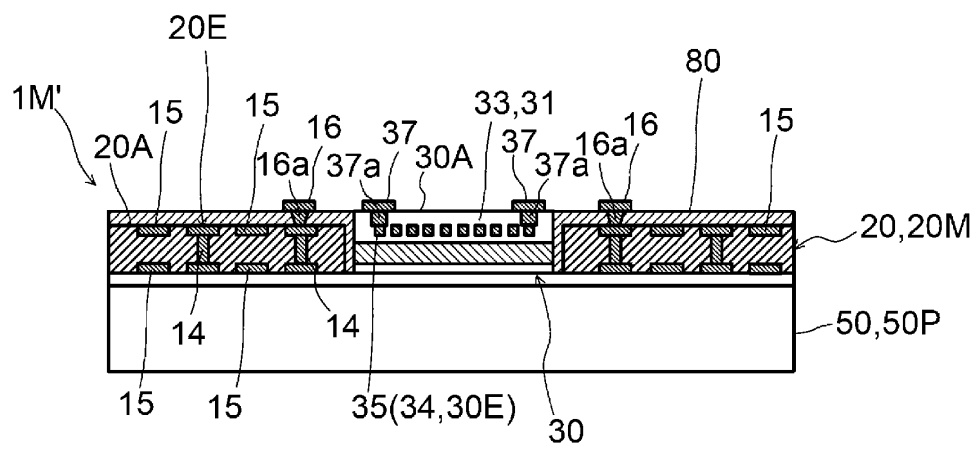
[図22]



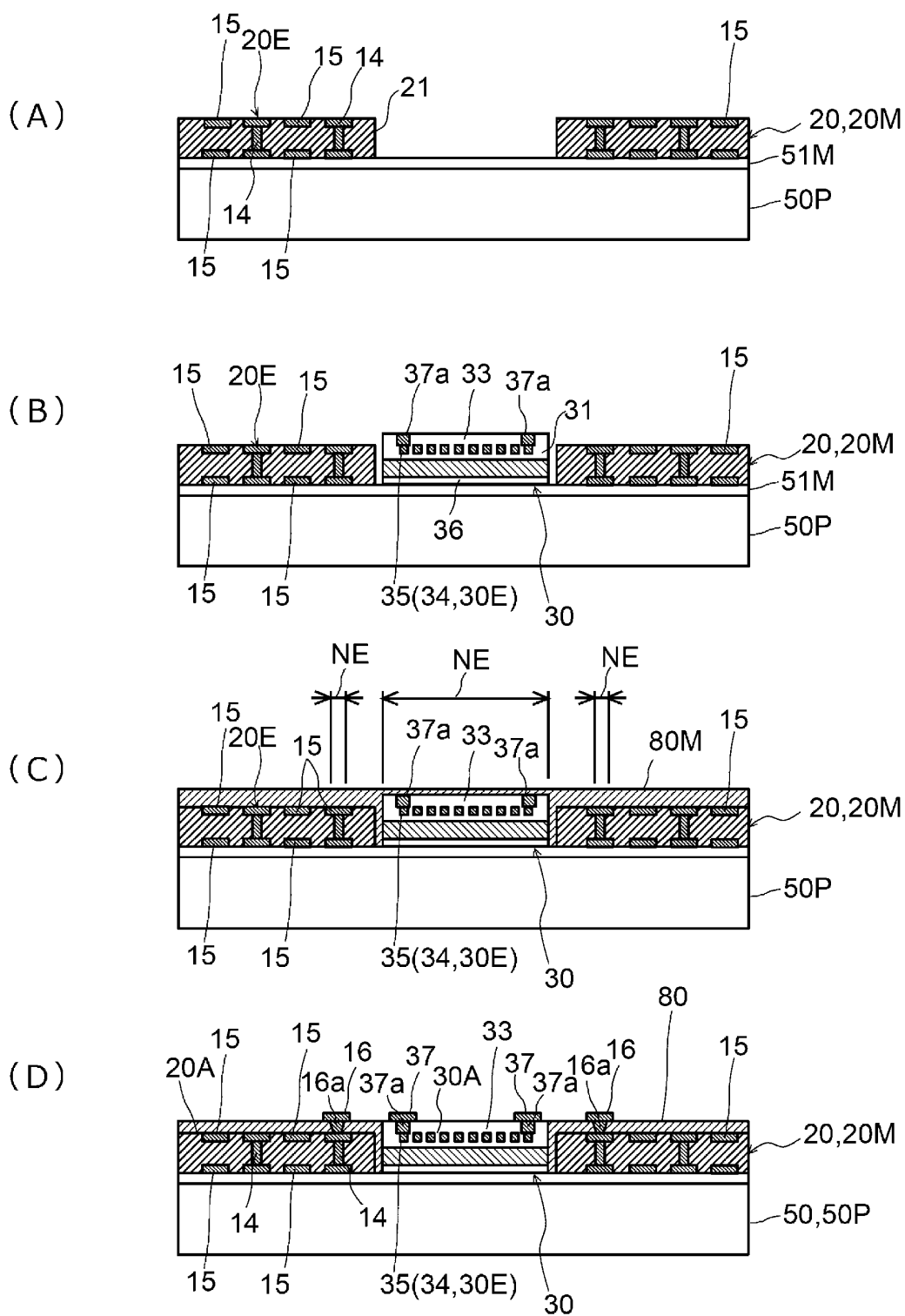
[図23]



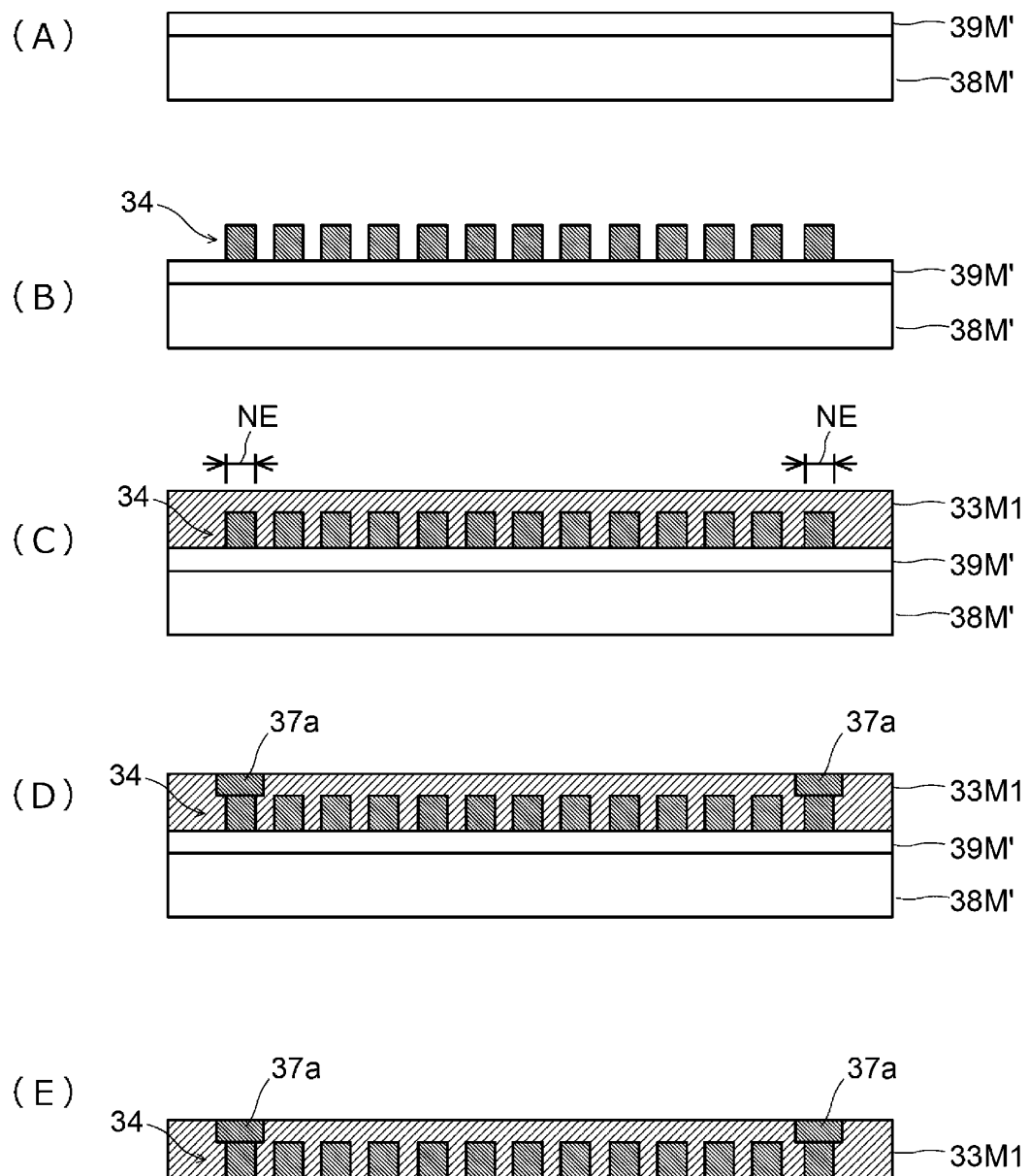
[図24]



[図25]

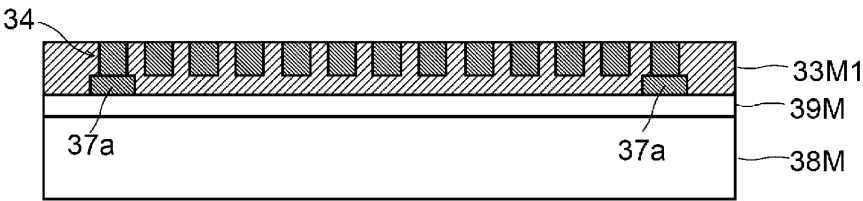


[図26A]

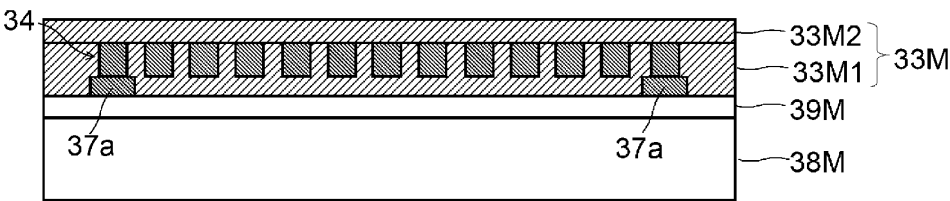


[図26B]

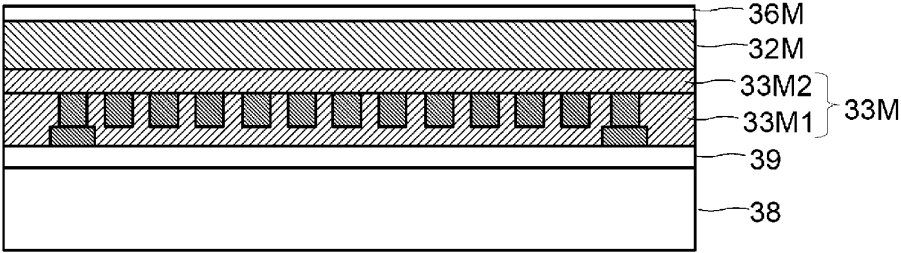
(F)



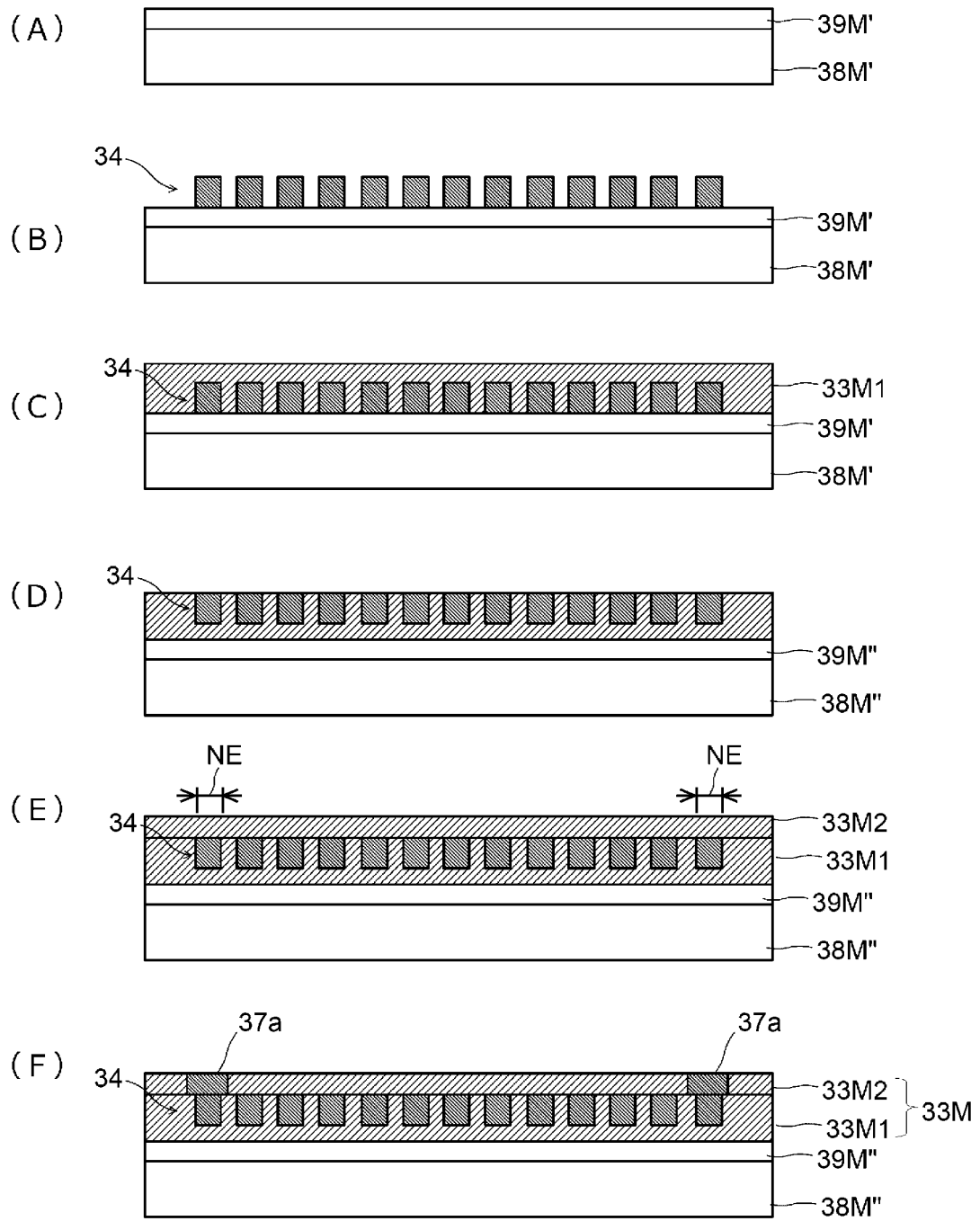
(G)



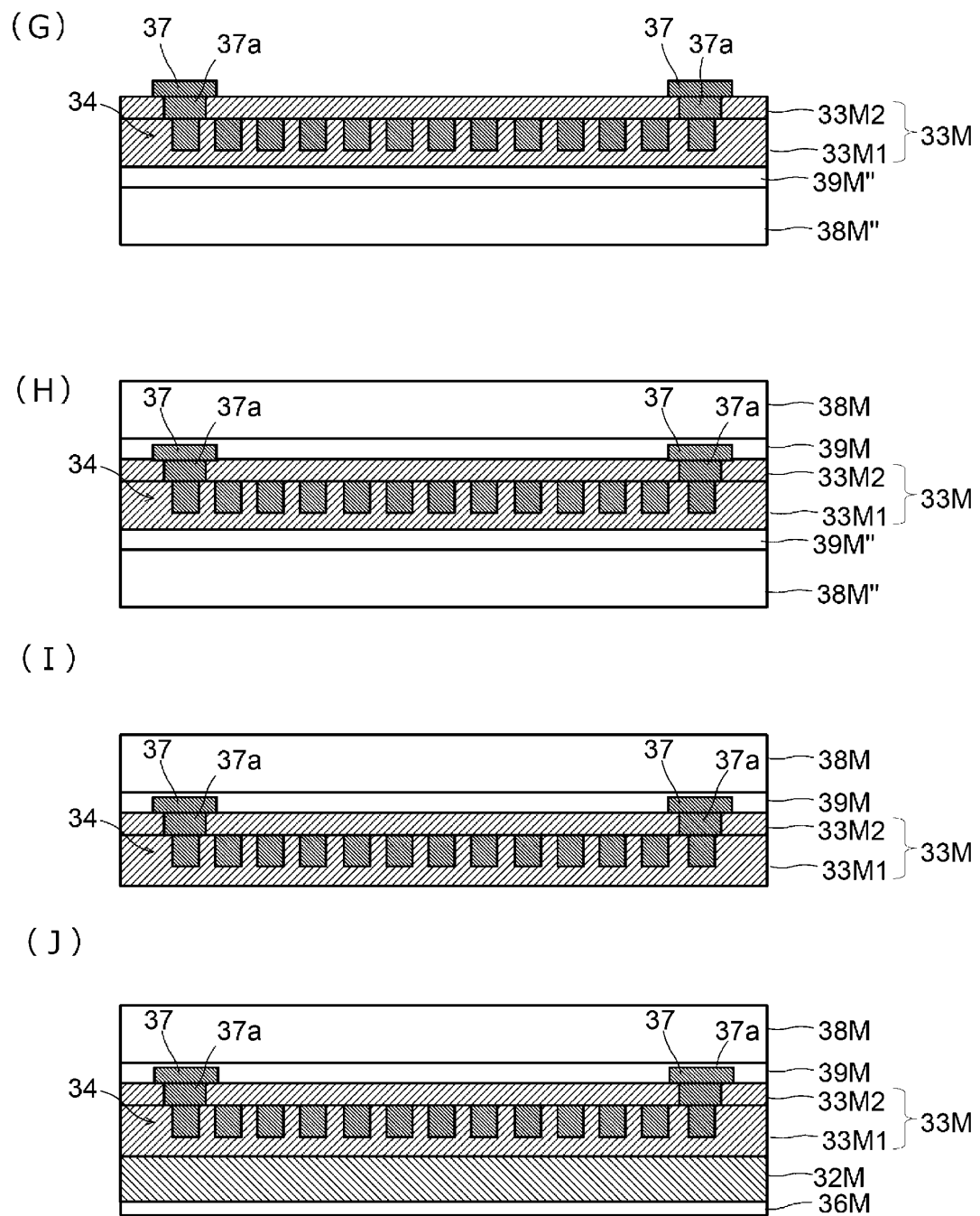
(H)



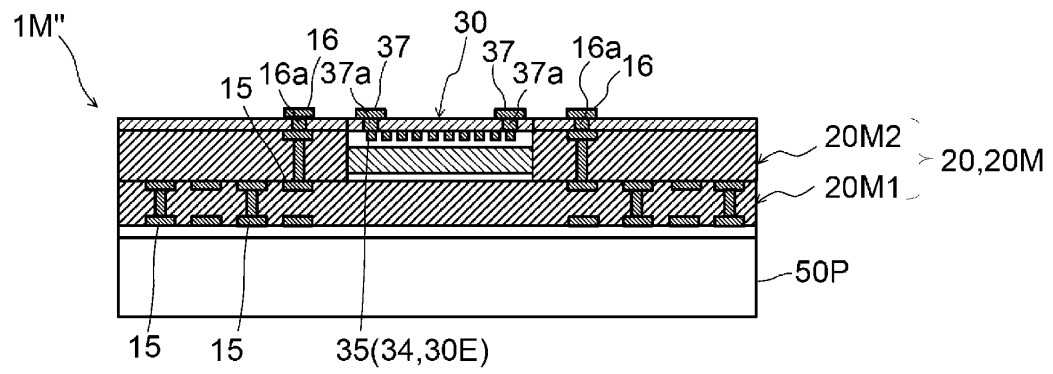
[図26C]



[図26D]

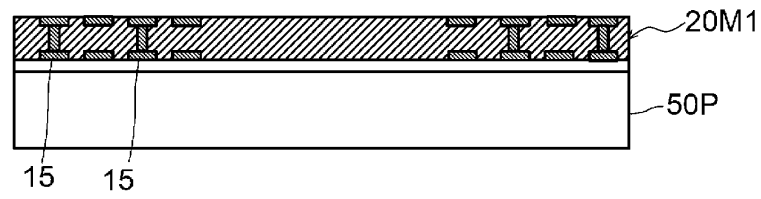


[図27]

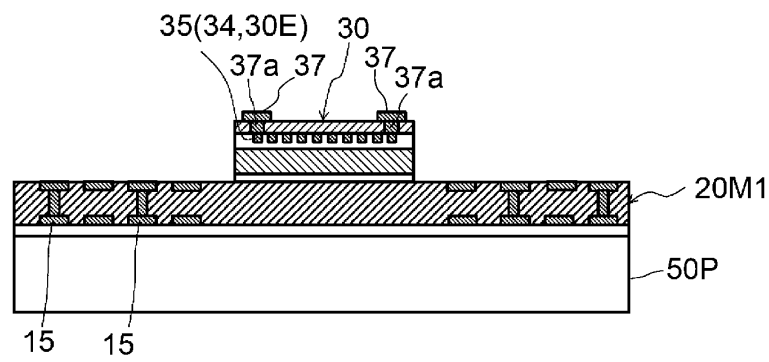


[図28]

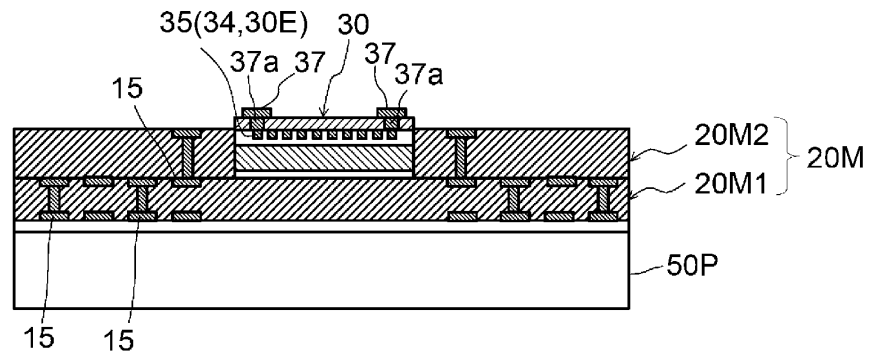
(A)



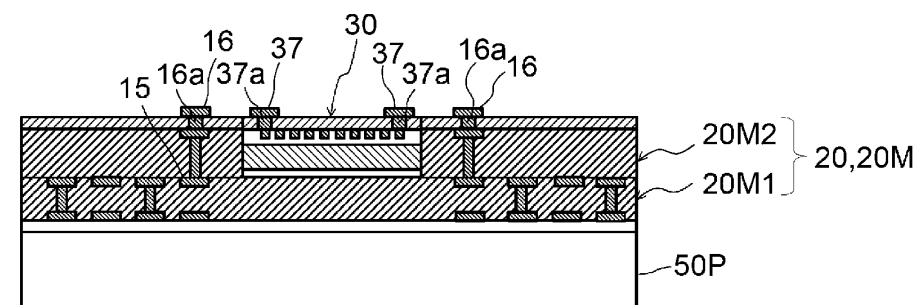
(B)



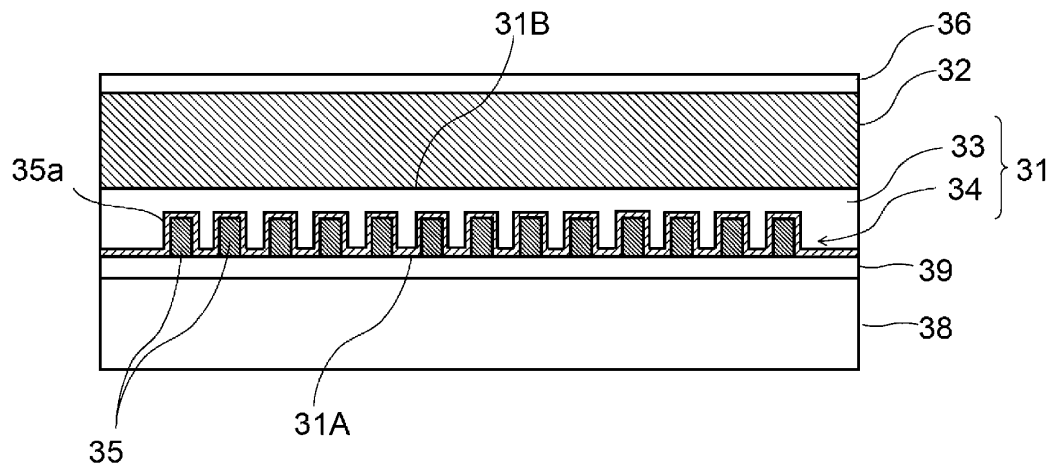
(C)



(D)

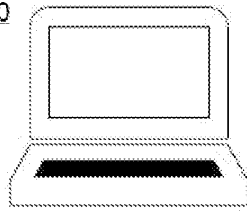


[図29]

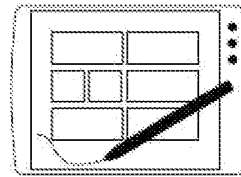


[図30]

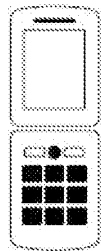
110



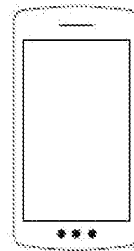
120



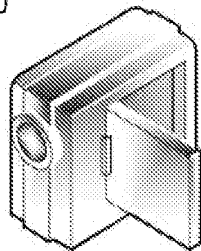
130



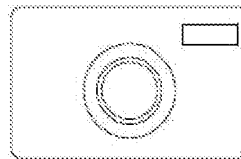
140



150



160



170

