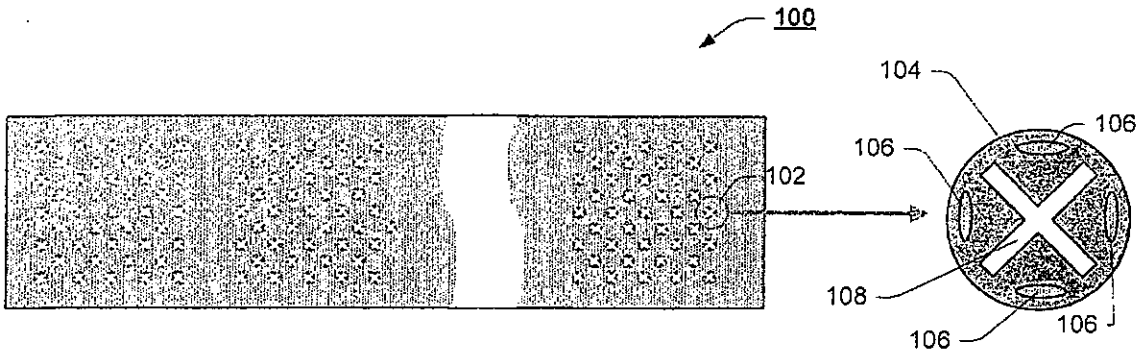

100
104
106
106
102
108
106
106

FIG. 1

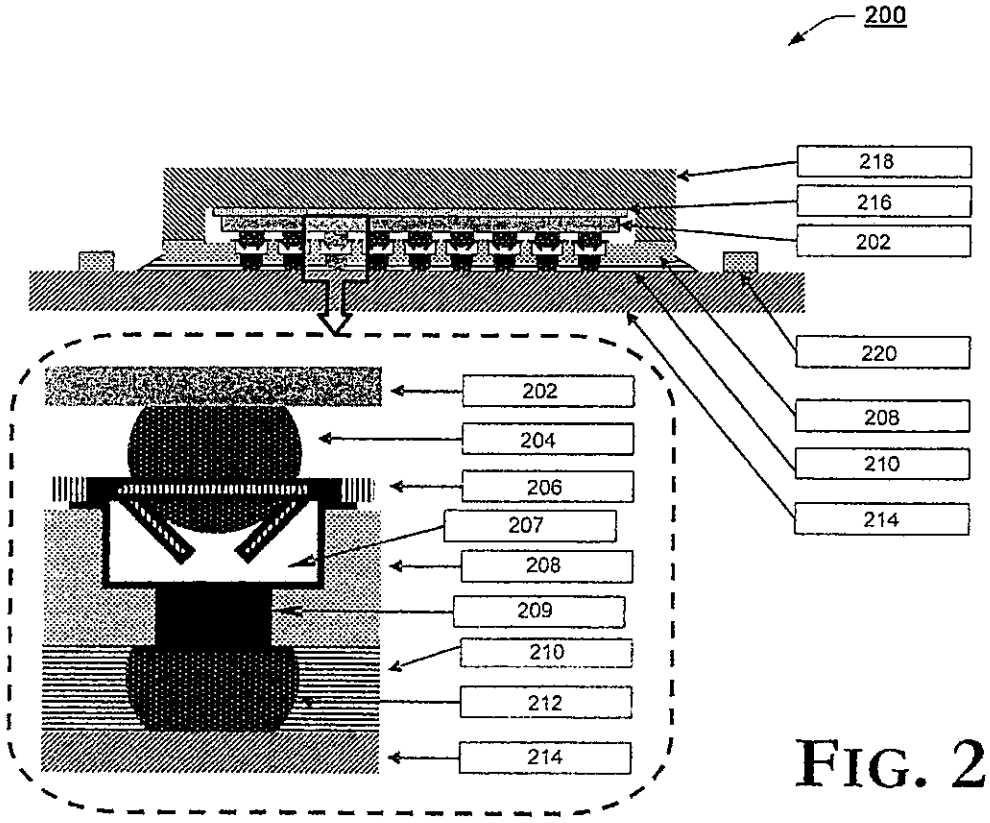

200
218
216
202
220
208
210
214
202
204
206
207
208
209
210
212
214

FIG. 2

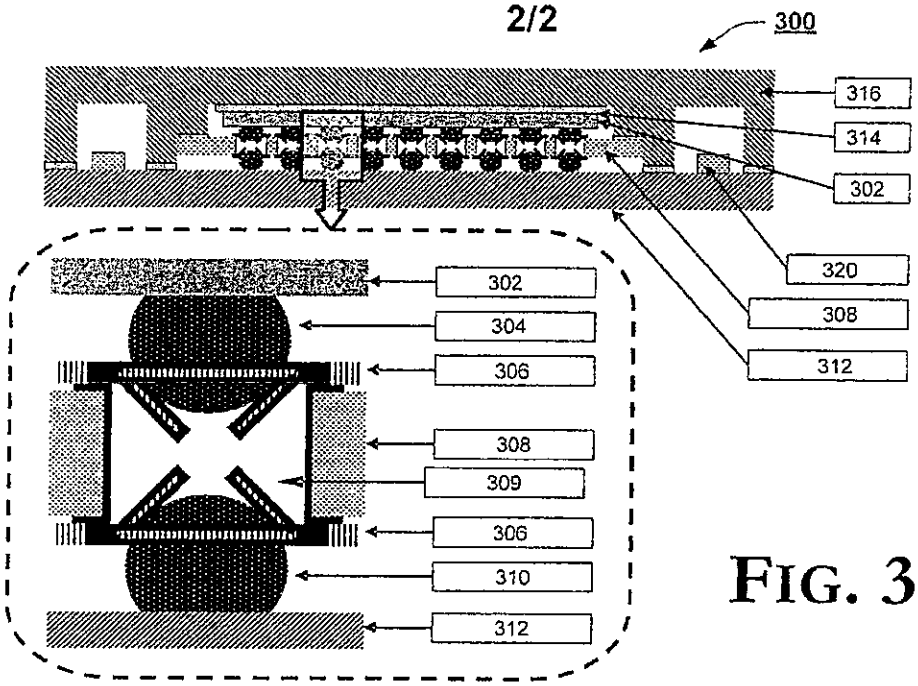

300
316
314
302
320
308
312
302
304
306
308
309
306
310
312

FIG. 3

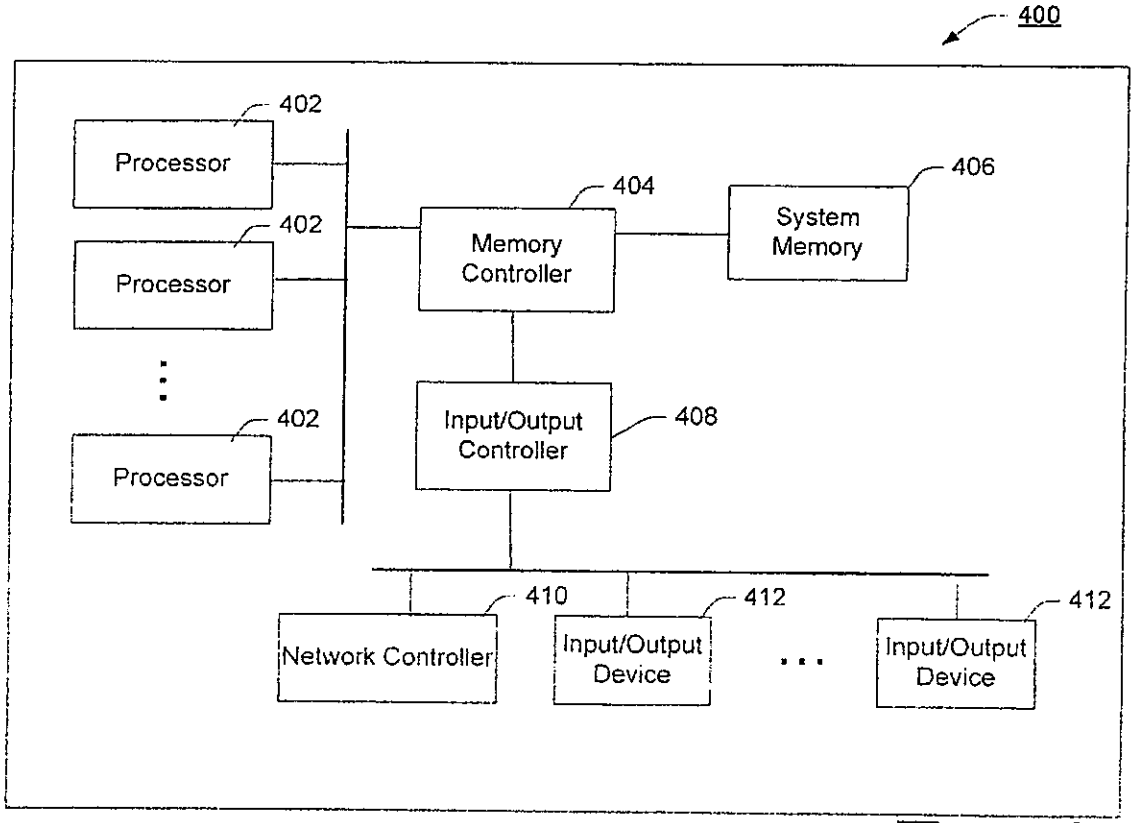

400
402
Processor
402
Processor
402
Processor
404
Memory Controller
406
System Memory
Input/Output Controller
408
410
Network Controller
412
Input/Output Device
412
Input/Output Device

FIG. 4