

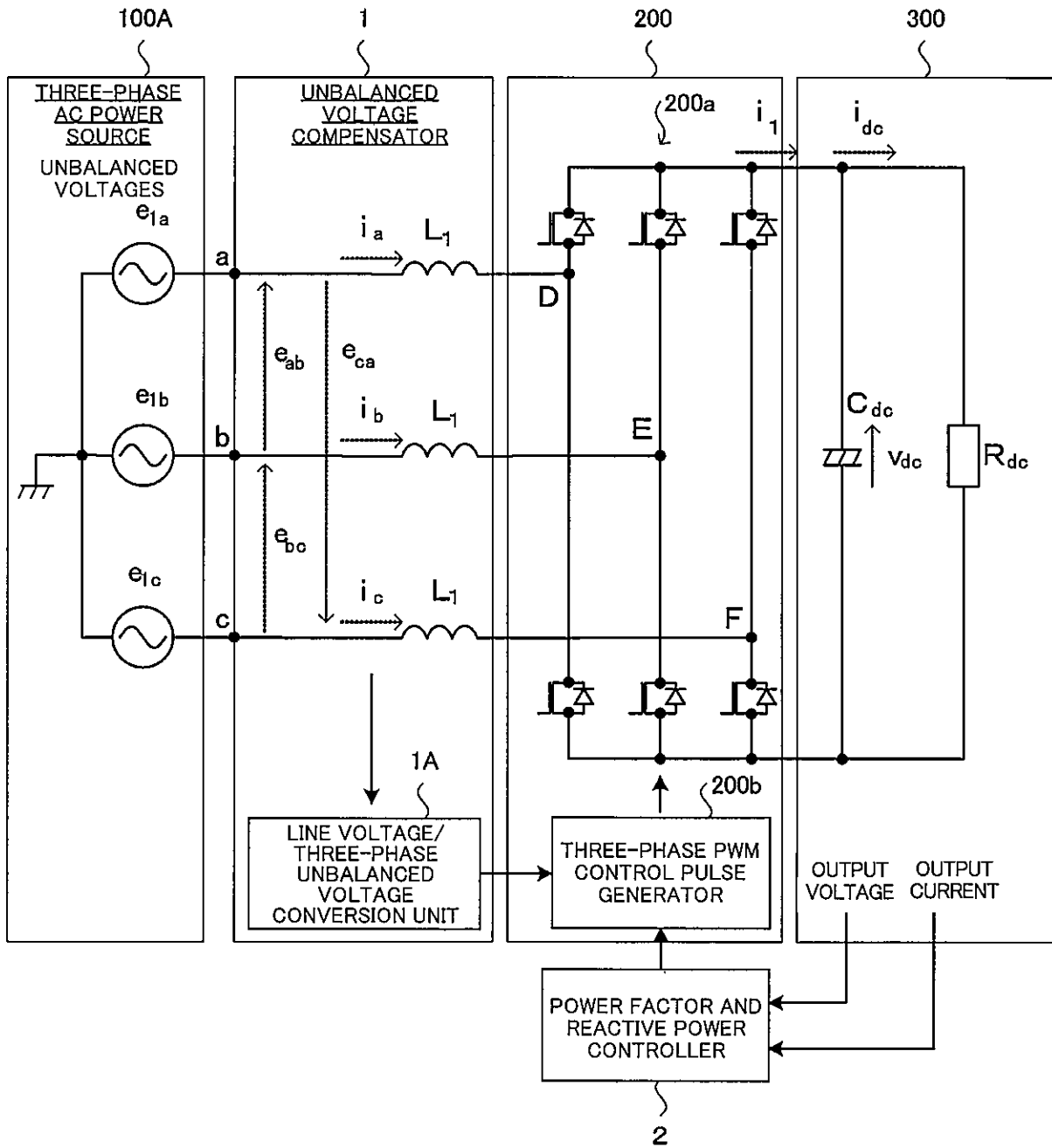


FIG. 1

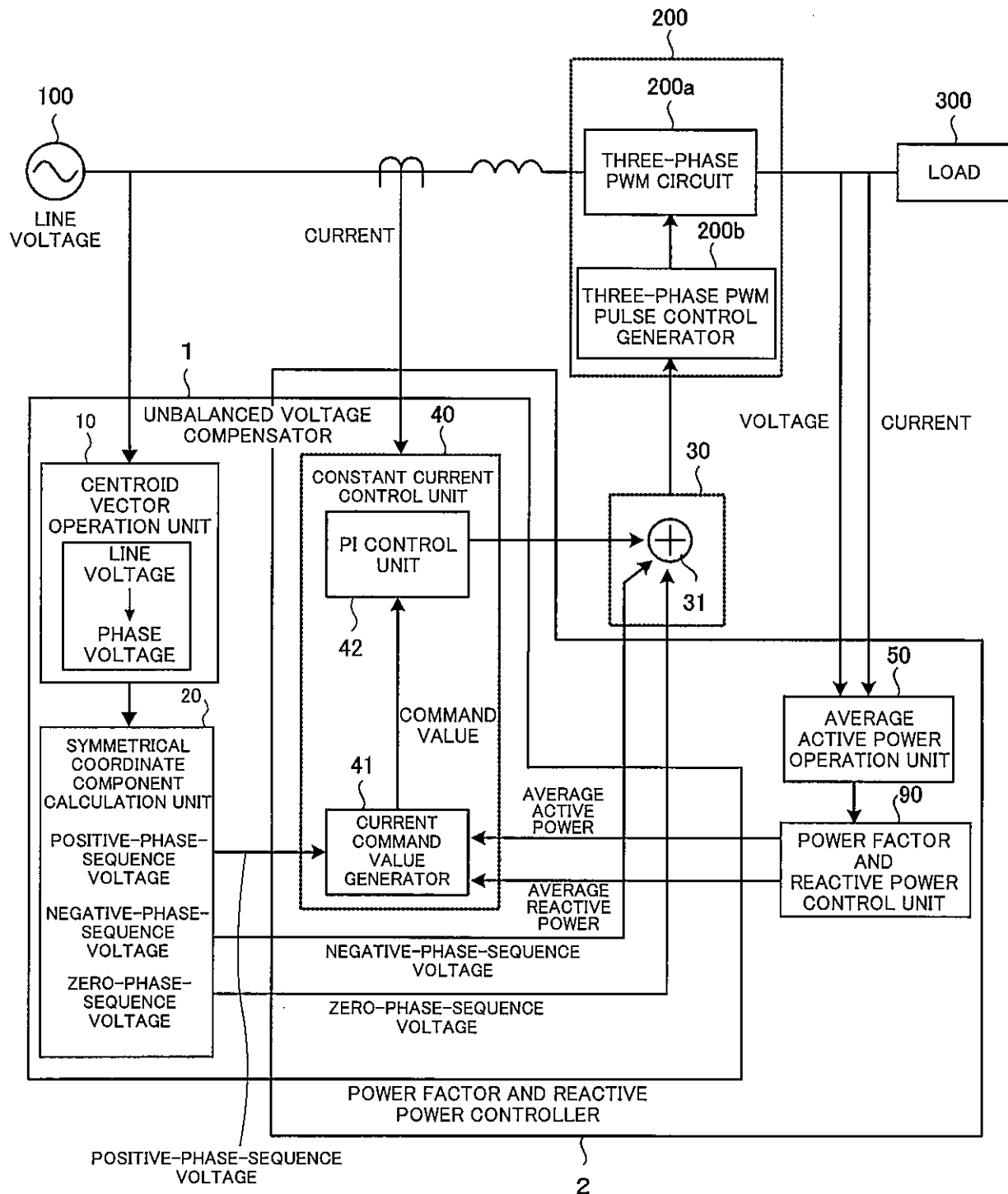


FIG. 2

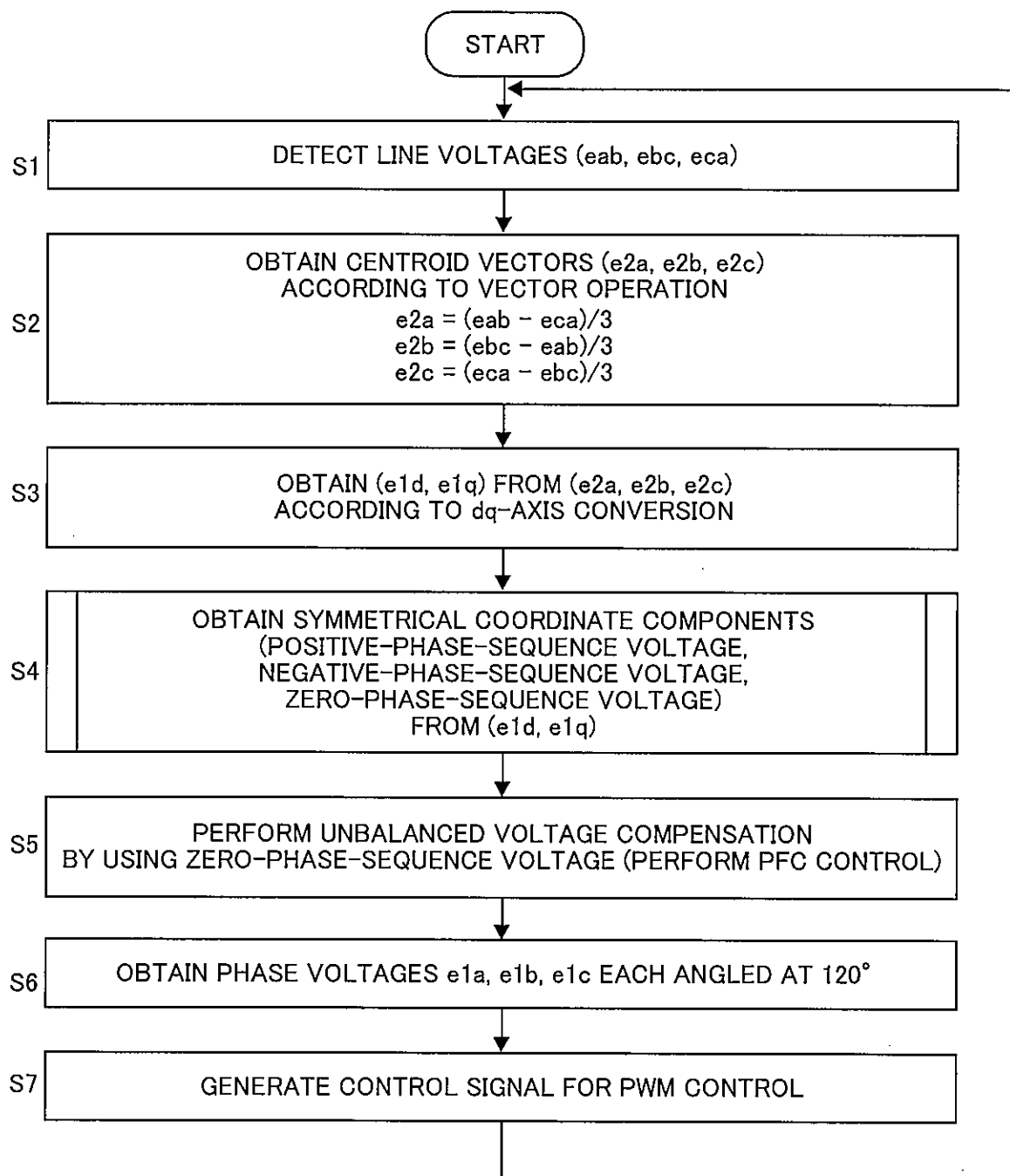


FIG. 3

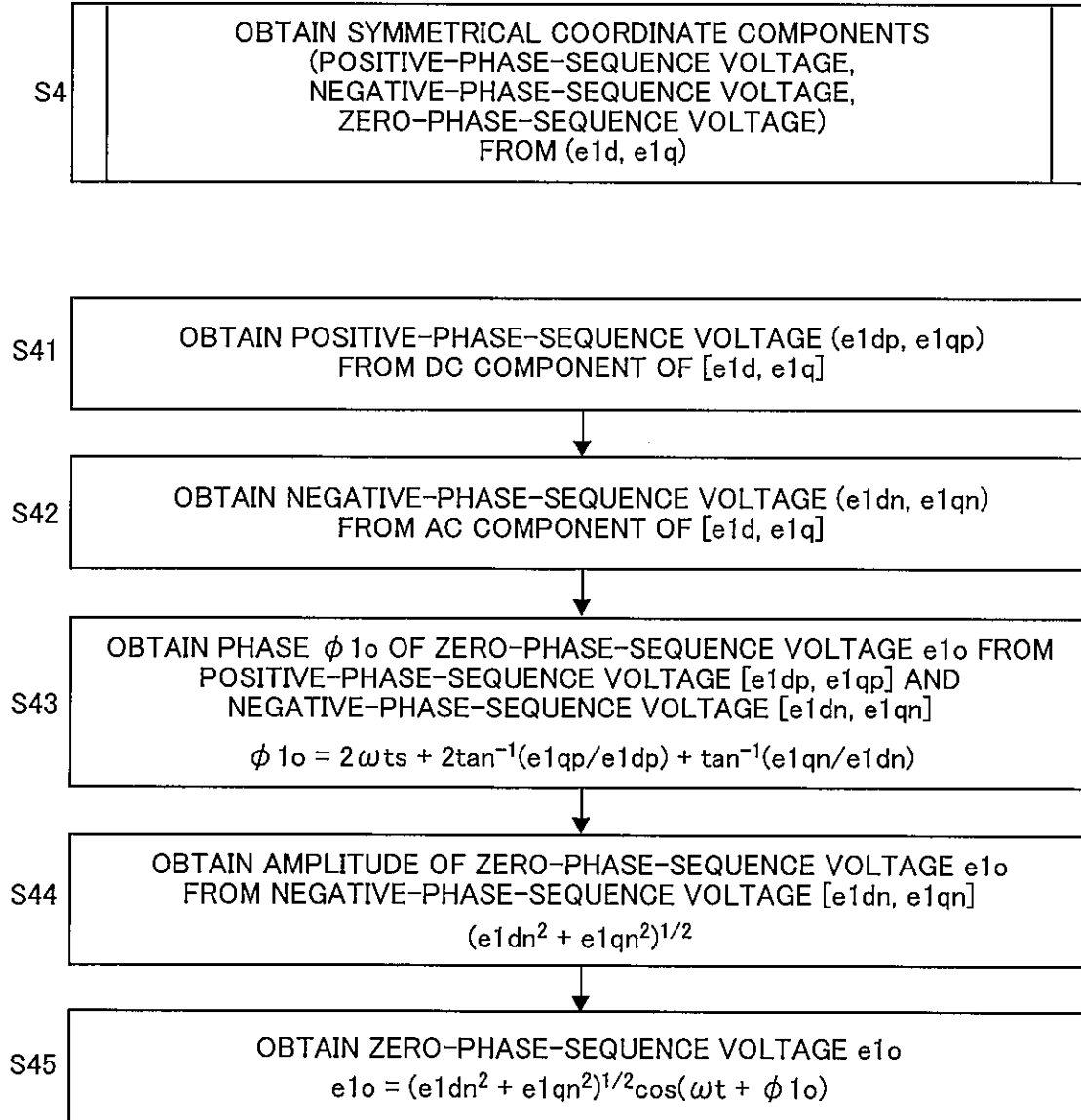


FIG. 4

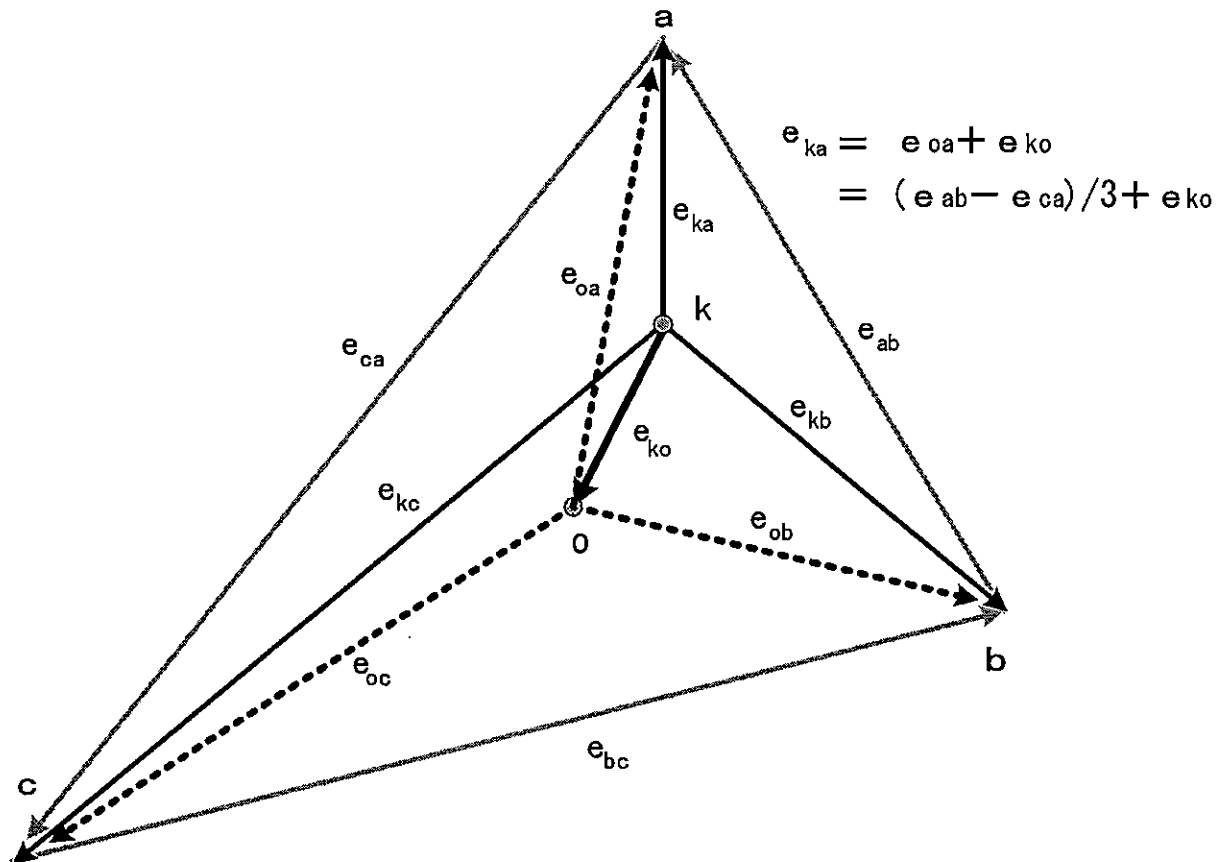


FIG. 6A

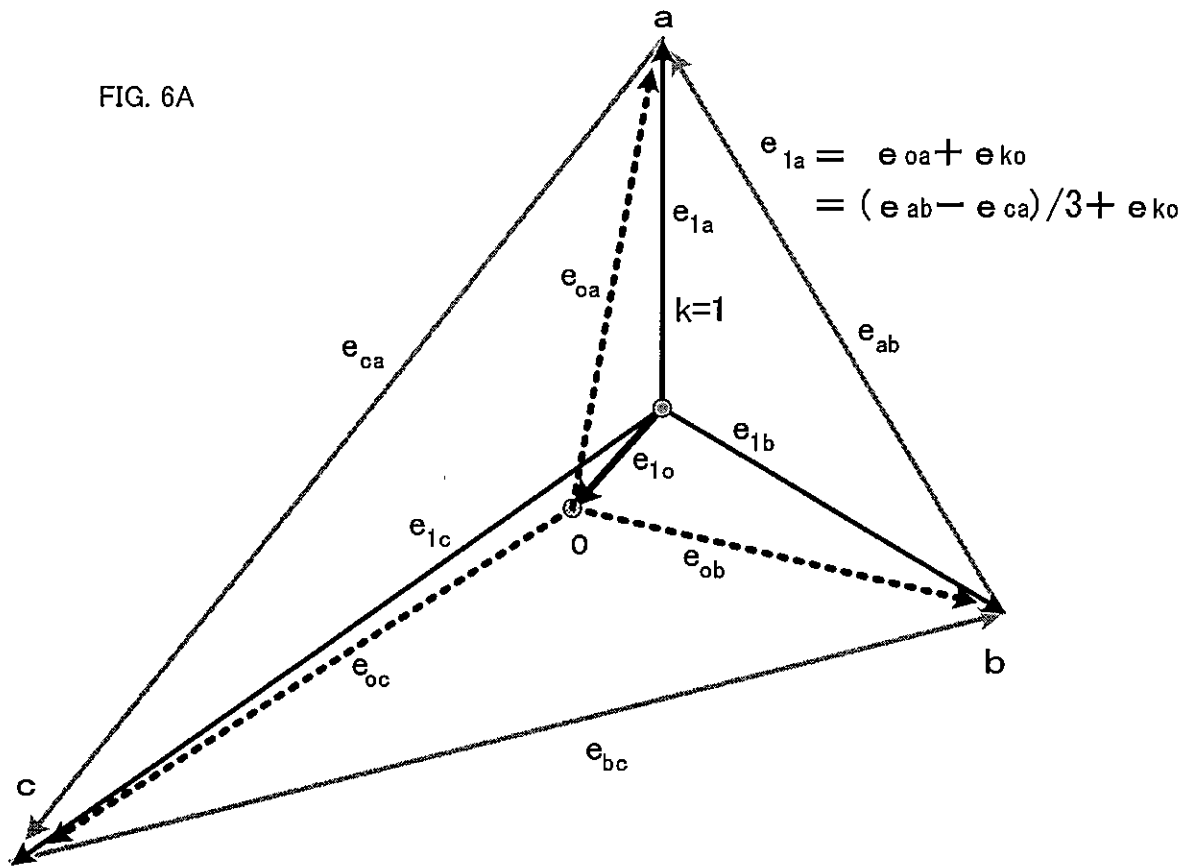
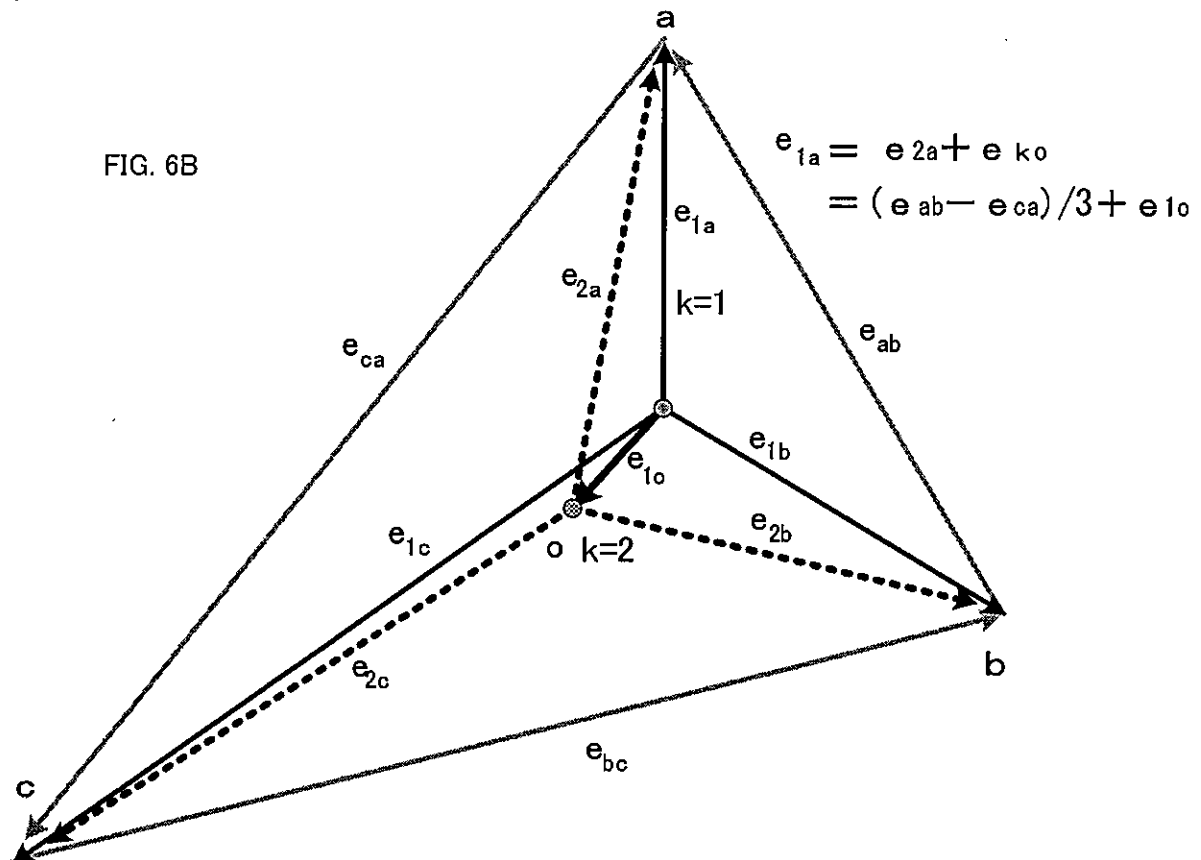


FIG. 6B



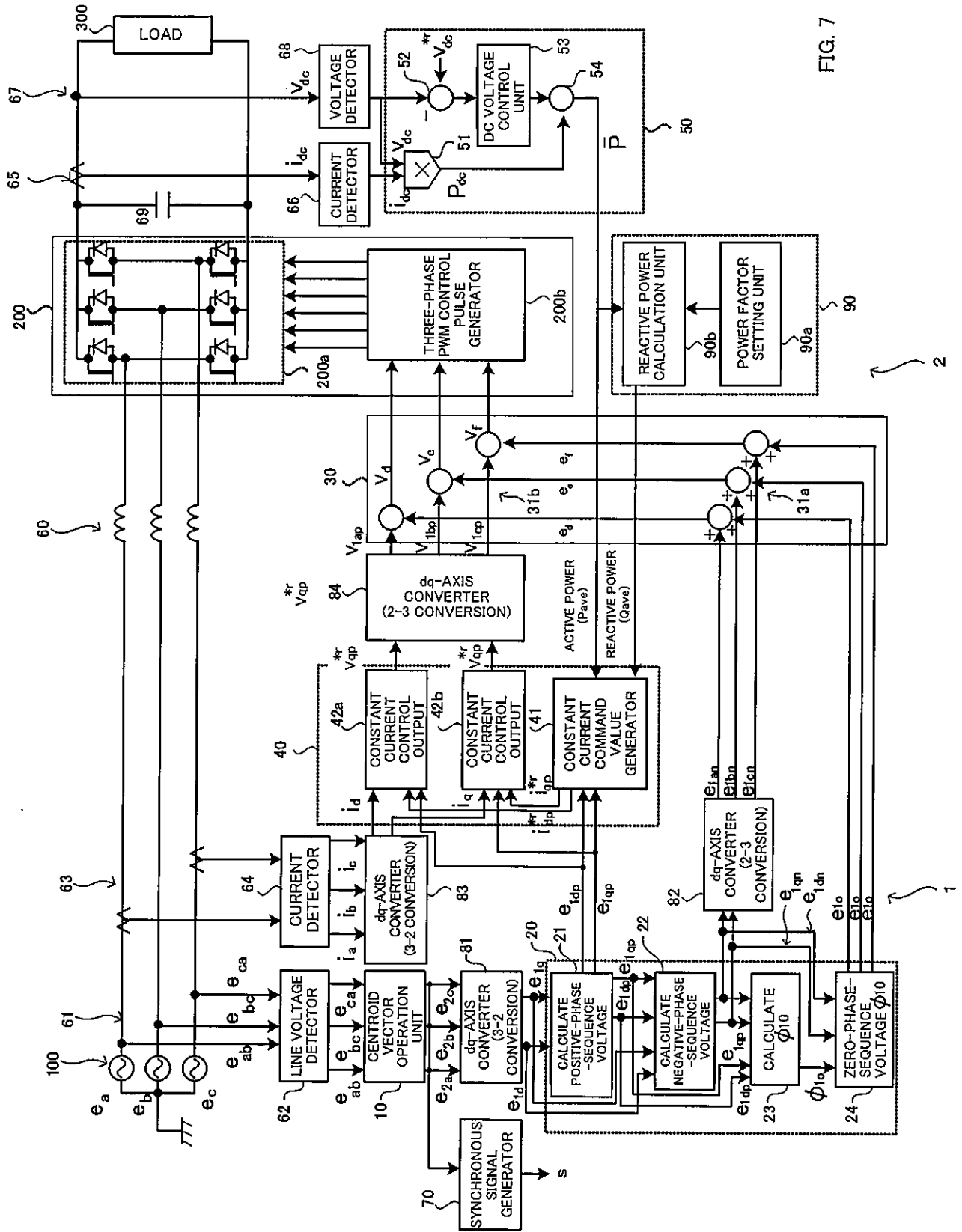


FIG. 7

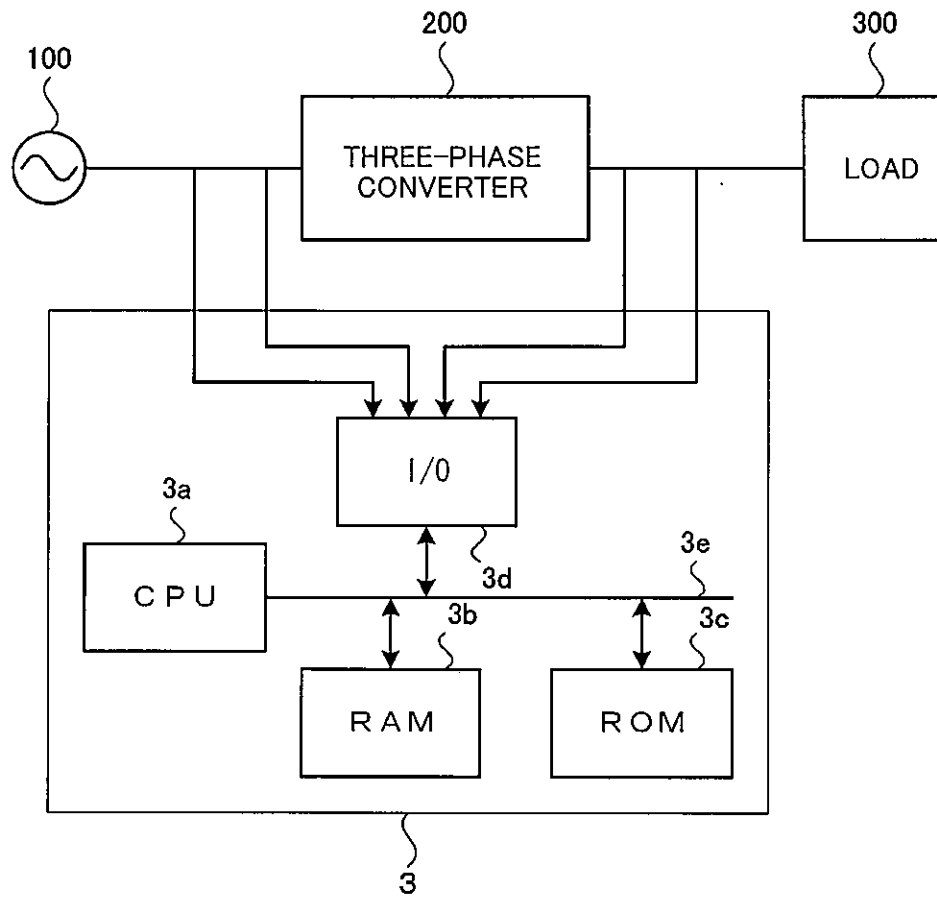


FIG. 8

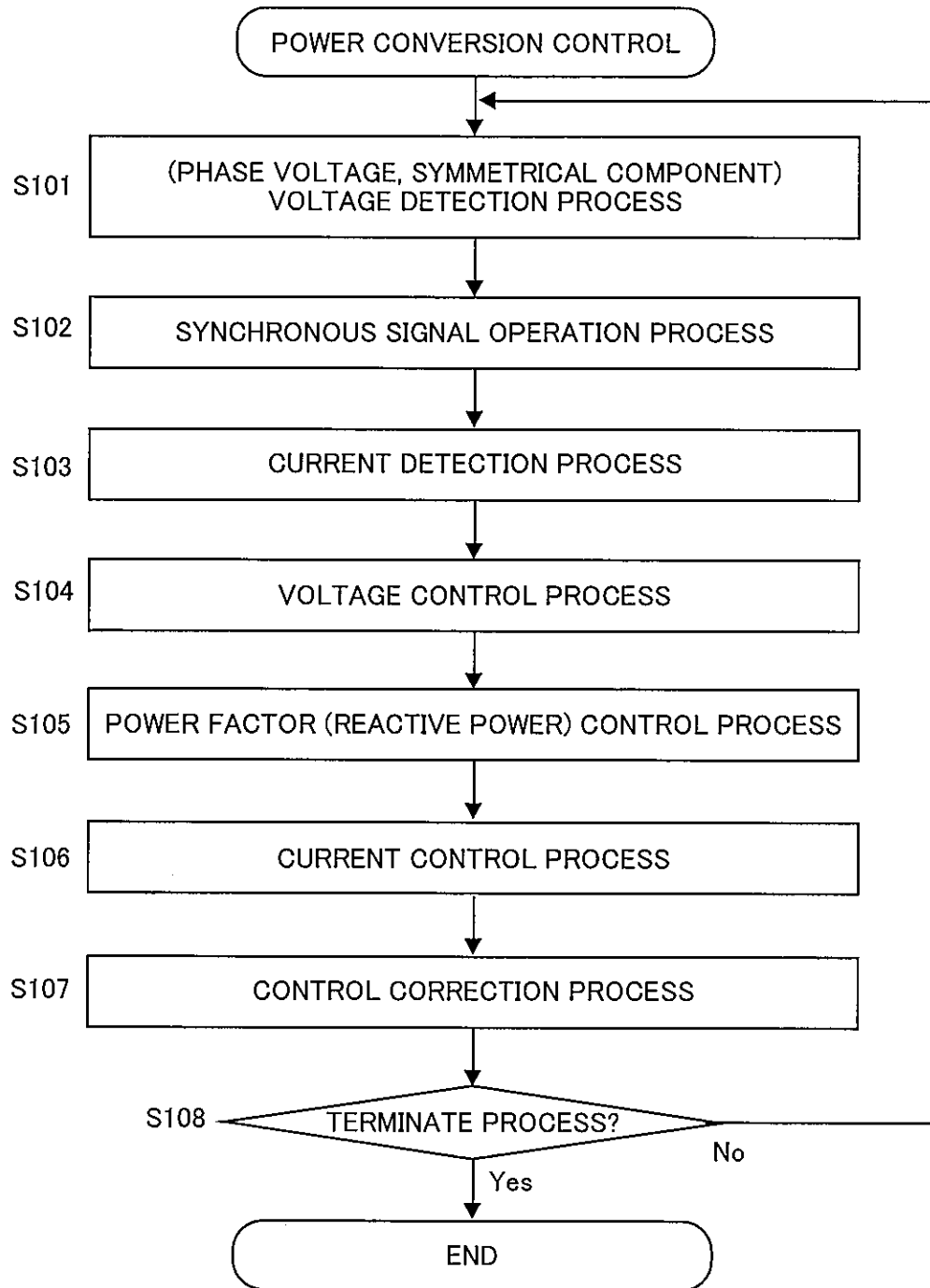


FIG. 9

FIG. 10A
VOLTAGE SAG COMPENSATION IS NOT PERFORMED

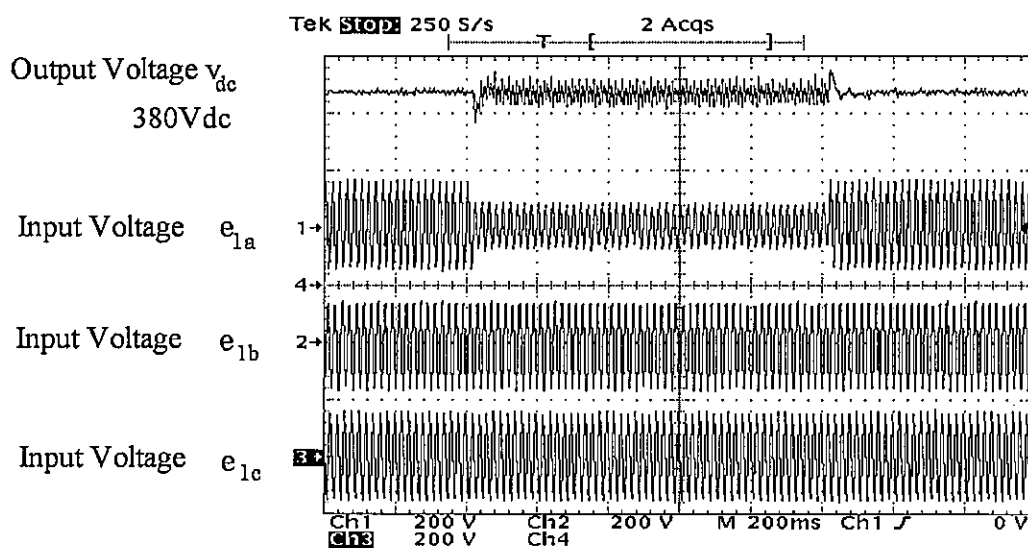
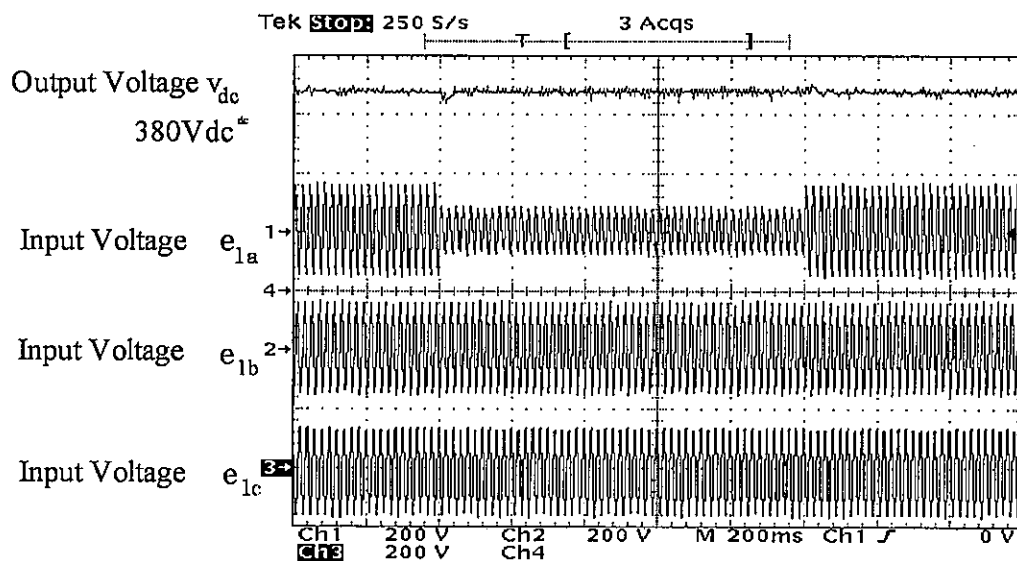


FIG. 10B
VOLTAGE SAG COMPENSATION IS PERFORMED



e_{1a} : 100%→50%→100%

FIG. 11A
VOLTAGE SAG COMPENSATION IS NOT PERFORMED
(10A/V)

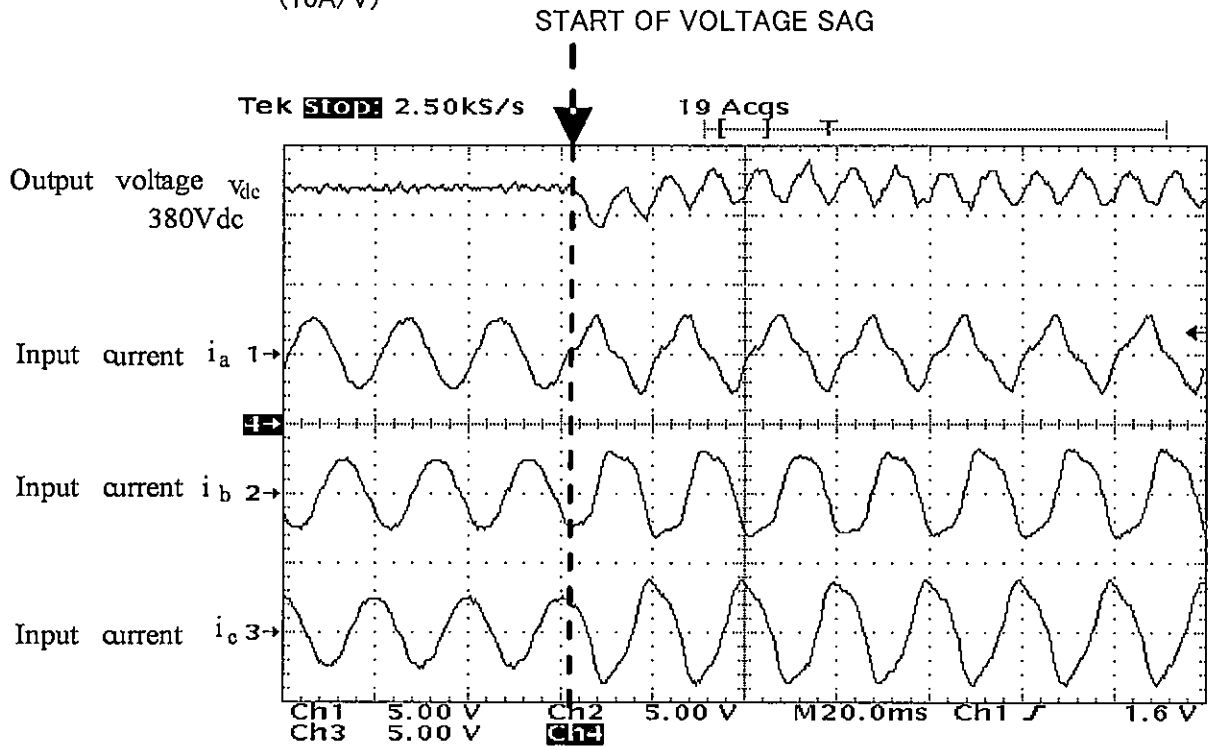


FIG. 11B
VOLTAGE SAG COMPENSATION IS PERFORMED
(10A/V)

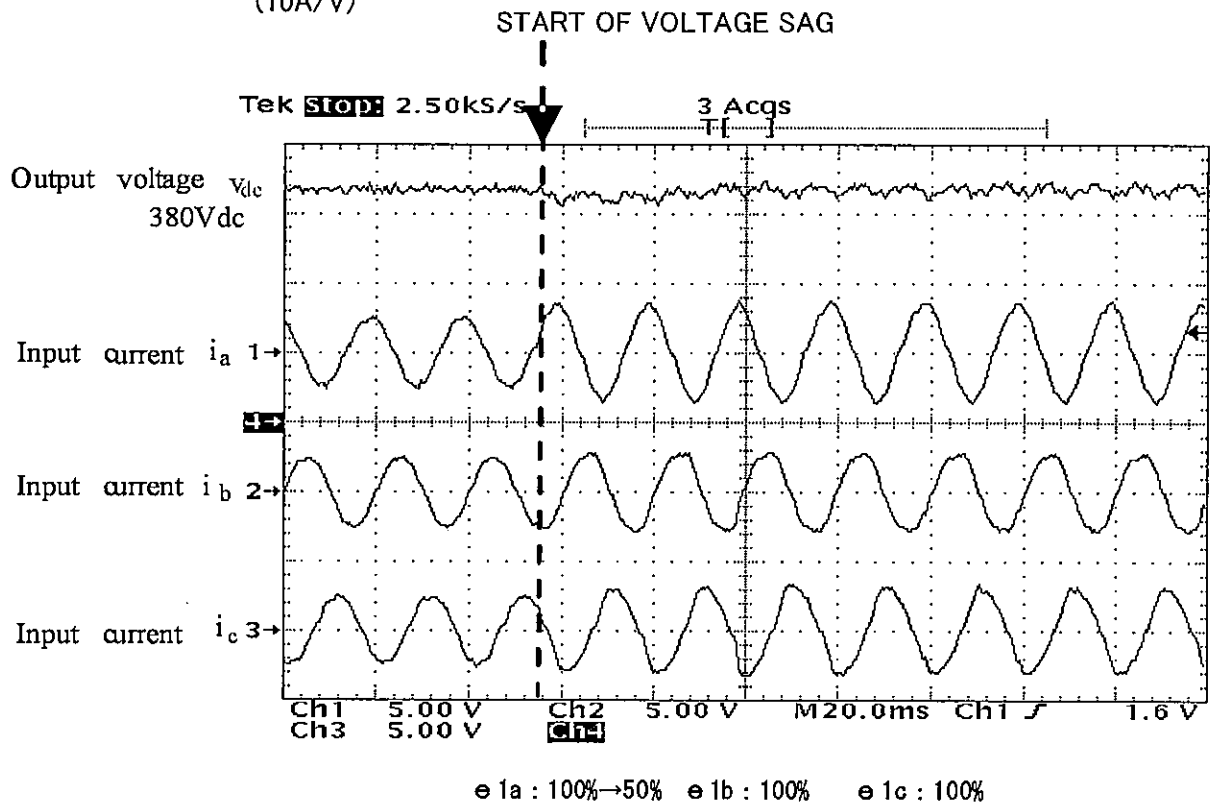


FIG. 12A
VOLTAGE SAG COMPENSATION IS NOT PERFORMED
(10A/V)

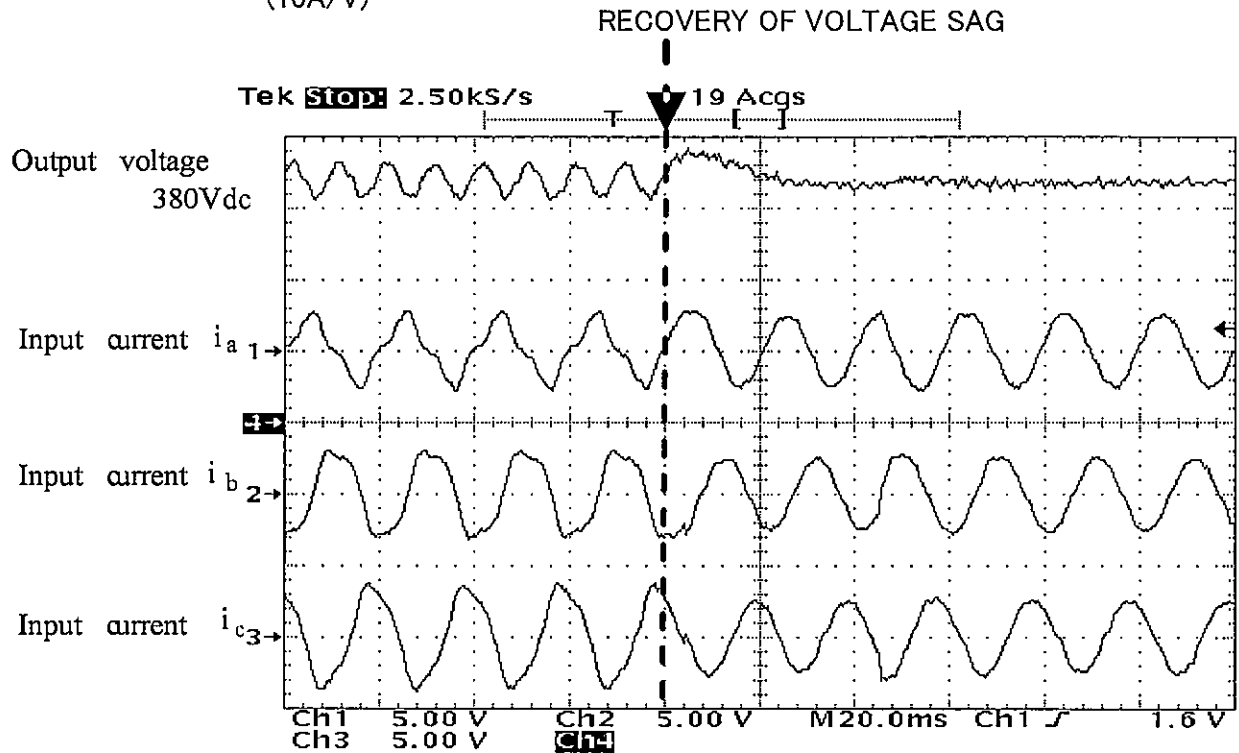


FIG. 12B
VOLTAGE SAG COMPENSATION IS PERFORMED
(10A/V)

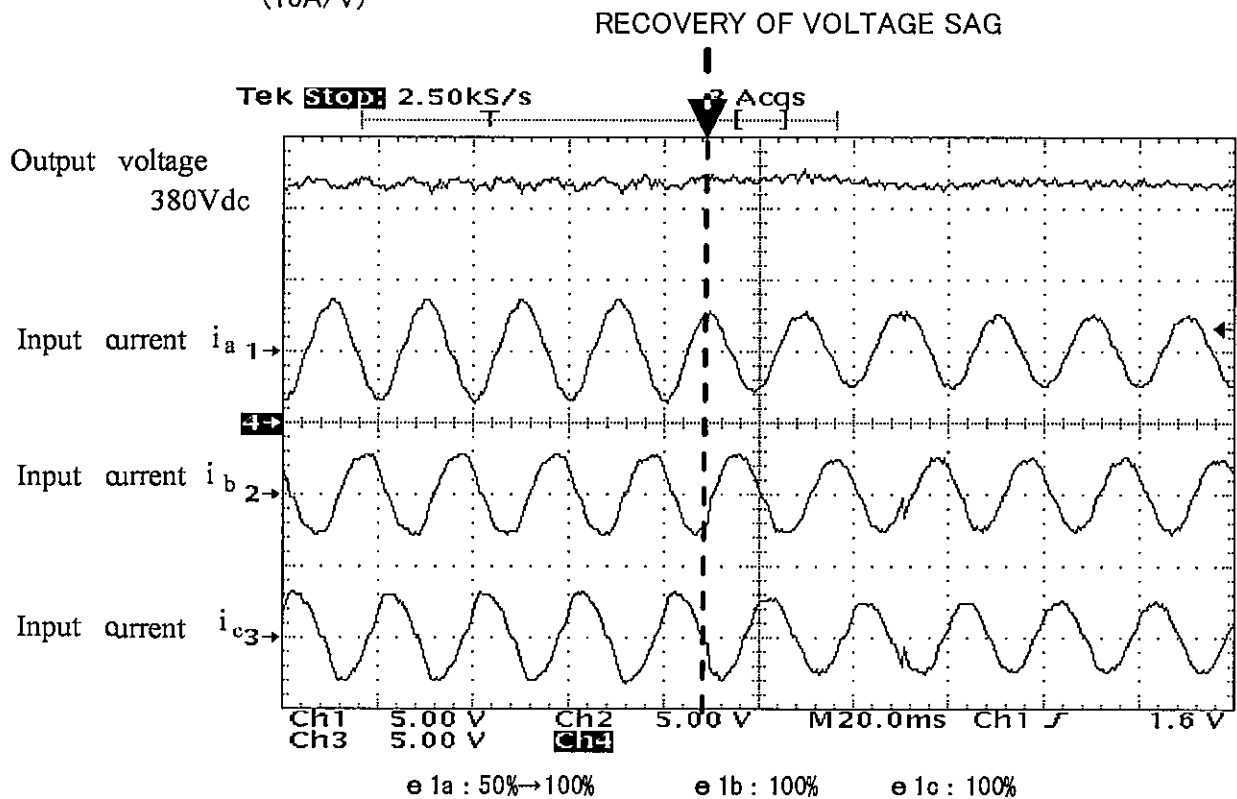


FIG. 13A
Input Voltage

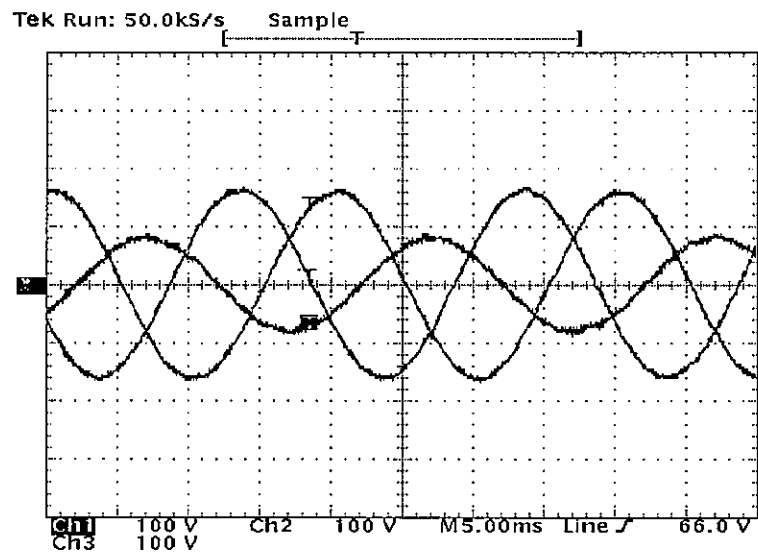


FIG. 13B
Input Current
VOLTAGE SAG
COMPENSATION IS
NOT PERFORMED
(20A/V)

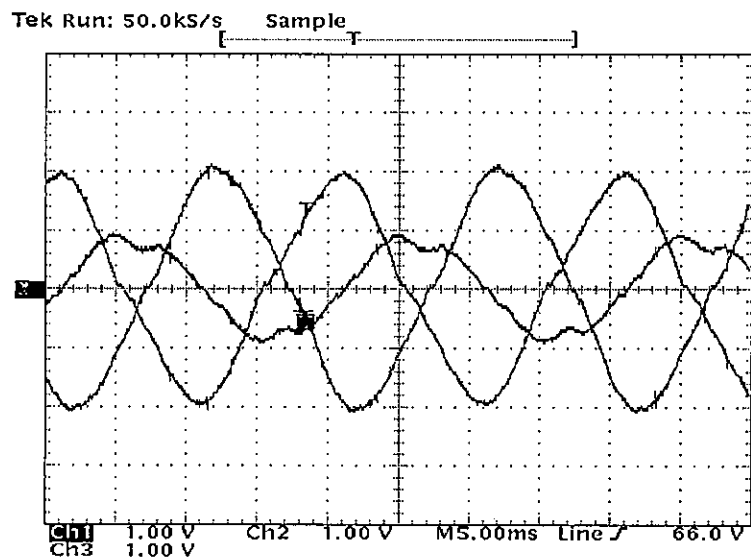
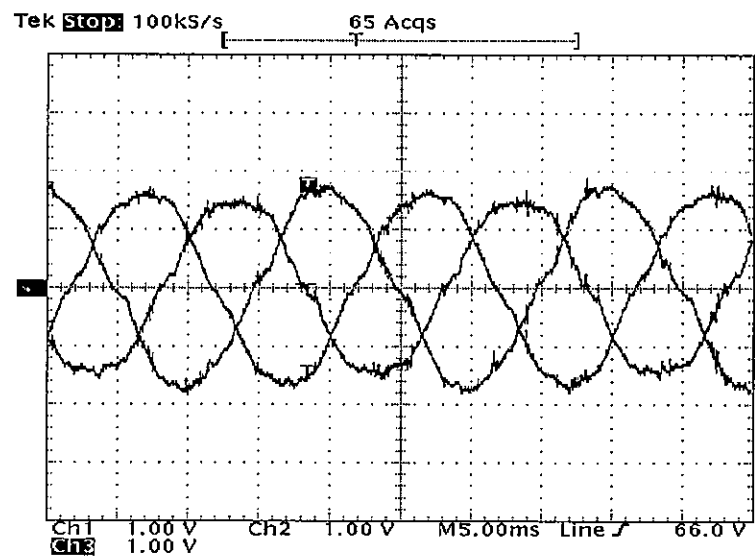


FIG. 13C
Input Current
VOLTAGE SAG
COMPENSATION IS
PERFORMED
(20A/V)



e 1a : 50%

e 1b : 100%

e 1c : 100%

FIG. 14A
Input Voltage

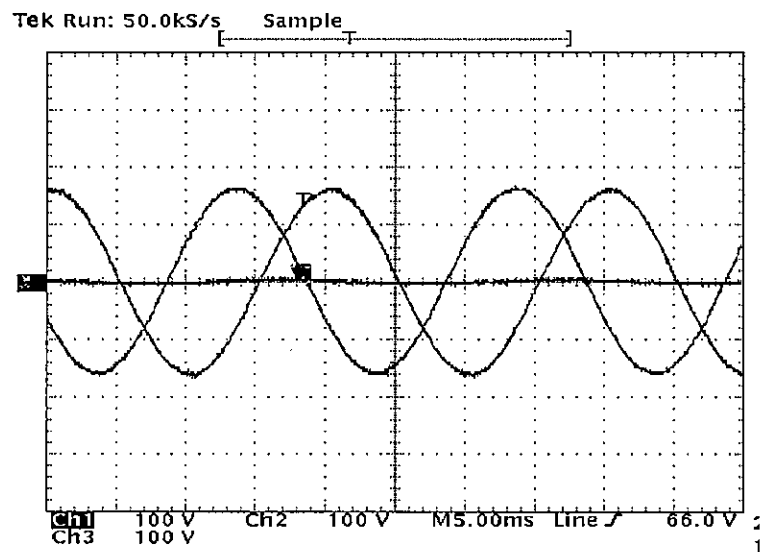


FIG. 14B
Input Current
VOLTAGE SAG
COMPENSATION IS
NOT PERFORMED
(20A/V)

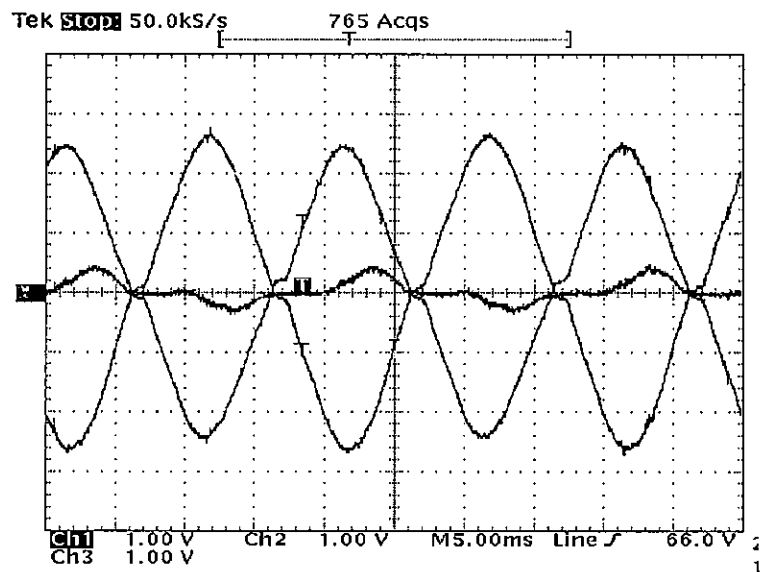
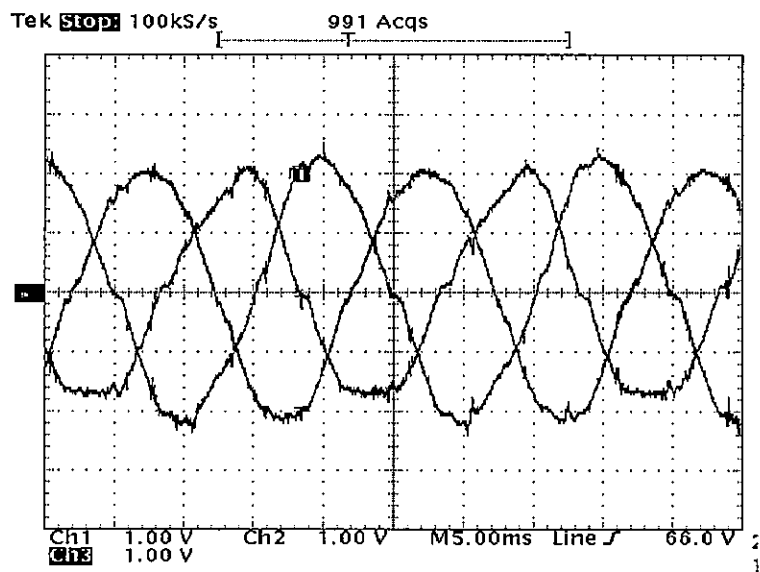


FIG. 14C
Input Current
VOLTAGE SAG
COMPENSATION IS
PERFORMED
(20A/V)



e1a: 0%

e1b: 100%

e1c: 100%

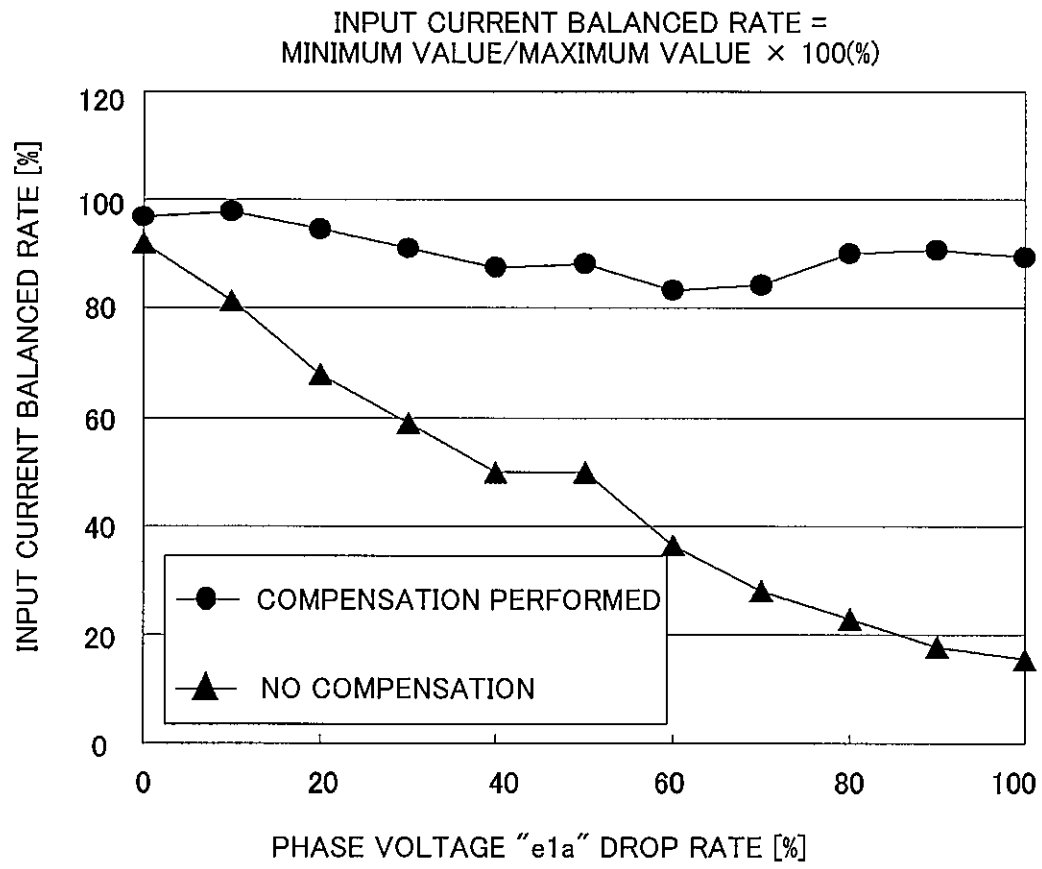


FIG. 15

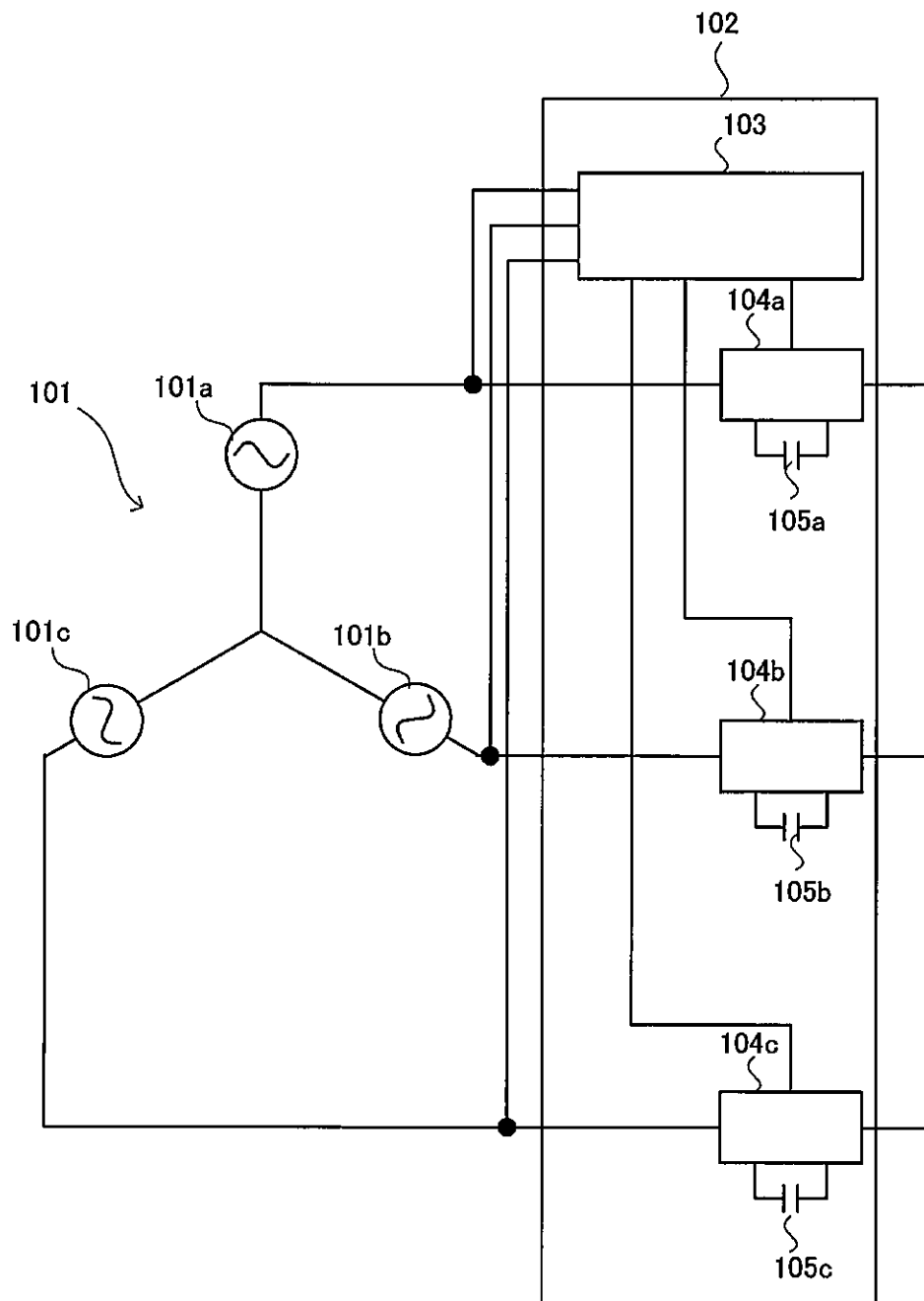


FIG. 16

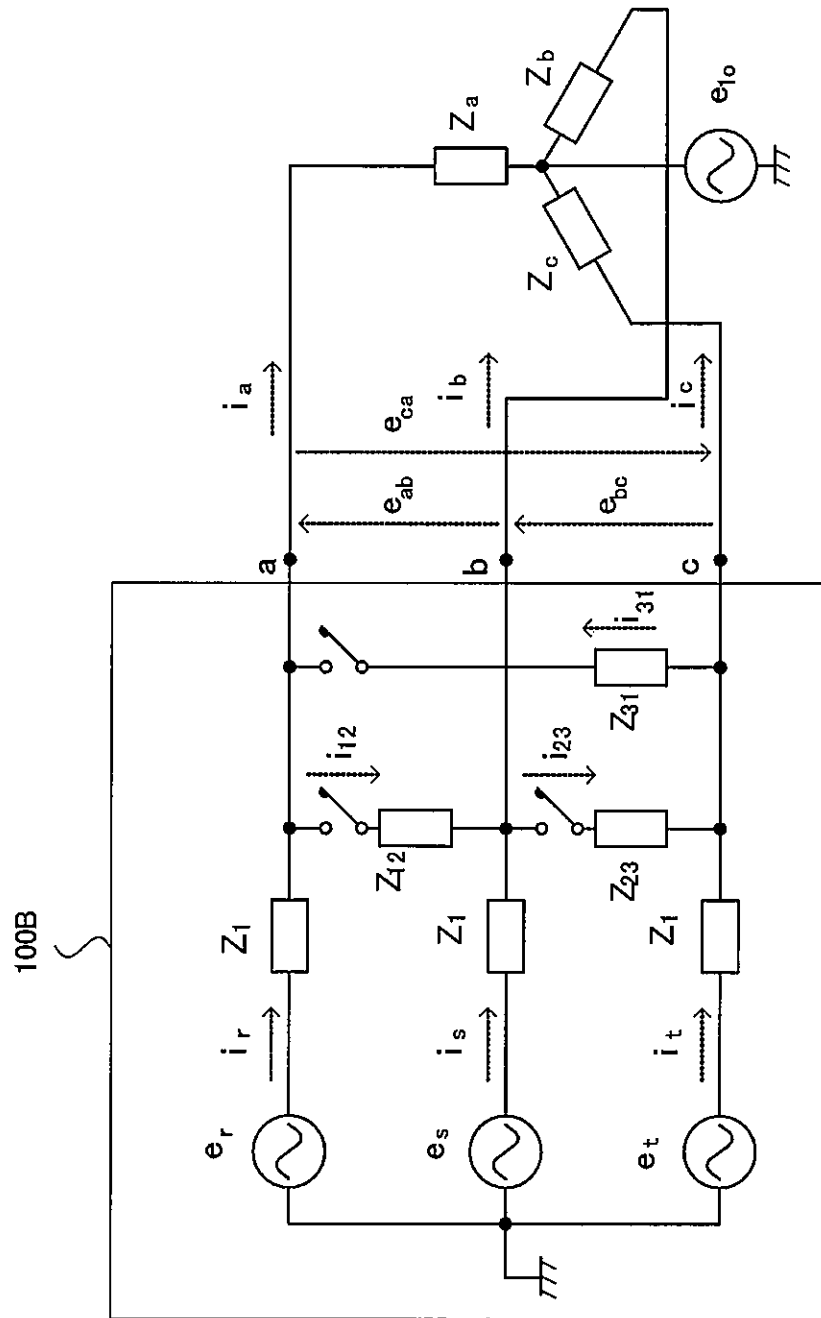


FIG. 17

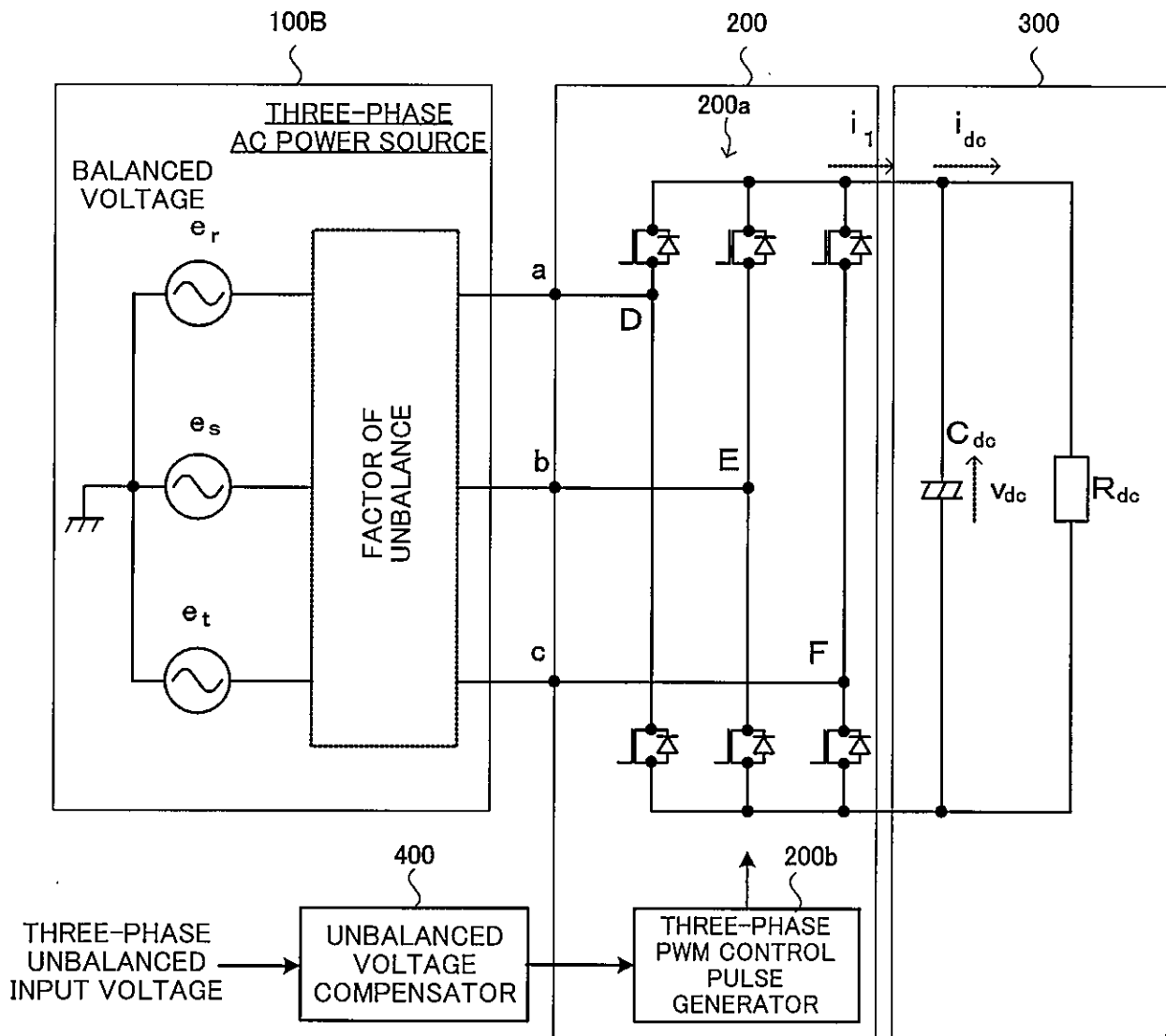


FIG. 18