



ABSTRACT OF THE DISCLOSURE

A plasma display panel driving system having a
5 common X electrode 22 arranged on a front glass
substrate 21 driven in common, a independent Y electrode
23 arranged parallel to the common X electrode 22 on the
front glass substrate 21 driven independently, an
address A electrode arranged perpendicular to the common
10 X electrode and the independent Y electrode on the back
glass substrate driven independently, and means for
performing at least one electric discharge for
equalizing of electric charge particle in a cell in
which another electric charge particle is caused
15 beforehand, thereby improving contrast.

Erasing and polarization of electric charge
particles are performed by the fine line erasing pulse
46a after sustaining period 2c, and a equalizing pulse
having a high voltage is supplied to the independent Y
20 electrode to which the last fine line erasing pulse 46a
or 86a is supplied, and a regulating pulse 40 is
supplied to the common X electrode after supplying of
the equalizing pulse.

Further, a field bloc having plurality of sub-
25 fields are provided, and a full writing electric
discharge and a fine line erasing electric discharge are
performed in the first sub-field of each field block for
reducing the number of electric discharging.