CLAIMS

1. Radio frequency circuitry comprising:

first circuitry;

second circuitry;

a first interconnection coupling the first circuitry to the second circuitry, wherein the first interconnection is continuous and coupled to a ground via a first switch at a first junction;

third circuitry; and

a second interconnection coupling the third circuitry to the first interconnection via a second switch at a second junction,

wherein the first switch and the ground are configured to provide a lower impedance at the first junction and a higher impedance at the second junction in an operational configuration of the radio frequency circuitry.

2. The radio frequency circuitry of claim 1, wherein the first circuitry, the second circuitry, or the third circuitry comprises an amplifier, a digital-to-analog converter, an analog-to-digital converter, a filter, a mixer, a digital up converter, a digital down converter, a modulator, or a demodulator.

3. The radio frequency circuitry of claim 1, wherein the first interconnection is configured to transmit a first signal having a first frequency from the first circuitry to the second circuitry and the second interconnection is configured to transmit a second signal having a second frequency from the third circuitry to the second circuitry.

4. The radio frequency circuitry of claim 1, wherein the second junction is within 5% of a length of the first interconnection from the second circuitry.

5. The radio frequency circuitry of claim 1, wherein a resistive element is coupled to the first switch.

6. The radio frequency circuitry of claim 5, wherein a combined resistance of the first switch and the resistive element equals a characteristic impedance of the first interconnection.

7. The radio frequency circuitry of claim 5, wherein the first junction is within 5% of a length of the first interconnection from the first circuitry.

8. Tangible, non-transitory, computer-readable media storing instructions that cause processing circuitry to:

receive an indication of a frequency of a signal to be sent from a first circuitry to a second circuitry over a first interconnection or a third circuitry to the second circuitry over a second interconnection and the first interconnection;

deactivate a first switch coupling the third circuitry to the first interconnection and deactivate a second switch coupling a third interconnection to the first interconnection based on the frequency being associated with a priority; and

activate the first switch and activate the second switch based on the frequency not being associated with the priority.

9. The tangible, non-transitory, computer-readable media of claim 8, wherein a ground is coupled to the second switch on the third interconnection.

10. The tangible, non-transitory, computer-readable media of claim 9, wherein the second interconnection couples to the first interconnection at a first junction, the third interconnection couples to the first interconnection at a second junction, and the second junction is a quarter lambda length away from the first junction based on the frequency.

11. The tangible, non-transitory, computer-readable media of claim 10, wherein the second switch and the ground provide a lower impedance at the second junction and a higher impedance at the first junction.

12. The tangible, non-transitory, computer-readable media of claim 8, wherein the instructions cause the processing circuitry to activate the first circuitry based on the frequency being associated with the priority.

13. The tangible, non-transitory, computer-readable media of claim 8, wherein the instructions cause the processing circuitry to deactivate the first circuitry based on the frequency not being associated with the priority.

14. The tangible, non-transitory, computer-readable media of claim 8, wherein the priority is a high priority.

15. An electronic device comprising:

one or more antennas; and

a transceiver coupled to the one or more antennas, the transceiver comprising

first circuitry,

second circuitry,

a first interconnection coupling the first circuitry to the second circuitry, wherein the first interconnection is configured to send a first signal of a first frequency from the first circuitry to the second circuitry, and wherein a ground is coupled to the first interconnection via a first switch at a first junction,

third circuitry, and

a second interconnection coupling the third circuitry to the first interconnection via a second switch at a second junction, wherein the second interconnection is configured to send a second signal of a second frequency from the third circuitry to the second circuitry, and

wherein the first switch and the ground are configured to provide a lower impedance at the first junction and a higher impedance at the second junction in an operational configuration of the transceiver.

16. The electronic device of claim 15, wherein the first interconnection does not comprise switching circuitry.

17. The electronic device of claim 15, wherein the second switch is configured to deactivate to send the first signal from the first circuitry to the second circuitry.

18. The electronic device of claim 15, wherein the first circuitry is configured to activate to send the first signal from the first circuitry to the second circuitry.

19. The electronic device of claim 15, wherein the second switch is configured to activate to send the second signal from the third circuitry to the second circuitry.

20. The electronic device of claim 15, wherein the first circuitry is configured to deactivate to send the second signal from the third circuitry to the second circuitry.