ACTIVE MATRIX DISPLAY DEVICE

ABSTRACT OF THE DISCLOSURE

An active matrix display device comprising row gate lines, column signal lines and matrix pixels disposed at intersections of the gate and signal lines. The display device also includes a V shift register for line-sequentially scanning the gate lines and selecting pixels of one row during each horizontal scanning period, and a horizontal scanning circuit for sequentially sampling an actual video signal to the signal lines within one horizontal scanning period and writing the sampled actual video signal dot-sequentially in the pixels of one row. A precharge means is included as a characteristic requisite, wherein a first precharge signal is supplied simultaneously to the entire signal lines during a blanking period which precedes the horizontal scanning period, and further a second precharge signal is supplied sequentially to the signal lines prior to the step of sequentially sampling the actual video signal to the signal lines during each horizontal scanning period. This device is capable of preventing potential fluctuation that may otherwise be caused on a signal line by dot-sequential driving.

FIGURE 1