CLAIMS:

1. A method for video decoding, comprising:

determining whether a first block associated with a second block is predicted according to a block differential pulse code modulation (BDPCM) mode,

in response to determining that the first block is predicted according to the BDPCM mode, associating the first block with an intra prediction mode value based on a BDPCM directional flag for the first block, the intra prediction mode value being selected from a plurality of intra prediction modes that include angular intra prediction modes;

determining an intra prediction mode value for the second block using the intra prediction mode value associated with the first block; and

reconstructing the second block using the determined intra prediction mode value for the second block.

2. The method according to claim 1, wherein the BDPCM directional flag is one of (i) a first value associated with a horizontal intra prediction directional mode, and (ii) a second value associated with a vertical intra prediction directional mode.

3. The method according to claim 2, wherein a total number of the plurality of intra prediction modes is 67 with the horizontal intra prediction directional mode being associated with angular intra prediction mode 18 and with the vertical intra prediction directional mode being associated with angular intra prediction mode 50.

4. The method according to claim 1, wherein determining whether the first block is predicted according to the BDPCM mode is based on a value of a BDPCM flag for the first block that indicates a presence of the BDPCM directional flag for the first block.

5. The method according to claim 1, wherein the first block and the second block are included in a same picture, and the first block spatially neighbors the second block.

6. The method according to claim 5, further comprising:

deriving for the second block, using a most probable mode (MPM) derivation process, a candidate list, the deriving the candidate list including the determining whether the first block is predicted according to the BDPCM mode, wherein

determining the intra prediction mode value for the second block further includes using the derived candidate list.

7. The method according to claim 6, wherein the candidate list includes:

a first candidate intra prediction mode value ($Mode_1$) that corresponds to the intra prediction mode value of the first block, and

a second candidate intra prediction mode value ($Mode_2$) and a third candidate intra prediction mode value ($Mode_3$) that are determined in accordance with a predetermined offset from the first candidate intra prediction mode value and a modulo M operation, in which M is a power of 2.

8. The method according to claim 1, wherein the second block is a chroma block and the first block is a luma block co-located with the chroma block.

9. The method according to claim 8, further comprising:

determining whether the second block is coded with a direct copy mode (DM); and

in response to determining that the second block is coded with the direct copy mode, determining whether the first block is predicted according to the BDPCM mode.

10. A video decoder for video decoding, comprising:

processing circuitry configured to:

determine whether a first block associated with a second block is predicted according a block differential pulse code modulation (BDPCM) mode,

in response to a determination that the first block is predicted according the BDPCM mode, associate the first block with an intra prediction mode value based on a BDPCM directional flag for the first block, the intra prediction mode value being selected from a plurality of intra prediction modes that include angular intra prediction modes,

determine an intra prediction mode value for the second block using the intra prediction mode value associated with the first block, and

reconstruct the second block using the determined intra prediction mode value for the second block.

11. The video decoder according to claim 10, wherein the BDPCM directional flag is one of (i) a first value associated with a horizontal intra prediction directional mode, and (ii) a second value associated with a vertical intra prediction directional mode.

12. The video decoder according to claim 11, wherein a total number of the plurality of intra prediction modes is 67 with the horizontal intra prediction directional mode being

associated with angular intra prediction mode 18 and with the vertical intra prediction directional mode being associated with angular intra prediction mode 50.

13. The video decoder according to claim 10, wherein the determination whether the first block is predicted according to the BDPCM mode is based on a value of a BDPCM flag for the first block that indicates a presence of the BDPCM directional flag for the first block.

14. The video decoder according to claim 10, wherein the first block and the second block are included in a same picture, and the first block spatially neighbors the second block.

15. The video decoder according to claim 14, wherein the processing circuitry is further configured to:

derive for the second block, using a most probable mode (MPM) derivation process, a candidate list, the derivation the candidate list including the determination of whether the first block is predicted according to the BDPCM mode, wherein

the determination of the intra prediction mode value for the second block further includes using the derived candidate list.

16. The video decoder according to claim 15, wherein the candidate list includes

a first candidate intra prediction mode value ($Mode_1$) that corresponds to the intra prediction mode value of the first block, and

a second candidate intra prediction mode value ($Mode_2$) and a third candidate intra prediction mode value ($Mode_3$) that are determined in accordance with a predetermined offset from the first candidate intra prediction mode value and a modulo M operation, in which M is a power of 2.

17. The video decoder according to claim 10, the wherein the second block is a chroma block and the first block is a luma block co-located with the chroma block.

18. The video decoder according to claim 17, wherein the processing circuitry is further configured to:

determine whether the second block is coded with a direct copy mode (DM), and

in response to a determination that the second block is coded with the direct copy mode, determine whether the first block is predicted according to the BDPCM mode.

19. A non-transitory computer readable medium having instructions stored therein, which when executed by a processor causes the processor to perform the method of video decoding according to any one of claims 1-9.