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CONVERGENCE CONTROL SYSTEM

ABSTRACT

A convergence control system for a television apparatus, for example a projection television, comprises: a plurality of channels (40), each channel having a digital memory (46) for storing a set of grid coordinate values defining a convergence correction grid; and, a pair of multiplying digital to analog converters (56, 58) for the grid coordinate values. A single circuit (44) for generating sequential digital addresses is coupled to each of the channels (40) for supplying the grid coordinate values from each of the digital memories (46) to respective multiplying digital to analog converters (56, 58) of the channels. The values of respective horizontal grid lines are supplied to only one of the multiplying digital to analog converters. A single waveform generator (70) develops a pair of analog waveforms (Veven, Vodd) of complementary phase for modulating respective multiplying digital to analog converters of each pair (56, 58) in each channel. The sum of the modulating waveforms (Veven, Vodd) is constant. The succession of modulated analog values define the convergence correction signal for each channel.

Figure 3