

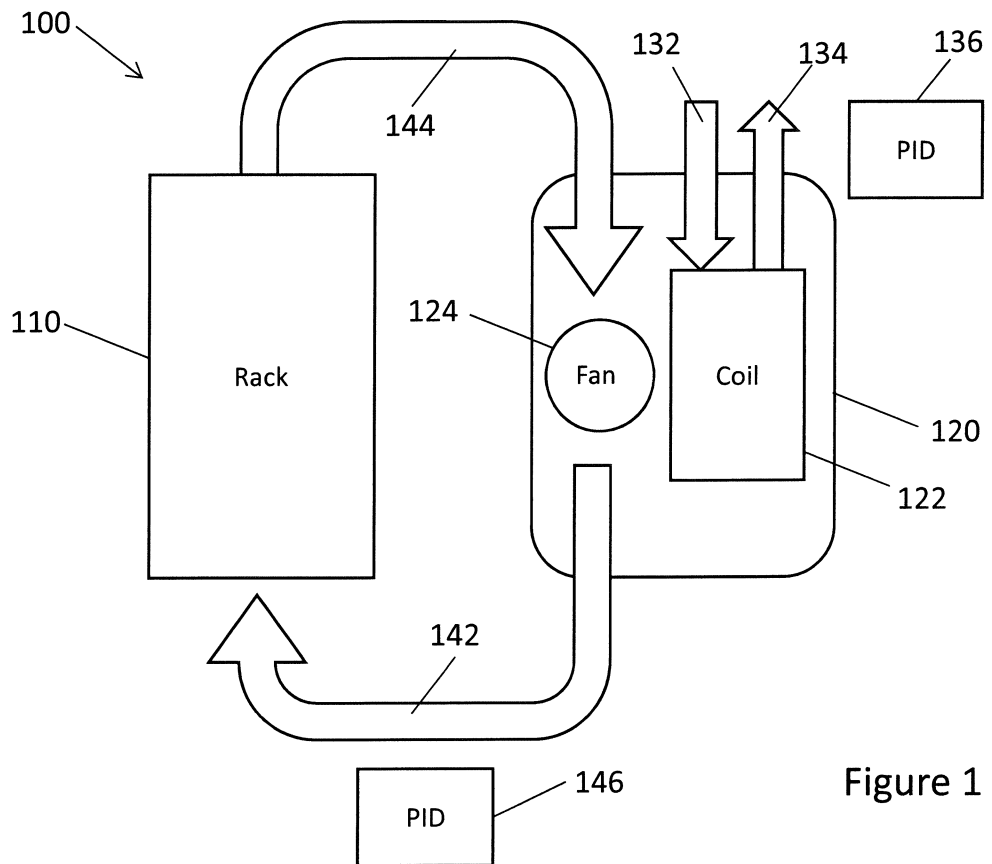


Figure 1

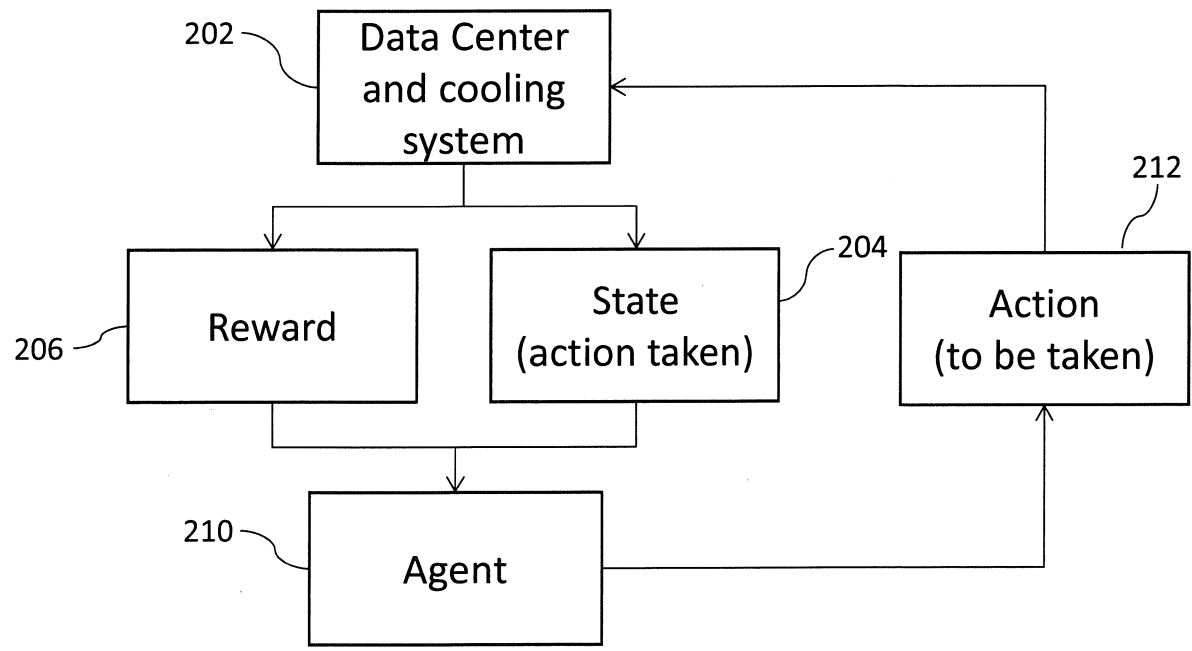


Figure 2

General DRL

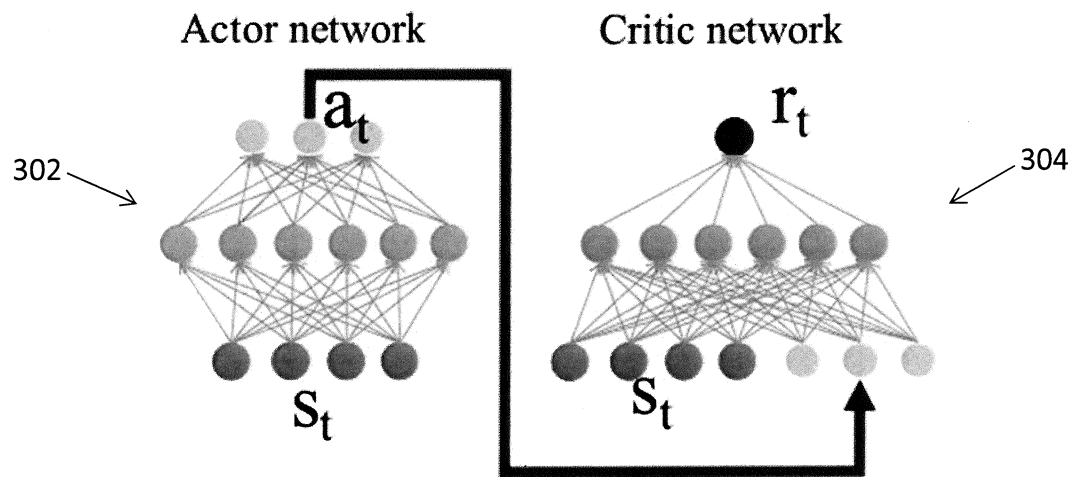


Figure 3a

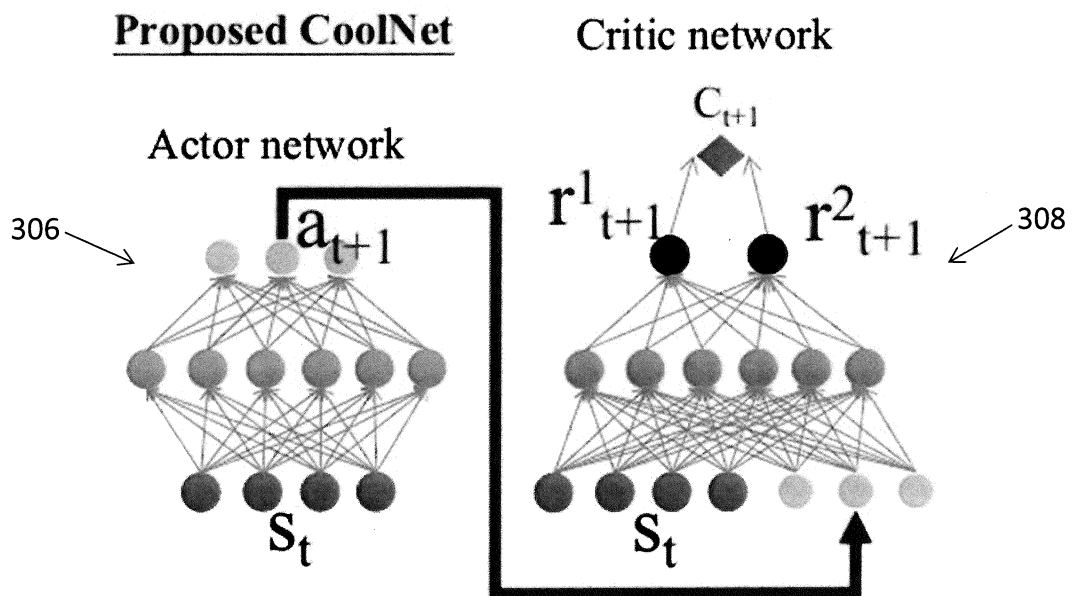
Proposed CoolNet

Figure 3b

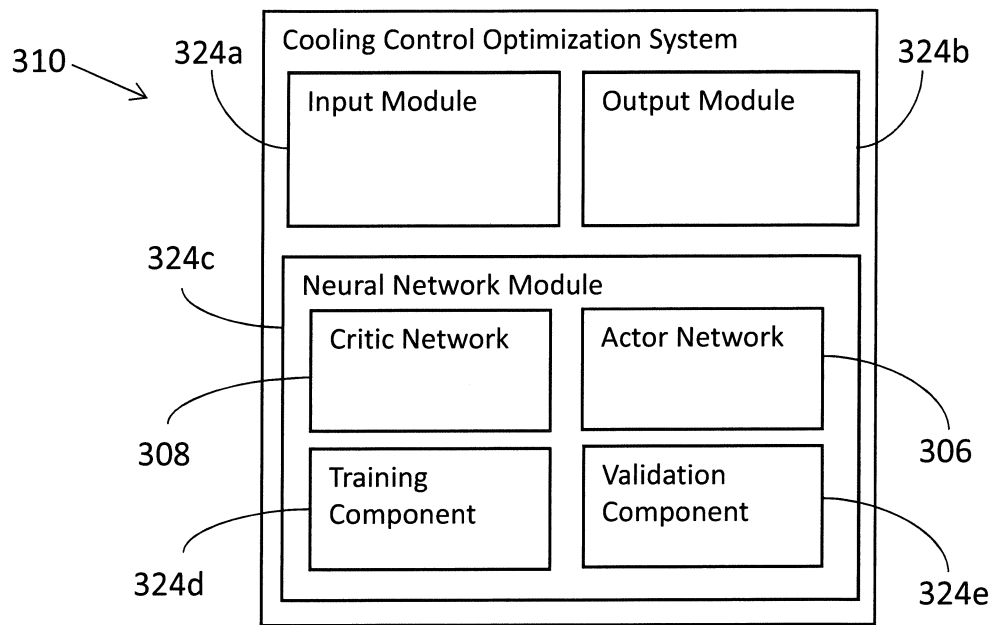


Figure 4

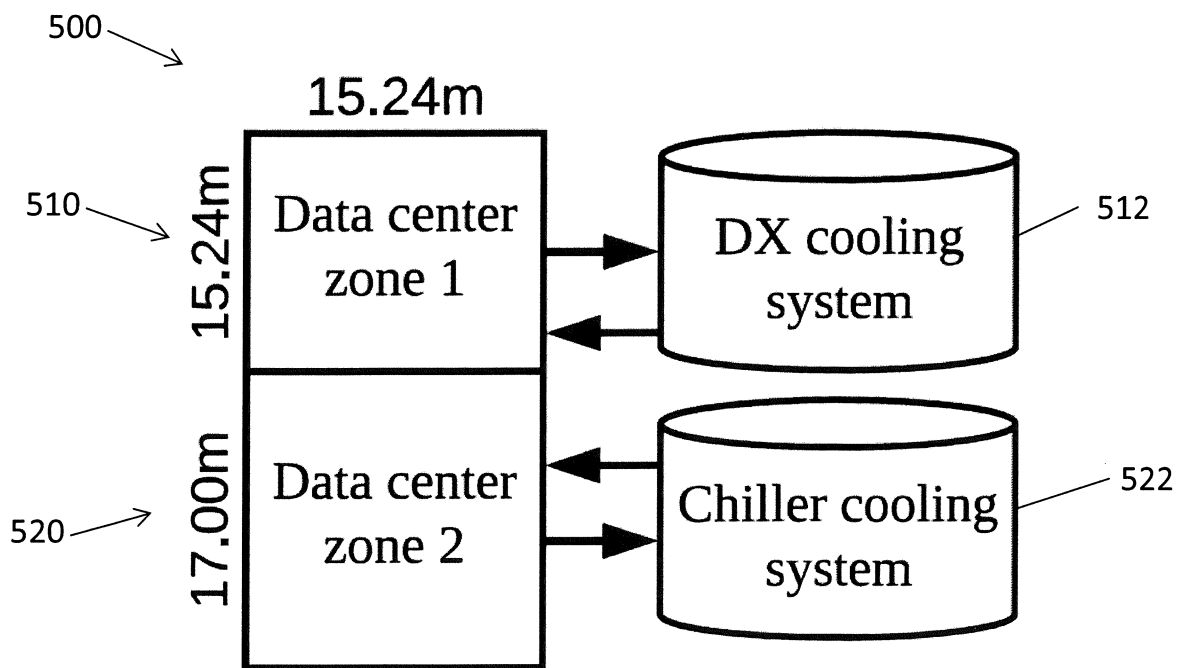


Figure 5

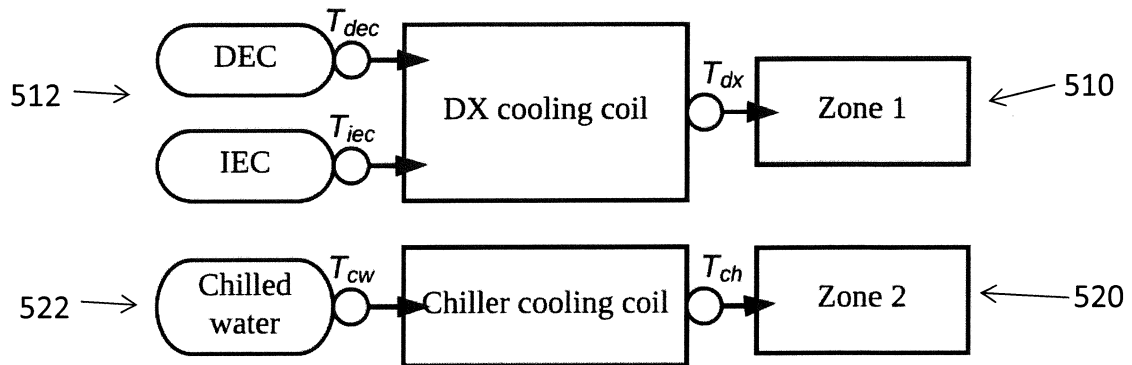


Figure 6

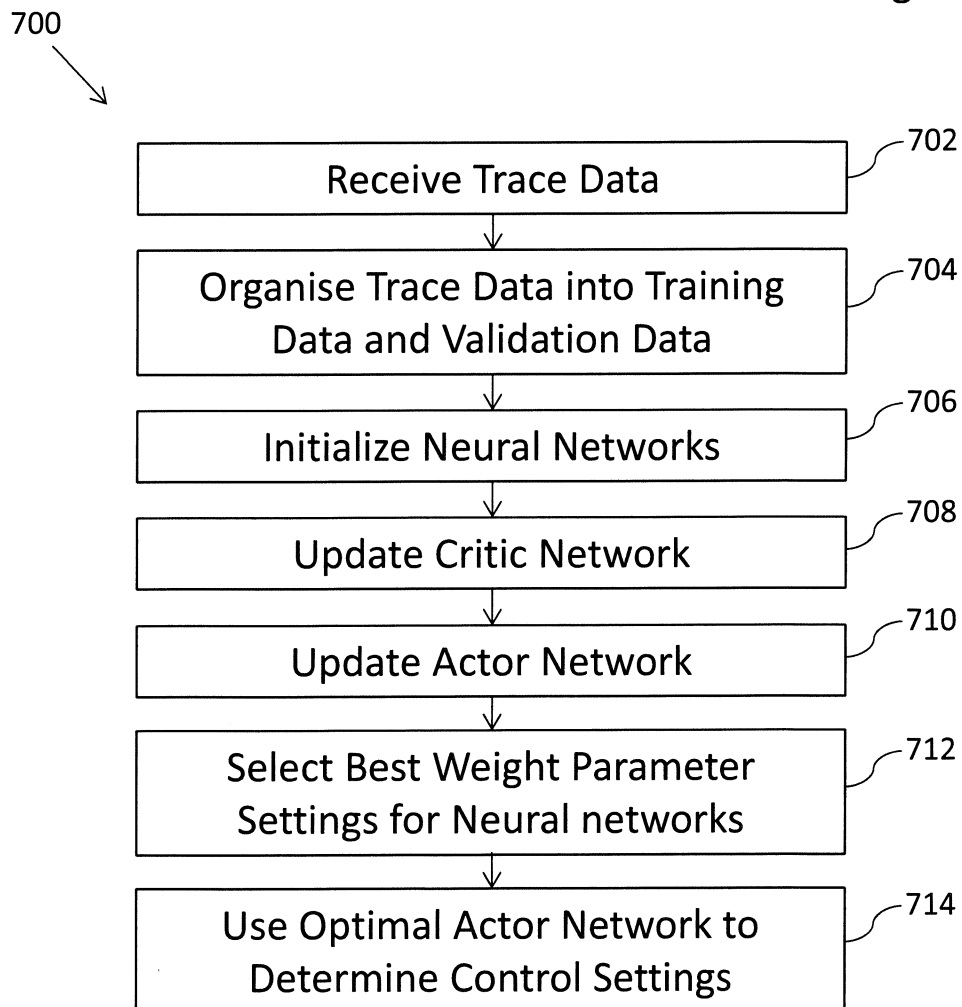


Figure 7

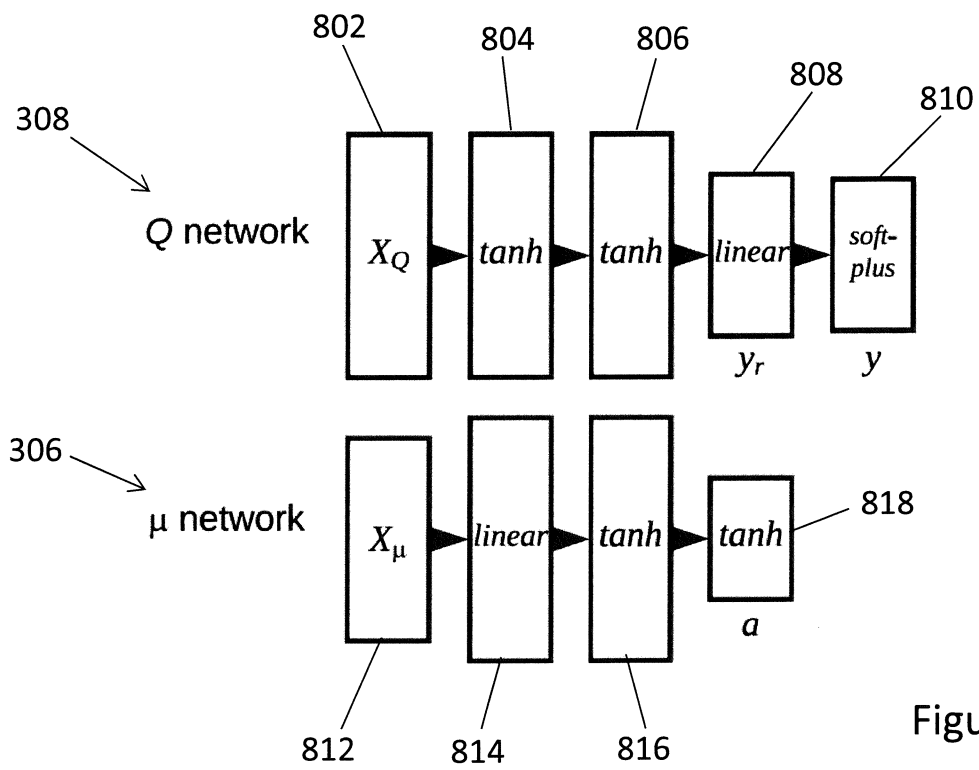


Figure 8

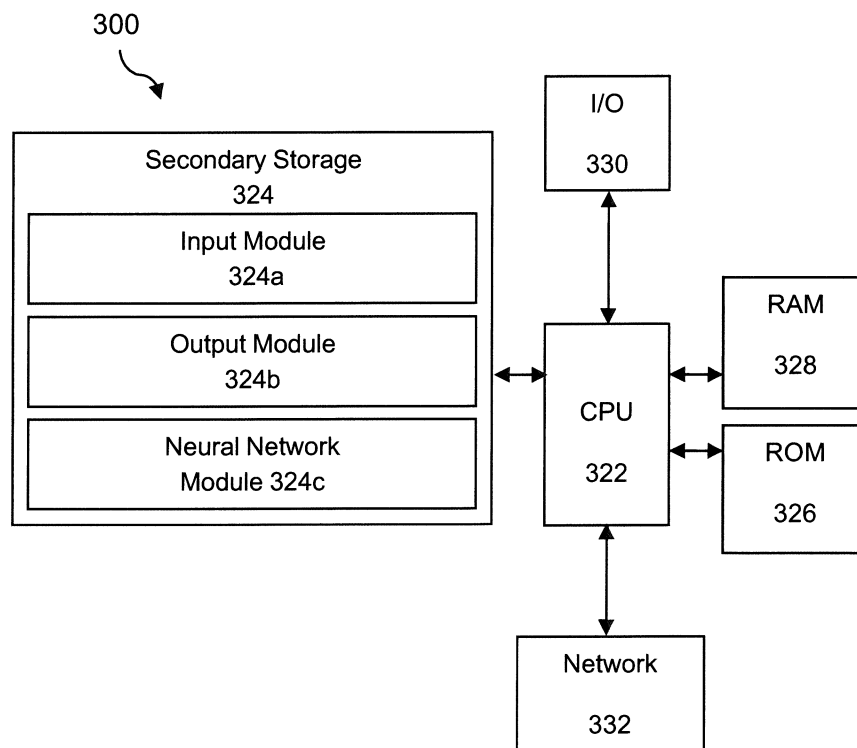


Figure 9

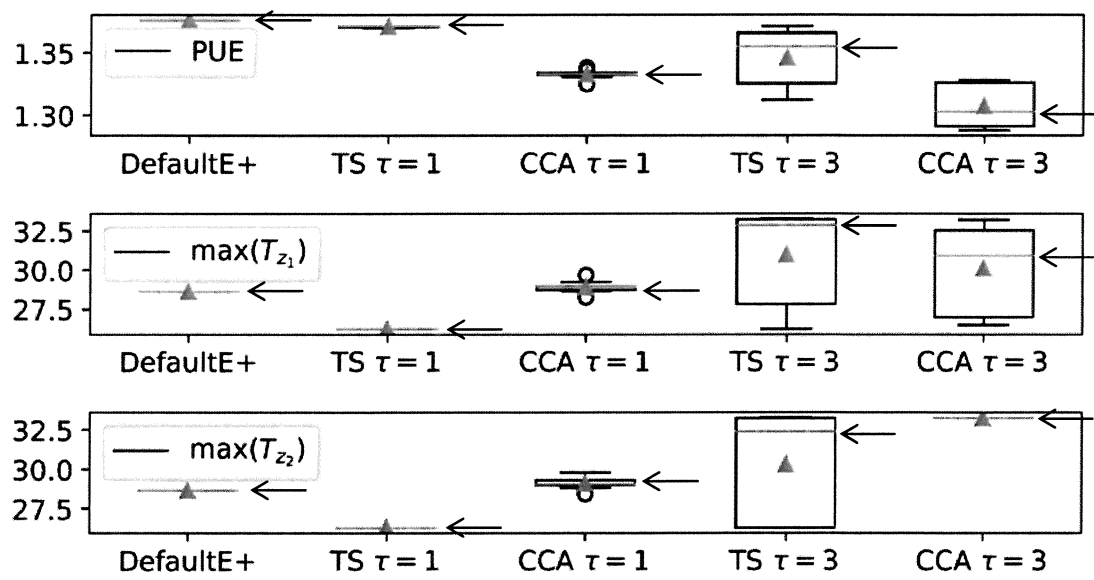
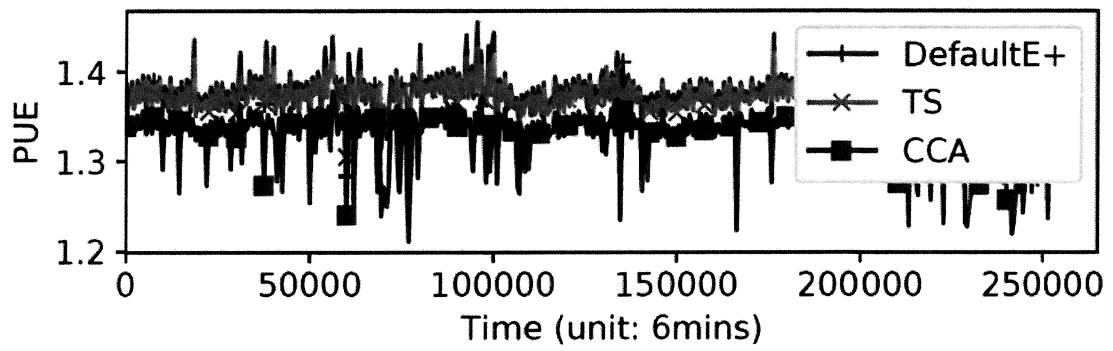
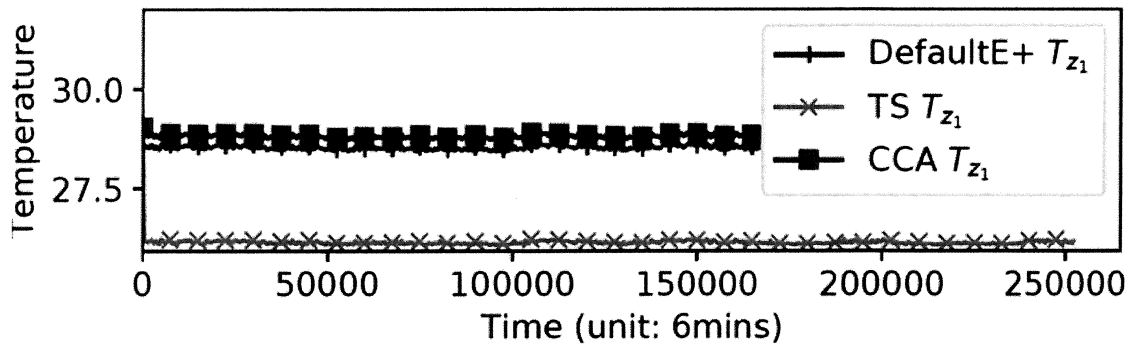


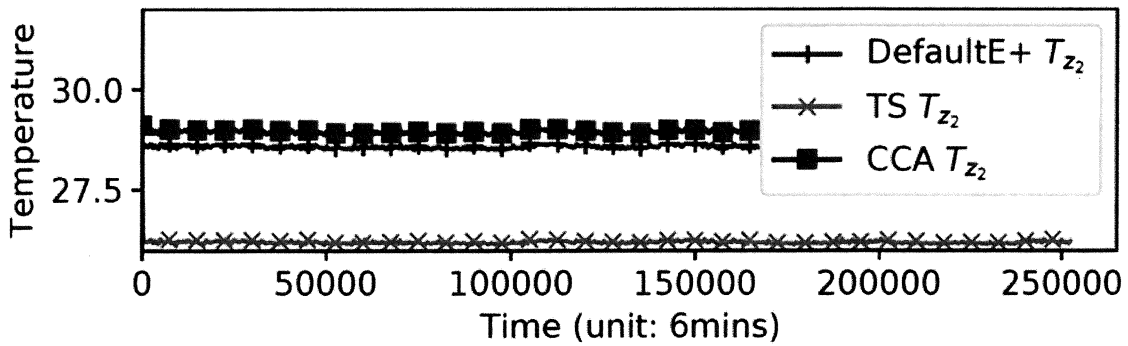
Figure 10



(a)



(b)



(c)

Figure 11

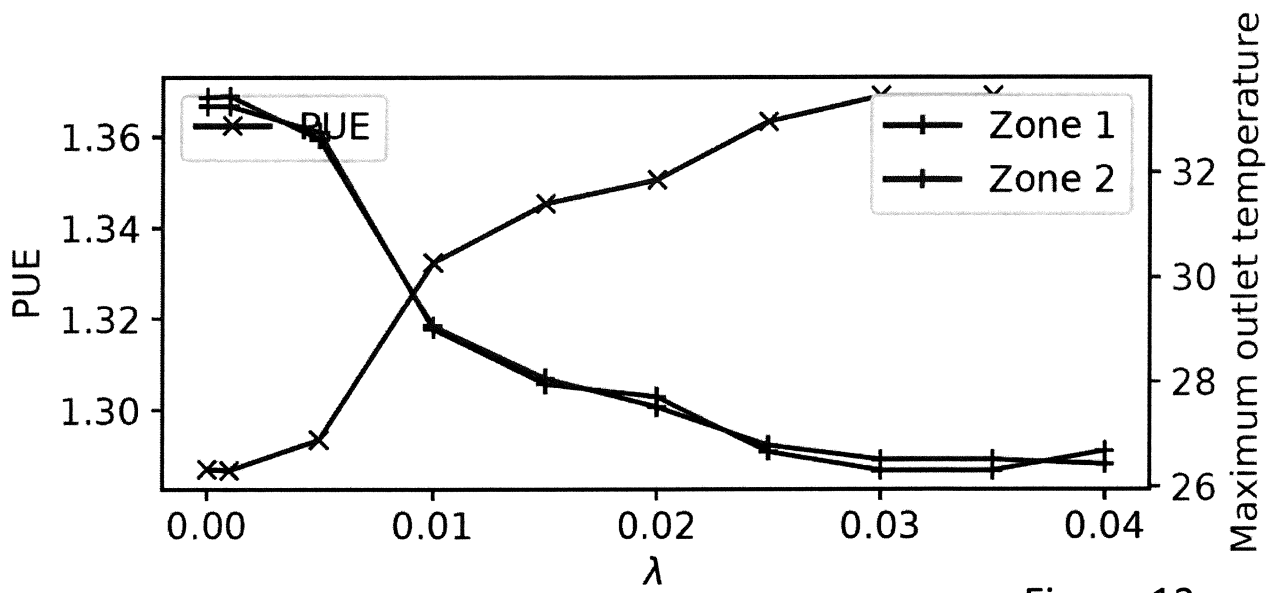


Figure 12

1300

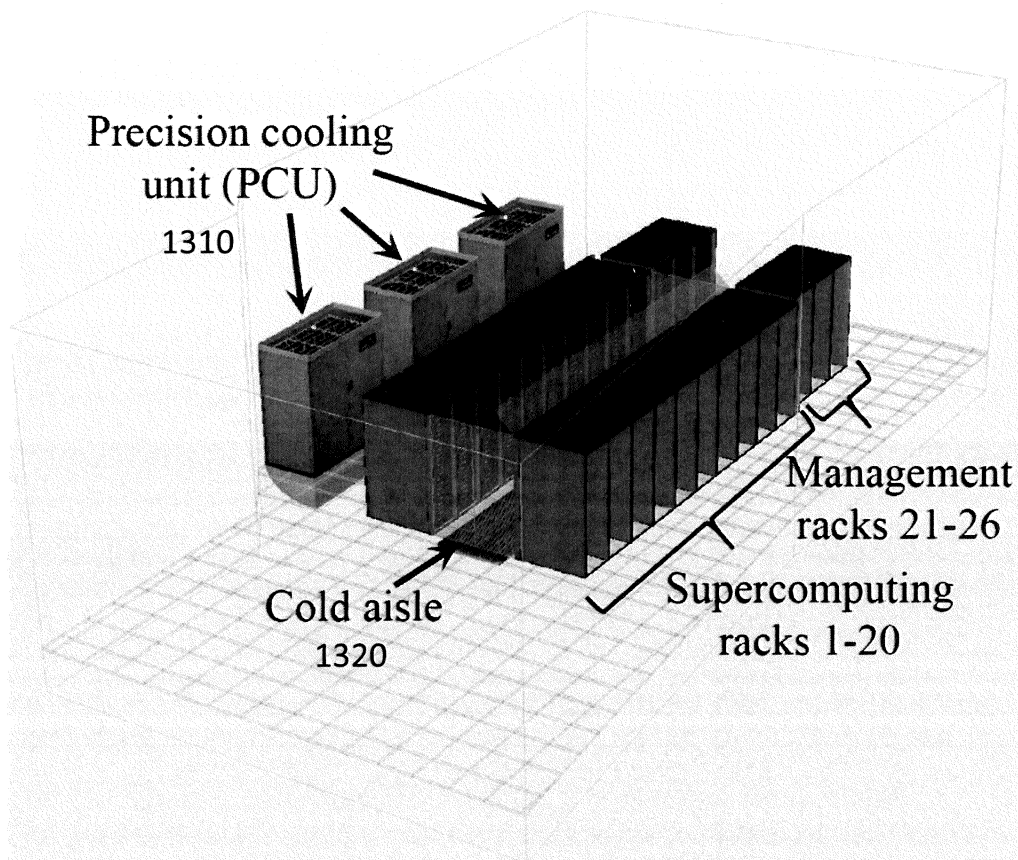


Figure 13

10/12

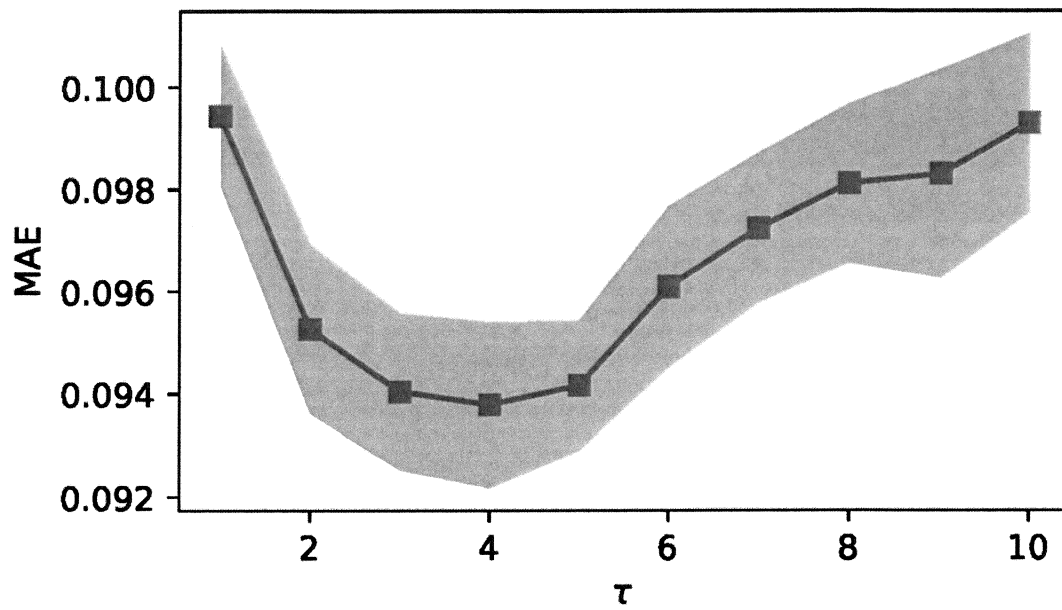


Figure 14

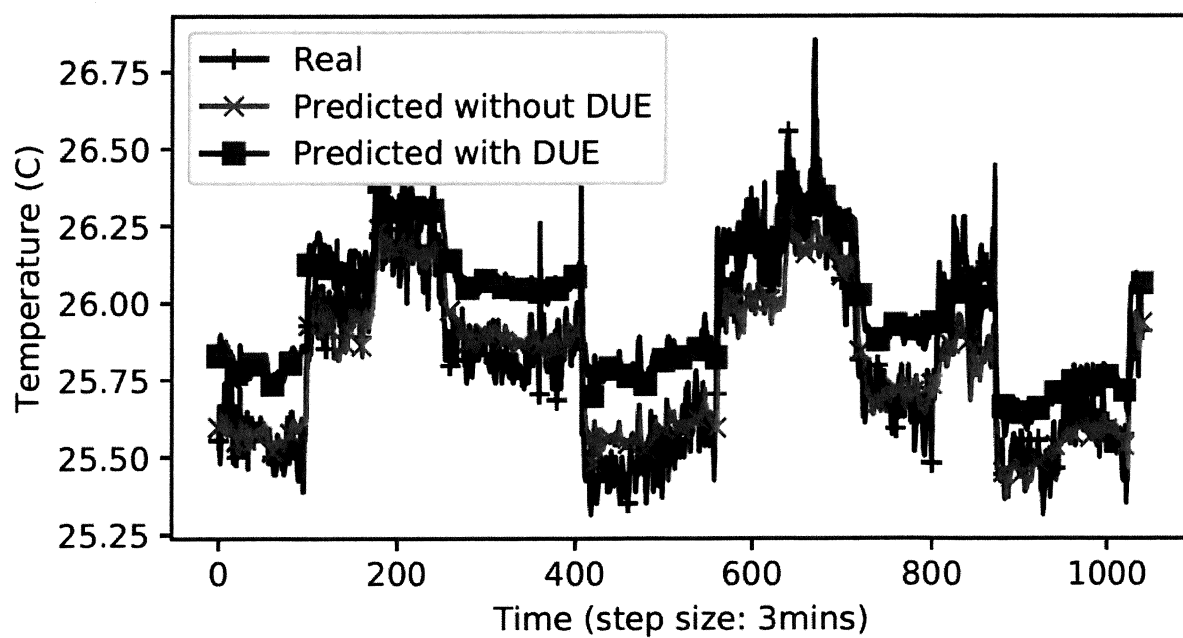


Figure 15

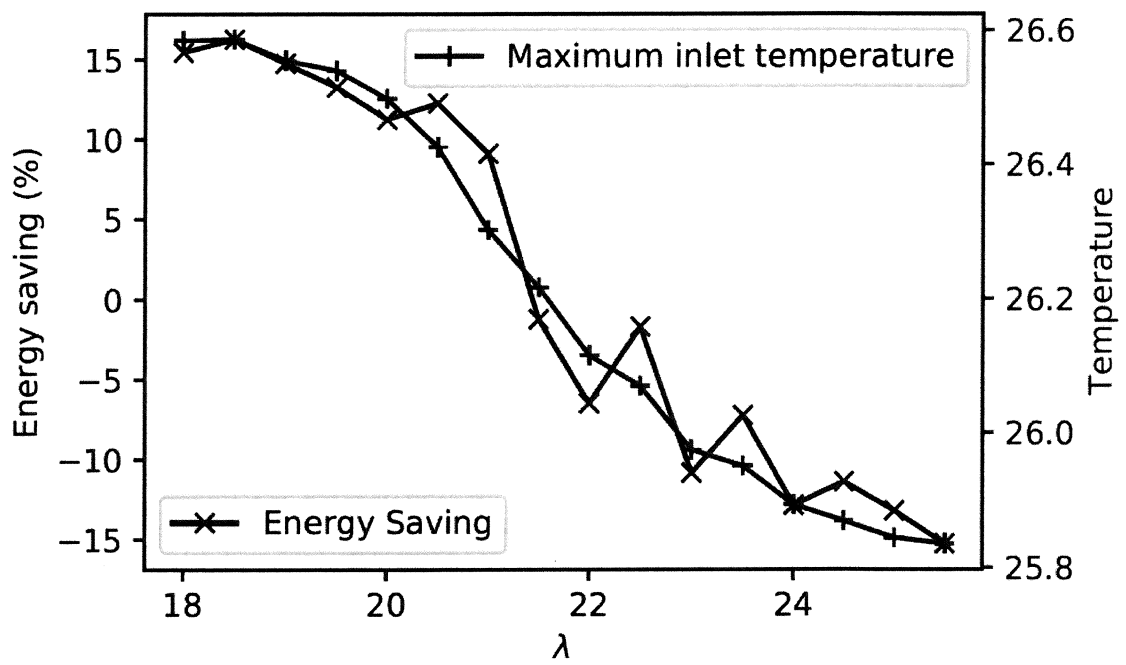
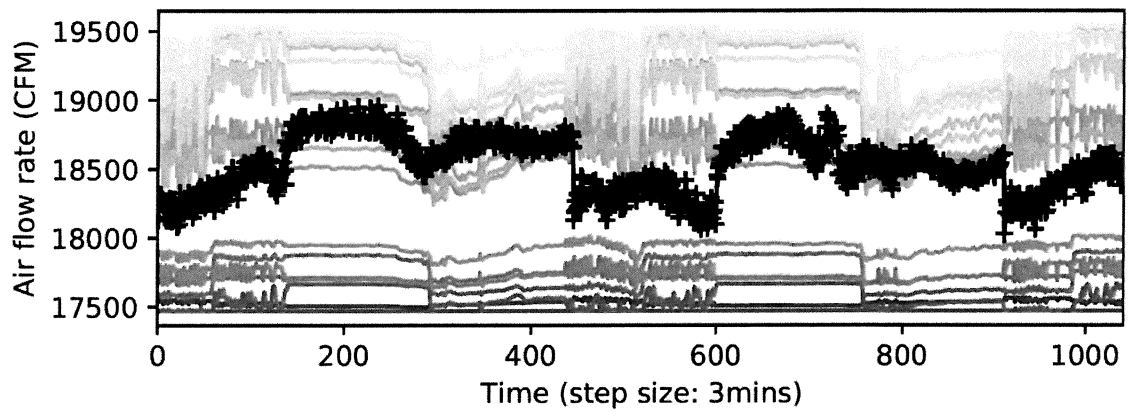
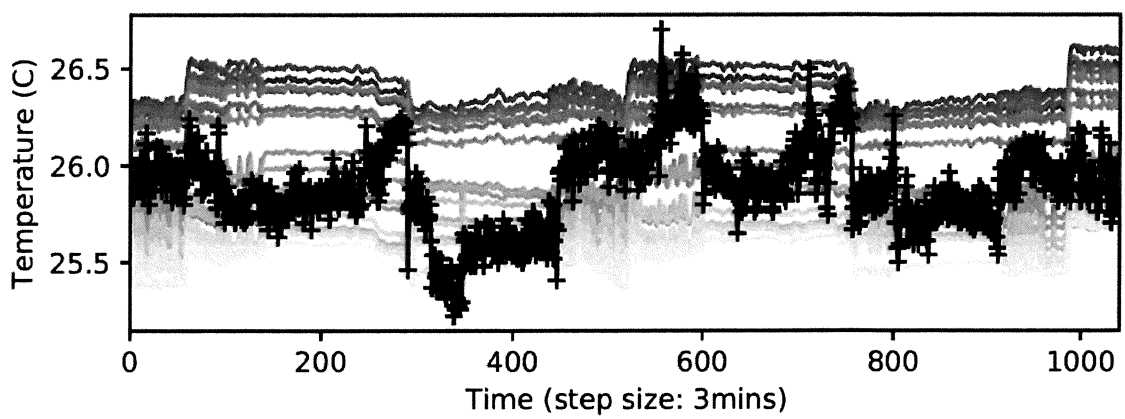


Figure 16



(a)



(b)

Figure 17