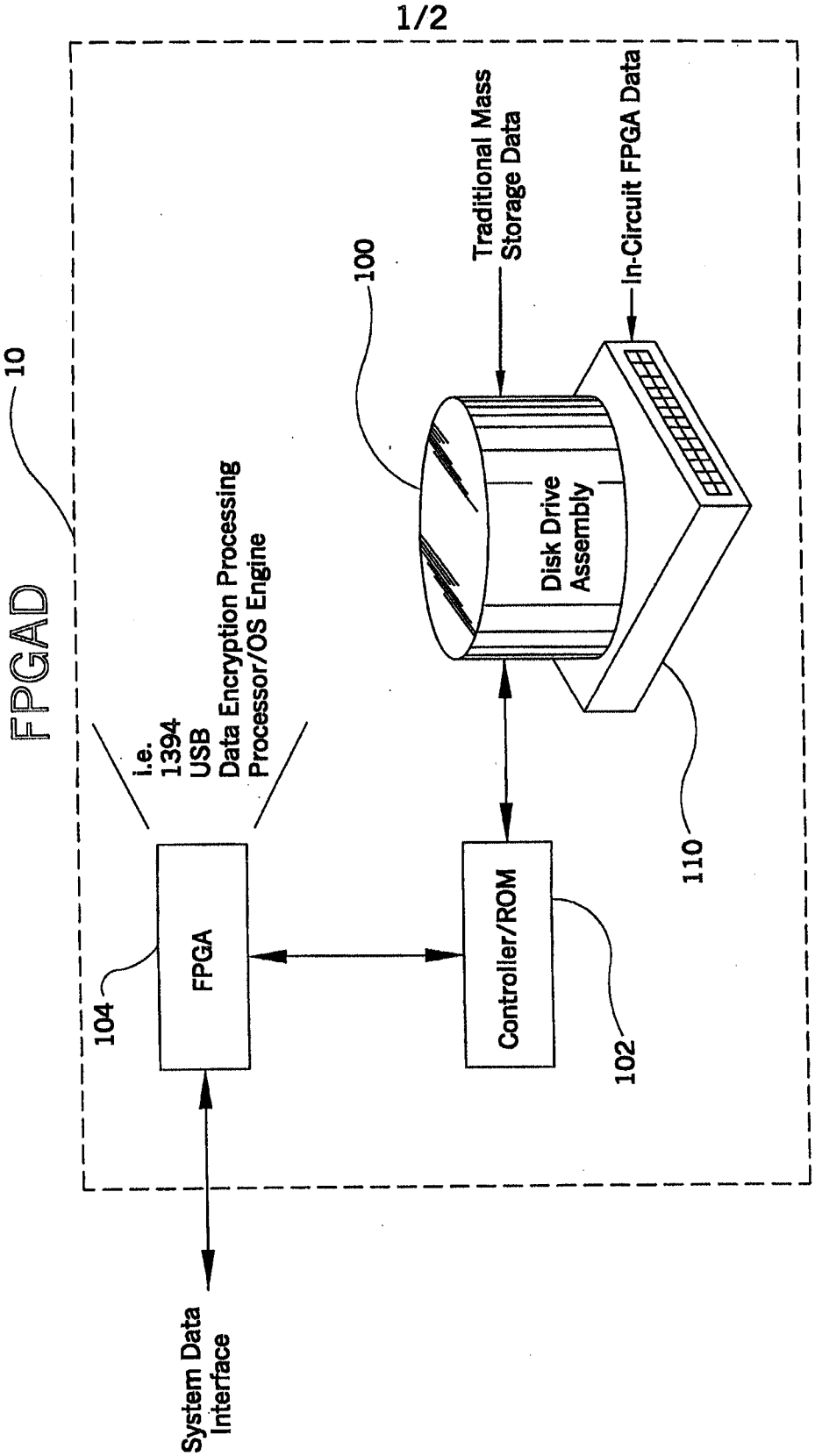

10
FPGAD
i.e.
1394
USB
Data Encryption Processing
Processor/OS Engine
100
Traditional Mass
Storage Data
In-Circuit FPGA Data
Disk Drive
Assembly
110
104
FPGA
Controller/ROM
102
System Data
Interface

Fig. 1

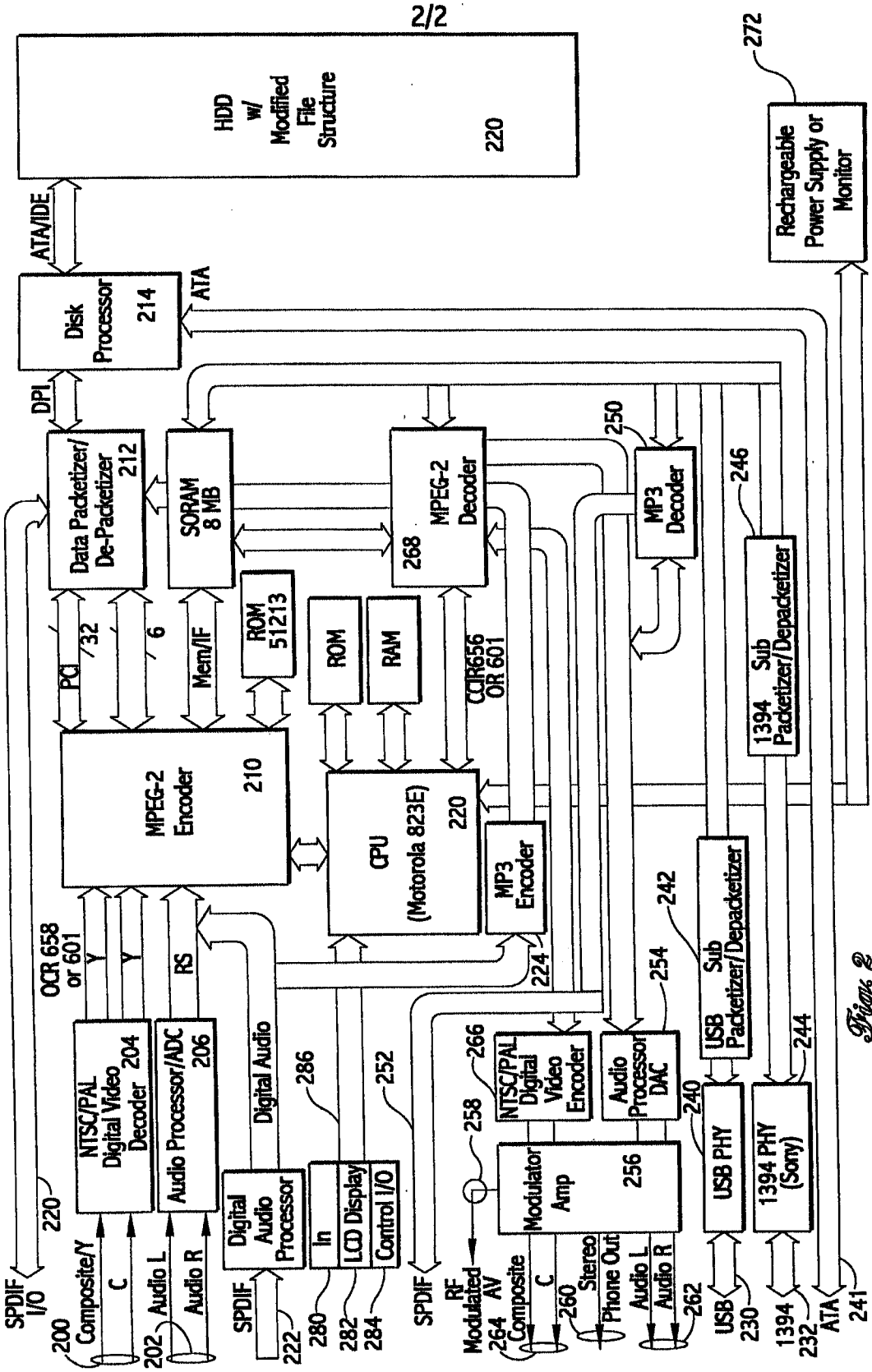

HDD w/ Modified File Structure
220
ATA/IDE
Disk Processor
214
ATA
DPI
Data Packetizer/ De-Packetizer
212
SDRAM 8 MB
MPEG-2 Decoder
268
MP3 Decoder
250
246
Rechargeable Power Supply or Monitor
272
PCI
32
6
Mem/IF
ROM 51213
ROM
RAM
CCIR656 OR 601
1394 Sub Packetizer/Depacketizer
MPEG-2 Encoder
210
CPU (Motorola 823E)
220
MP3 Encoder
OCR 658 or 601
Y
Y
RS
224
242
254
Sub Packetizer/Depacketizer
USB
NTSC/PAL Digital Video Decoder
204
Audio Processor/ADC
206
Digital Audio
286
252
NTSC/PAL Digital Video Encoder
266
Audio Processor DAC
240
258
Modulator Amp
256
USB PHY
1394 PHY (Sony)
244
SPDIF I/O
220
Composite/Y
C
Audio L
Audio R
200
202
Digital Audio Processor
In
LCD Display
Control I/O
SPDIF
222
280
282
284
SPDIF
RF Modulated AV
264
Composite
C
260
Stereo Phone Out
Audio L
Audio R
262
USB
230
1394
232
ATA
241

Fig. 2