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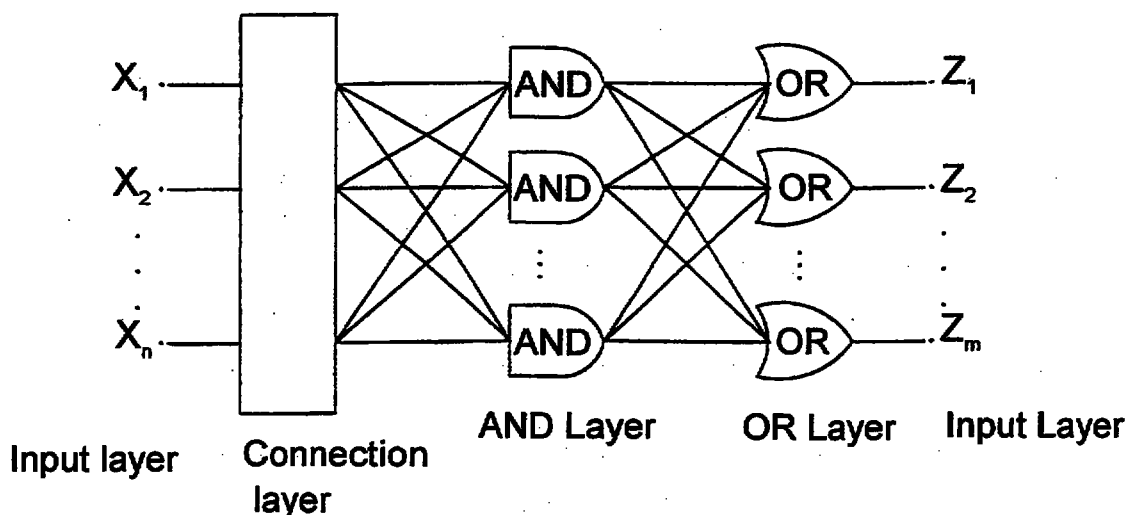
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- (71) Applicant: SOWA INSTITUTE OF TECHNOLOGY CO., LTD. [JP/JP]; 3-9, Higashi Ooyodo 2-chome, Miyazaki-shi, Miyazaki-ken 880-0901 (JP). Published:
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- (72) Inventor: ZHENG, Tang; Sowa Institute of Technology Co., Ltd., 3-9, Higashi Ooyodo 2-chome, Miyazaki-shi, Miyazaki-ken 880-0901 (JP). For two-letter codes and other abbreviations, refer to the "Guidance Notes on Codes and Abbreviations" appearing at the beginning of each regular issue of the PCT Gazette.

(54) Title: LEARNING METHODS IN BINARY SYSTEMS

AND - OR network



(57) Abstract: This invention provides learning methods in binary systems by modifying the connected states of the circuit among each basic binary gate in binary combined logical and sequential circuits composed with basic binary gates such as AND, OR, NOT, NAND, NOR and EXOR gates. As the pseudo-neuron theory and the pseudo-potential energy theory are skillfully introduced, it is possible to attain specified learning effects during a very short learning period. Further, as implementation of the learning methods into the conventional computer and other digital equipment is simple, it is expected to be used widely in wide application, for example, such as in image processing, voice processing or natural word processing.

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