

## ABSTRACT OF THE DISCLOSURE

The pedal misapplication suppression accelerator includes a micro-controller (13) configured with a triggering condition including a number of intervals and, for each interval, a corresponding duration and a  
5 corresponding threshold. Each interval is a range specifying how much the vehicle's acceleration pedal has changed its position in terms of percentages of a pedal stroke. Each duration specifies the fastest time duration allowable for the acceleration pedal to attain a corresponding interval of pedal position change. The micro-controller (13) converts progress signals of the acceleration  
10 pedal to corresponding percentages, obtains a difference DEF between the successive percentages, records a time duration RES between successive progress signals, and calculates  $DEF/RES=X$ . When X is greater than or equal to a threshold of a corresponding interval, the micro-controller (13) sends an idle signal to the vehicle's engine control unit or intercepts the progress signals  
15 to prevent them from reaching the engine control unit.